

# PHOTOCOUPLER PS2706-1

# AC INPUT RESPONSE DARLINGTON TRANSISTOR SOP MULTI PHOTOCOUPLER SERIES

-NEPOC Series-

#### **DESCRIPTION**

The PS2706-1 is an optically coupled isolator containing a GaAs light emitting diode and an NPN silicon darlington-connected phototransistor.

This is mounted in a plastic SOP (Small Out-line Package) for high density applications.

This package has shield effect to cut off ambient light.

#### **FEATURES**

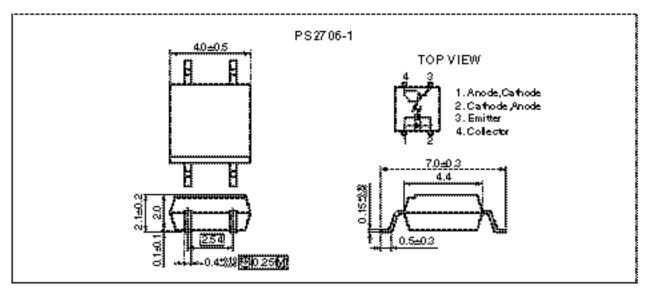
- · AC input response
- High current transfer ratio (CTR = 2 000% TYP.)
- High isolation voltage (BV = 3 750 Vr.m.s.)
- · Small and thin (SOP) package
- High-speed switching (tr, tf = 200  $\mu$ s TYP.)
- · Ordering number of taping product: PS2706-1F3, F4
- ★ Safety standards
  - UL approved: File No. E72422
  - BSI approved: File No. 8219/8220
  - CSA approved: File No. CA 101391
  - DIN EN60747-5-2 (VDE0884 Part2) approved (Option)

## **APPLICATIONS**

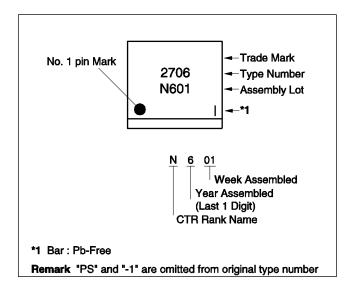
- · Hybrid IC
- · Telephone, Exchange equipment
- · FA/OA equipment
- · Programmable logic controllers

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## **PACKAGE DIMENSIONS (in millimeters)**



## **★** MARKING EXAMPLES



# **★** ORDERING INFORMATION

| Part Number   | Order Number    | Solder Plating<br>Specification | Packing Style                | Safety Standard<br>Approval | Application Part<br>Number*1 |
|---------------|-----------------|---------------------------------|------------------------------|-----------------------------|------------------------------|
| PS2706-1      | PS2706-1-A      | Pb-Free                         | Magazine case 100 pcs        | Standard products           | PS2706-1                     |
| PS2706-1-F3   | PS2706-1-F3-A   |                                 | Embossed Tape 3 500 pcs/reel | (UL, BSI, CSA               |                              |
| PS2706-1-F4   | PS2706-1-F4-A   |                                 |                              | approved)                   |                              |
| PS2706-1-V    | PS2706-1-V-A    |                                 | Magazine case 100 pcs        | DIN EN60747-5-2             |                              |
| PS2706-1-V-F3 | PS2706-1-V-F3-A |                                 | Embossed Tape 3 500 pcs/reel | (VDE0884 Part2)             |                              |
| PS2706-1-V-F4 | PS2706-1-V-F4-A |                                 |                              | Approved (Option)           |                              |

<sup>\*1</sup> For the application of the Safety Standard, following part number should be used.

# ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

|                               | Parameter                    | Symbol           | Ratings     | Unit    |
|-------------------------------|------------------------------|------------------|-------------|---------|
| Diode                         | Forward Current (DC)         | lF               | ±50         | mA      |
|                               | Power Dissipation Derating   | ⊿P₀/°C           | 0.8         | mW/°C   |
|                               | Power Dissipation            | PD               | 80          | mW      |
|                               | Peak Forward Current*1       | IFP              | ±1          | Α       |
| Transistor                    | Collector to Emitter Voltage | Vceo             | 40          | V       |
|                               | Emitter to Collector Voltage | VECO             | 6           | V       |
|                               | Collector Current            | lc               | 200         | mA      |
|                               | Power Dissipation Derating   | ⊿Pc/°C           | 1.5         | mW/°C   |
|                               | Power Dissipation            | Pc               | 150         | mW      |
| Isolation Voltage*2           |                              | BV               | 3 750       | Vr.m.s. |
| Operating Ambient Temperature |                              | TA               | -55 to +100 | °C      |
| Storage Temperature           |                              | T <sub>stg</sub> | –55 to +150 | °C      |

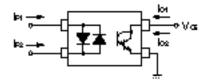
<sup>\*1</sup> PW = 100 \( \mu \s, \text{ Duty Cycle} = 1\%

<sup>\*2</sup> AC voltage for 1 minute at  $T_A = 25$ °C, RH = 60% between input and output Pins 1-2 shorted together, 3-4 shorted together.

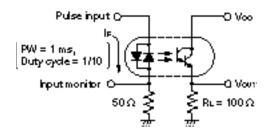
# ELECTRICAL CHARACTERISTICS (TA = 25 °C)

|            | Parameter                                   | Symbol           | Conditions  | MIN.             | TYP.  | MAX. | Unit |
|------------|---|------------------|---|------------------|-------|------|------|
| Diode      | Forward Voltage                             | VF               | $I_F = \pm 5 \text{ mA}$  |                  | 1.1   | 1.4  | V    |
|            | Terminal Capacitance                        | Ct               | V = 0 V, f = 1 MHz  |                  | 60    |      | pF   |
| Transistor | Collector to Emitter Dark<br>Current        | Iceo             | IF = 0 mA, VcE = 40 V   |                  |       | 400  | nA   |
| Coupled    | Current Transfer Ratio (Ic/I <sub>F</sub> ) | CTR              | $I_F = \pm 1$ mA, $V_{CE} = 2$ V  | 200              | 2 000 |      | %    |
|            | CTR Ratio*1                                 | CTR1/<br>CTR2    | $I_F = \pm 1$ mA, $V_{CE} = 2$ V  | 0.3              | 1.0   | 3.0  |      |
|            | Collector Saturation<br>Voltage             | VCE (sat)        | $I_F = \pm 1 \text{ mA}, I_C = 2 \text{ mA}$                            |                  |       | 1.0  | V    |
|            | Isolation Resistance                        | R <sub>I-O</sub> | Vi-o = 1 kVbc   | 10 <sup>11</sup> |       |      | Ω    |
|            | Isolation Capacitance                       | C <sub>I-O</sub> | V = 0 V, f = 1 MHz  |                  | 0.4   |      | pF   |
|            | Rise Time *2                                | tr               | $Vcc = 5 \text{ V}, \text{ Ic} = 2 \text{ mA}, \text{ RL} = 100 \Omega$ |                  | 200   |      | μS   |
|            | Fall Time *2                                | tf               |   |                  | 200   |      |      |

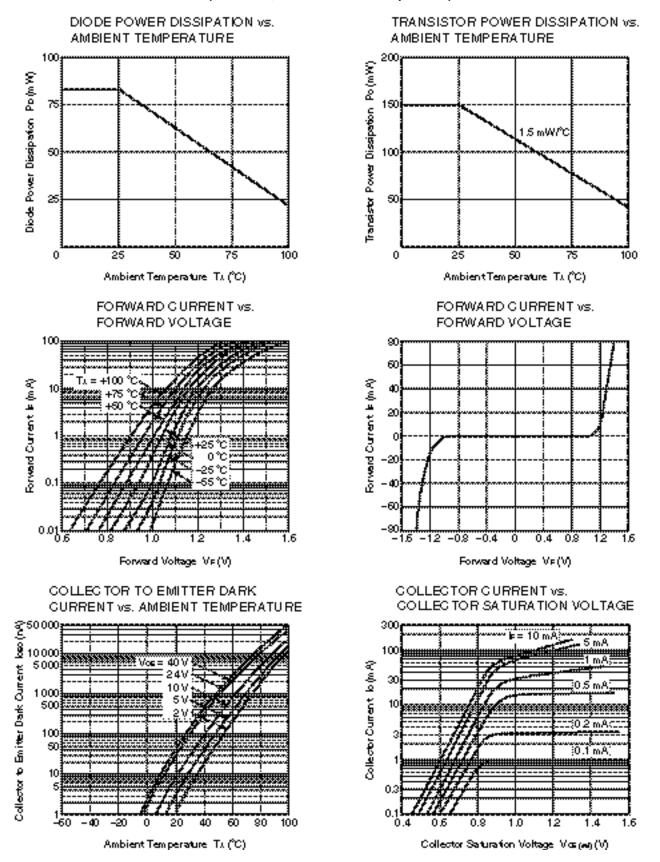
# \*1 CTR1 = Ic1/IF1, CTR2 = Ic2/IF2



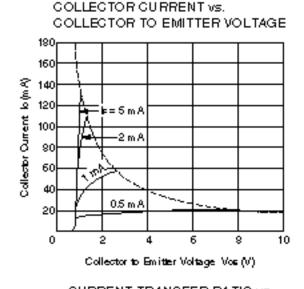
## \*2 Test circuit for switching time

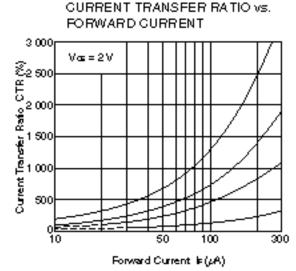


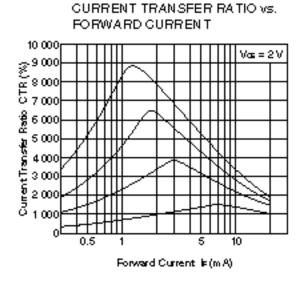
#### TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)



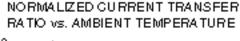
Remark The graphs indicate nominal characteristics.

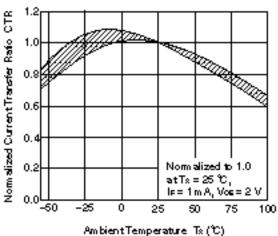




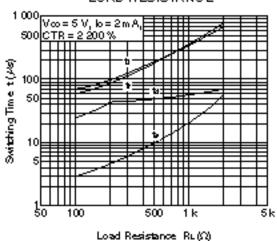




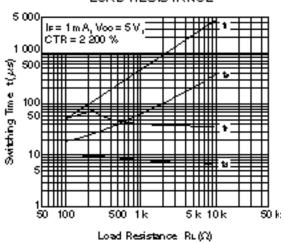


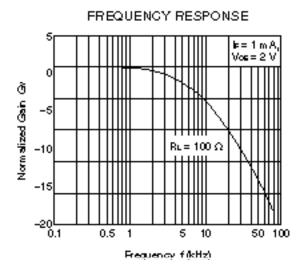


## SWITCHING TIME vs. LOAD RESISTANCE

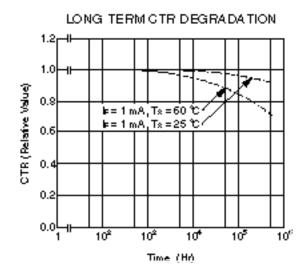


SWITCHING TIME vs. LOAD RESISTANCE

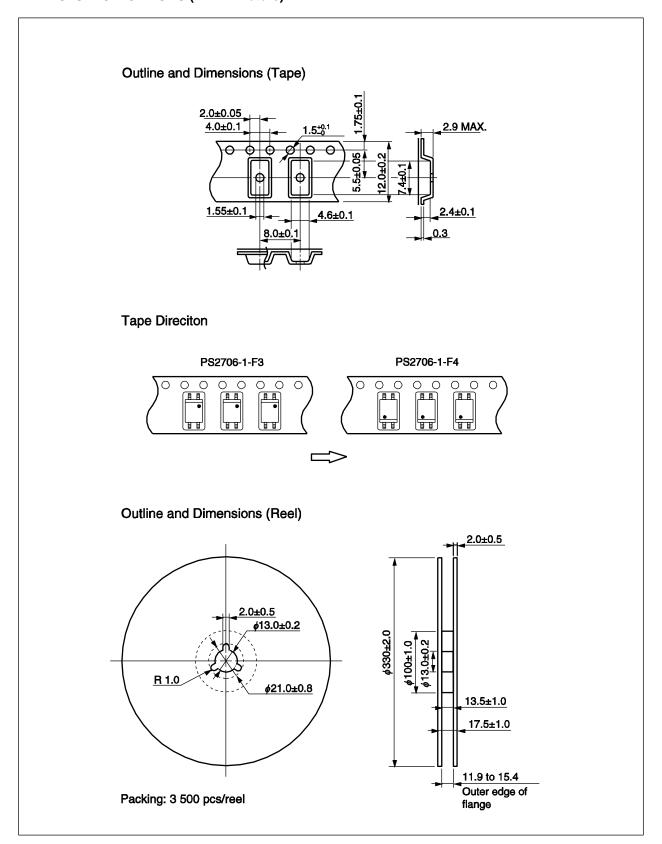




**Remark** The graphs indicate nominal characteristics.



# **TAPING SPECIFICATIONS (in millimeters)**



#### **NOTES ON HANDLING**

#### 1. Recommended soldering conditions

#### (1) Infrared reflow soldering

· Peak reflow temperature 260°C or below (package surface temperature)

· Time of peak reflow temperature 10 seconds or less 60 seconds or less • Time of temperature higher than 220°C

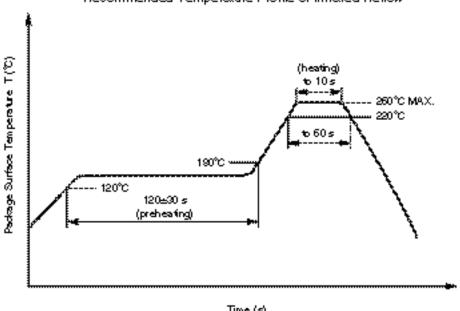
• Time to preheat temperature from 120 to 180°C 120±30 s

· Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

#### Recommended Temperature Profile of Infrared Reflow



## Time (s)

#### (2) Wave soldering

 Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

· Preheating conditions 120°C or below (package surface temperature)

· Number of times One (Allowed to be dipped in solder including plastic mold portion.)

• Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

#### (3) Soldering by soldering iron

• Peak temperature (lead part temperature) 350°C or below · Time (each pins) 3 seconds or less

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead.

(b) Please be sure that the temperature of the package would not be heated over 100°C.

#### (4) Cautions

Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

#### 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

#### ★ 3. Measurement conditions of current transfer ratios (CTR), which differ according to photocoupler

Check the setting values before use, since the forward current conditions at CTR measurement differ according to product.

When using products other than at the specified forward current, the characteristics curves may differ from the standard curves due to CTR value variations or the like. Therefore, check the characteristics under the actual operating conditions and thoroughly take variations or the like into consideration before use.

#### **USAGE CAUTIONS**

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

# SPECIFICATION OF VDE MARKS LICENSE DOCUMENT (VDE0884)

| Parameter   | Symbol               | Speck                                | Unit                                   |
|---|----------------------|--------------------------------------|--|
| Application classification (DIN VDE 0109) for rated line voltages ≤ 300 Vr.m.s. for rated line voltages ≤ 600 Vr.m.s.   |                      | IV<br>III                            |  |
| Climatic test class (DIN IEC 68 Teil 1/09.80)   |                      | 55/100/21                            |  |
| Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.2 \times U_{\text{IORM}},  P_{d} < 5 \; \text{pC}$ | UIORM<br>Upr         | 710<br>850                           | V <sub>peak</sub><br>V <sub>peak</sub> |
| Test voltage (partial discharge test, procedure b for all devices test) $U_{pr}=1.6\times U_{IORM},P_d<5\;pC$   | $U_pr$               | 1 140                                | V <sub>peak</sub>                      |
| Highest permissible overvoltage   | Utr                  | 6 000                                | V <sub>peak</sub>                      |
| Degree of pollution (DIN VDE 0109)  |                      | 2                                    |  |
| Clearance distance  |                      | > 5                                  | mm                                     |
| Creepage distance   |                      | > 5                                  | mm                                     |
| Comparative tracking index (DIN IEC 112/VDE 0303 part 1)  | СТІ                  | 175                                  |  |
| Material group (DIN VDE 0109)   |                      | III a                                |  |
| Storage temperature range   | T <sub>stg</sub>     | -55 to +150                          | °C                                     |
| Operating temperature range   | TA                   | -55 to +100                          | °C                                     |
| Isolation resistance, minimum value  VIO = 500 V dc at TA = 25 °C  VIO = 500 V dc at TA MAX. at least 100 °C  | Ris MIN.<br>Ris MIN. | 10 <sup>12</sup><br>10 <sup>11</sup> | Ω<br>Ω                                 |
| Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I <sub>F</sub> , Psi = 0)  | Tsi<br>Isi           | 150<br>200                           | °C<br>mA                               |
| Power (output or total power dissipation) Isolation resistance VIO = 500 V dc at TA = 175 °C (Tsi)  | Psi<br>Ris MIN.      | 300<br>10 <sup>9</sup>               | mW<br>Ω                                |

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M8E 00.4-0110

#### Caution

**GaAs Products** 

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
- Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

#### ▶For further information, please contact

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