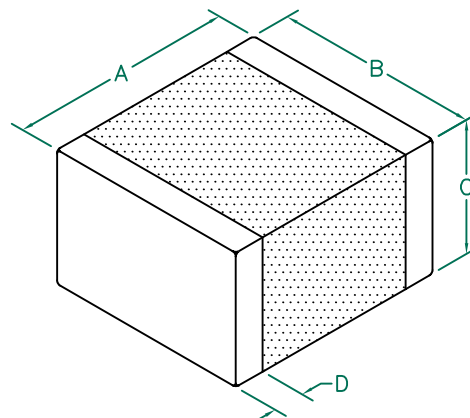


HI2220P601R-10

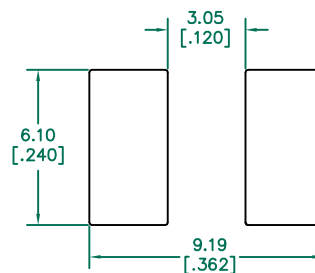
**UNCONTROLLED
DOCUMENT**

PHYSICAL DIMENSIONS:

A	5.59 [.220]	+ 0.51 [.020]
B	5.08 [.200]	+ 0.25 [.010]
C	3.05 [.120]	+ 0.25 [.010]
D	0.76 [.030]	+ 0.25 [.010]



LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 (0.30) to this dimension.)

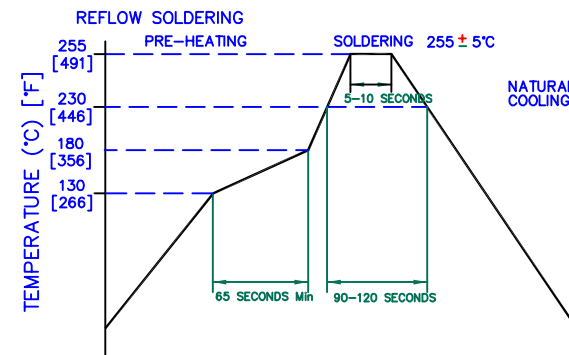
ELECTRICAL CHARACTERISTICS:

Z @ 100MHz (Ω)	DCR (Ω)	Rated Current
Nominal	600	
Minimum	450	
Maximum	750	0.025 4000 mA

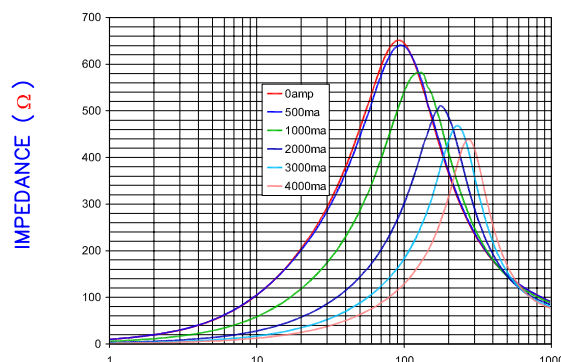
NOTES: UNLESS OTHERWISE SPECIFIED

1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 2,000 PCS/REEL.
2. U.S. PATENT 5,821,846 AND 6,107,907 SHOULD APPEAR ON THE LABEL OF EACH REEL OF PACKAGED PARTS.
3. TERMINATION FINISH IS 100% TIN.
4. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
5. OPERATEING TEMPERATURE TEMP: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$ (INCLUDING SELF-HEATING)

RECOMMENDED SOLDERING CONDITIONS

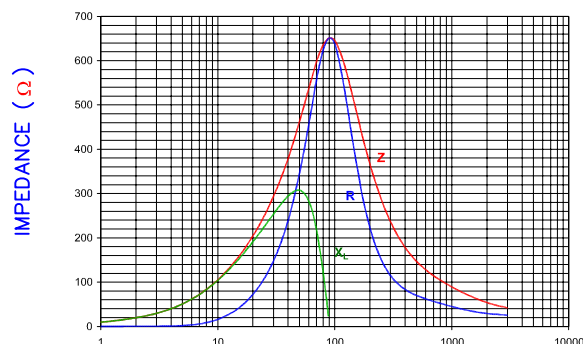


Z vs FREQUENCY IMPEDANCE UNDER DC BIAS



FREQUENCY (MHz)

|Z|, R, AND X vs. FREQUENCY



FREQUENCY (MHz)

Z

R

X_L



DIMENSIONS ARE IN mm [INCHES] .				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
E	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU	PROJECT/PART NUMBER: HI2220P601R-10			
D	UPDATE COMPANY LOGO ADD ROHS	01/24/08	JRK				
C	CHANGE C DIMENSION FROM .130	05/11/04	JRK	DATE: 01/12/01			
B	ADD DC BIAS CURVE, CHG DCR RATING	03/19/03	JRK				
A	ORIGINAL DRAFT	01/12/01	BAC	SCALE: NTS			
REV	DESCRIPTION	DATE	INT				
				TOOL # -			
				SHEET: 2 of 2			

Laird

REV E PART TYPE: CO-FIRE DRAWN BY: BAC