

**SiGe:C LOW NOISE AMPLIFIER FOR GPS****DESCRIPTION**

The  $\mu$ PC8236T6N is a silicon germanium carbon (SiGe:C) monolithic integrated circuit designed as low noise amplifier for GPS. This device exhibits low noise figure and high power gain characteristics, so this IC can improve the sensitivity of GPS receiver. In addition, the  $\mu$ PC8236T6N which is included output matching circuit contributes to reduce external components and system size.

The package is a 6-pin plastic TSON (Thin Small Out-line Non-leaded) (T6N) suitable for surface mount.

This IC is manufactured using our UHS4 (Ultra High Speed Process) SiGe:C bipolar process.

**FEATURES**

- Supply voltage :  $V_{CC} = 1.6$  to  $3.3$  V (2.7 V TYP.)
- Low noise :  $NF = 0.8$  dB TYP. @  $V_{CC} = 2.7$  V,  $f_{IN} = 1$  575 MHz
- High gain :  $G_P = 19.5$  dB TYP. @  $V_{CC} = 2.7$  V,  $f_{IN} = 1$  575 MHz
- High gain :  $G_P = 19.1$  dB TYP. @  $V_{CC} = 1.8$  V,  $f_{IN} = 1$  575 MHz
- Low current consumption :  $I_{CC} = 6.5$  mA TYP. @  $V_{CC} = 2.7$  V
- Built-in power-saving function :  $V_{PSon} = 1.0$  V to  $V_{CC}$ ,  $V_{PSoff} = 0$  to  $0.4$  V
- High-density surface mounting : 6-pin plastic TSON (T6N) package ( $1.5 \times 1.5 \times 0.37$  mm)
- Included output matching circuit
- Included very robust bandgap regulator (Small  $V_{CC}$  and  $T_A$  dependence)
- Included protection circuits for ESD

**APPLICATION**

- Low noise amplifier for GPS

**ORDERING INFORMATION**

Part Number	Order Number	Package	Marking	Supplying Form
$\mu$ PC8236T6N-E2	$\mu$ PC8236T6N-E2-A	6-pin plastic TSON (T6N) (Pb-Free)	6S	<ul style="list-style-type: none"><li>8 mm wide embossed taping</li><li>Pin 1, 6 face the perforation side of the tape</li><li>Qty 3 kpcs/reel</li></ul>

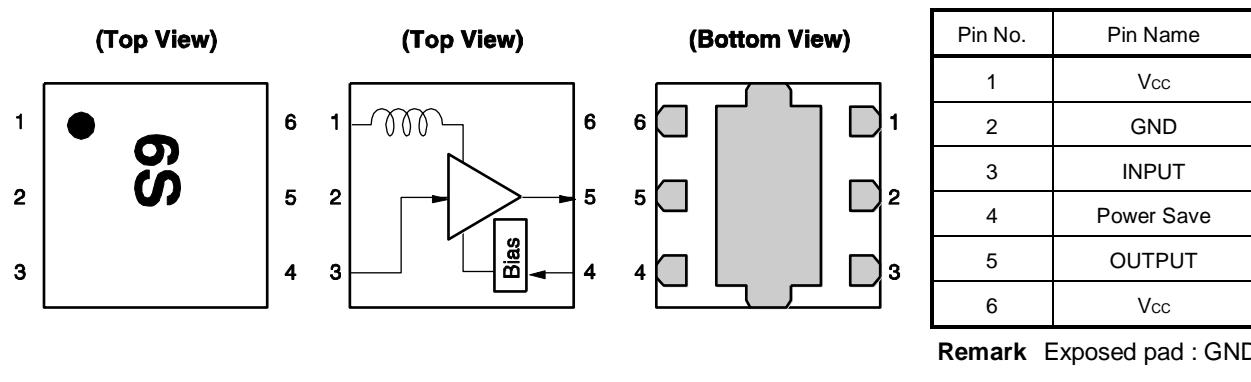
**Remark** To order evaluation samples, contact your nearby sales office.

Part number for sample order:  $\mu$ PC8236T6N-A

**Caution: Observe precautions when handling because these devices are sensitive to electrostatic discharge**

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

## PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



## ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Test Conditions			Ratings	Unit
Supply Voltage	V <sub>cc</sub>	T <sub>A</sub> = +25°C			4.0	V
Power-Saving Voltage	V <sub>ps</sub>	T <sub>A</sub> = +25°C			4.0	V
Total Power Dissipation	P <sub>tot</sub>				150	mW
Operating Ambient Temperature	T <sub>A</sub>				-40 to +85	°C
Storage Temperature	T <sub>stg</sub>				-55 to +150	°C
Input Power	P <sub>in</sub>				+10	dBm

## RECOMMENDED OPERATING RANGE

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V <sub>cc</sub>	1.6	2.7	3.3	V
Operating Ambient Temperature	T <sub>A</sub>	-40	+25	+85	°C
Power Save Turn-on Voltage	V <sub>pson</sub>	1.0	—	V <sub>cc</sub>	V
Power Save Turn-off Voltage	V <sub>psoff</sub>	0	—	0.4	V

## ELECTRICAL CHARACTERISTICS

(T<sub>A</sub> = +25°C, V<sub>cc</sub> = V<sub>ps</sub> = 2.7 V, f<sub>in</sub> = 1 575 MHz, unless otherwise specified)

Parameter	Symbol	Test Conditions		MIN.	TYP.	MAX.	Unit
Circuit Current	I <sub>cc</sub>	No Signal (V <sub>ps</sub> = 2.7 V)		5.0	6.5	8.0	mA
		At Power-Saving Mode (V <sub>ps</sub> = 0 V)		—	—	1	μA
Power Gain	G <sub>P</sub>	P <sub>in</sub> = -35 dBm		17	19.5	22	dB
Noise Figure	NF			—	0.8	1.1	dB
Input Return Loss	R <sub>Lin</sub>			7.5	11	—	dB
Output Return Loss	R <sub>Lout</sub>			11	14	—	dB

## STANDARD CHARACTERISTICS FOR REFERENCE 1

(TA = +25°C, Vcc = VPS = 2.7 V, fin = 1 575 MHz, unless otherwise specified)

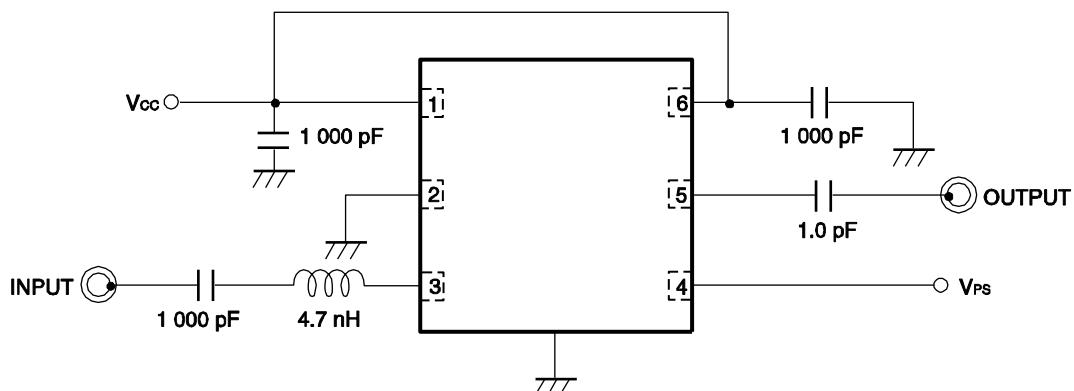
Parameter	Symbol	Test Conditions	Reference	Unit
Input 3rd Order Intercept Point	IIP <sub>3</sub>	f <sub>in1</sub> = 1 575 MHz, f <sub>in2</sub> = 1 574 MHz	-3	dBm
Isolation	ISL		39	dB
Gain 1 dB Compression Input Power	P <sub>in (1 dB)</sub>		-18	dBm

## STANDARD CHARACTERISTICS FOR REFERENCE 2

(TA = +25°C, Vcc = VPS = 1.8 V, fin = 1 575 MHz, unless otherwise specified)

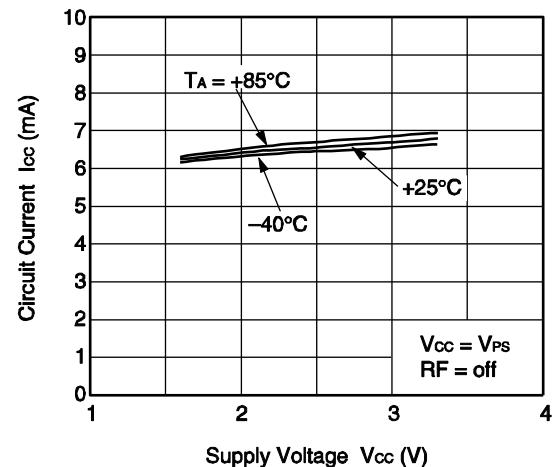
Parameter	Symbol	Test Conditions	Reference	Unit
Circuit Current	I <sub>cc</sub>	No Signal (V <sub>PS</sub> = 1.8 V)	6.2	mA
Power Gain	G <sub>P</sub>	P <sub>in</sub> = -35 dBm	19.1	dB
Noise Figure	NF		0.8	dB
Input 3rd Order Intercept Point	IIP <sub>3</sub>	f <sub>in1</sub> = 1 575 MHz, f <sub>in2</sub> = 1 574 MHz	-5	dBm
Input Return Loss	RL <sub>in</sub>		11	dB
Output Return Loss	RL <sub>out</sub>		14	dB
Isolation	ISL		39	dB
Gain 1 dB Compression Input Power	P <sub>in (1 dB)</sub>		-19	dBm

## TEST CIRCUIT

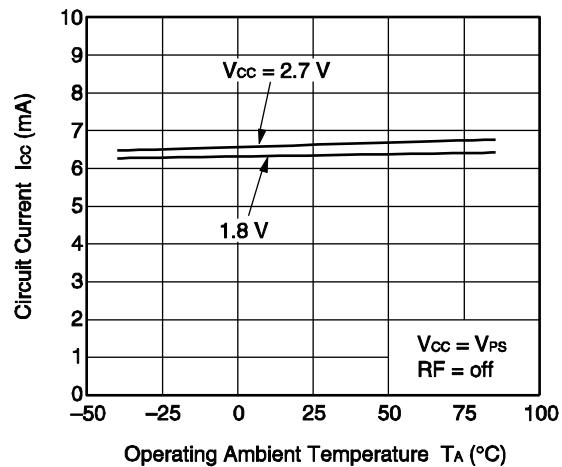


TYPICAL CHARACTERISTICS ( $T_A = +25^\circ\text{C}$ , unless otherwise specified)

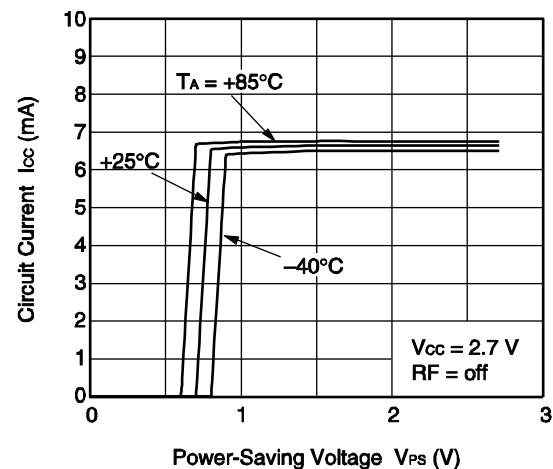
## CIRCUIT CURRENT vs. SUPPLY VOLTAGE



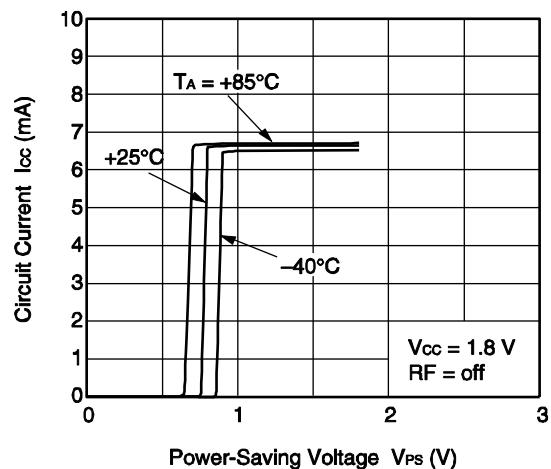
## CIRCUIT CURRENT vs. OPERATING AMBIENT TEMPERATURE



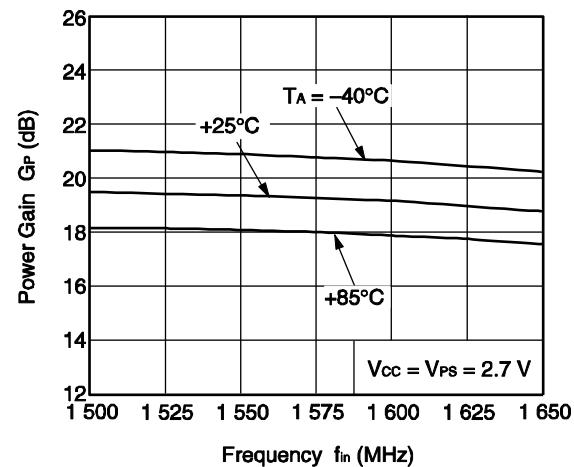
## CIRCUIT CURRENT vs. POWER-SAVING VOLTAGE



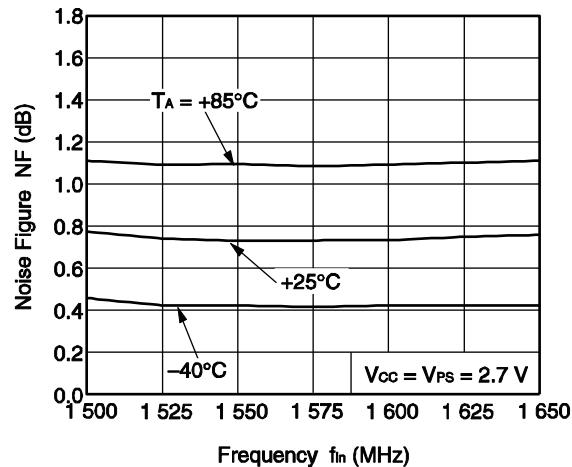
## CIRCUIT CURRENT vs. POWER-SAVING VOLTAGE



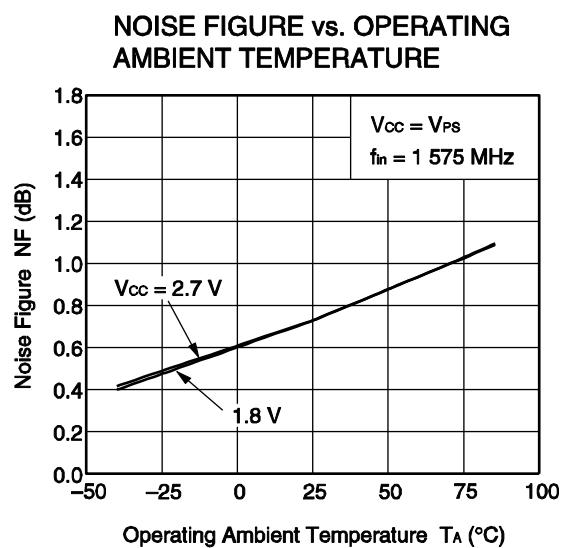
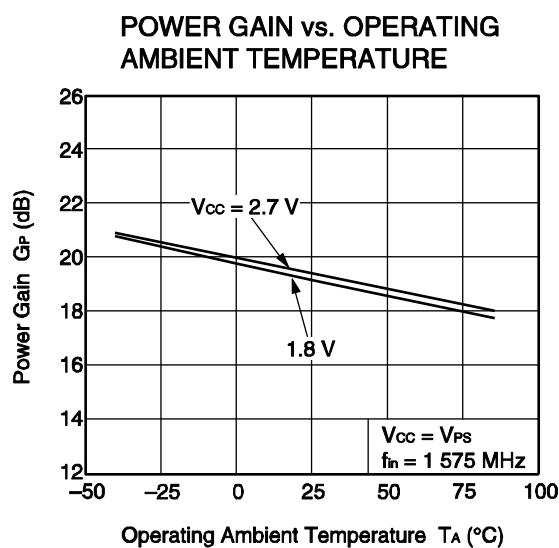
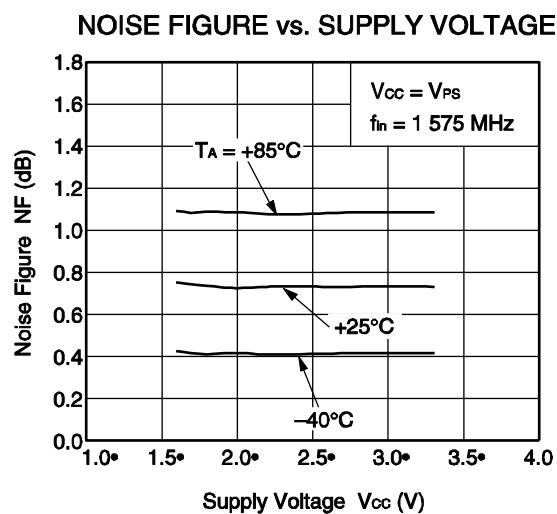
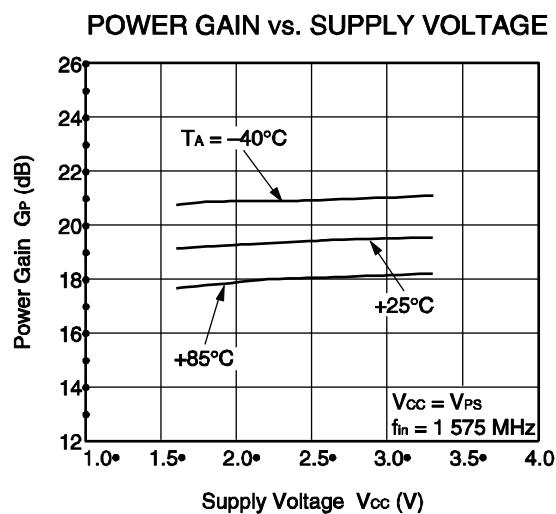
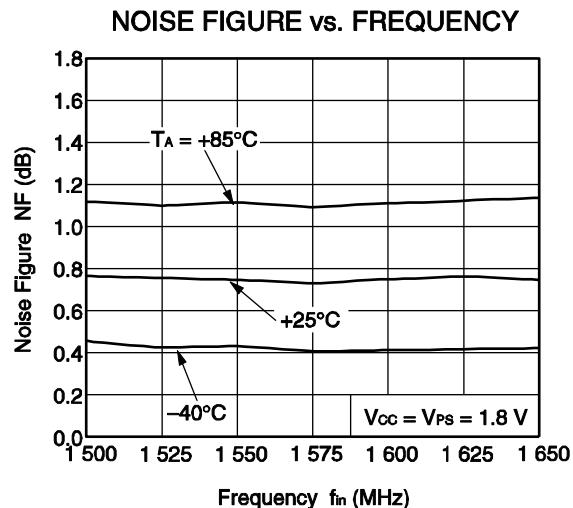
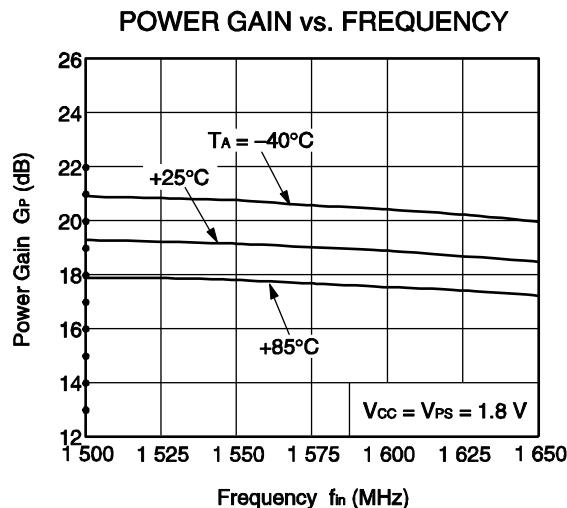
## POWER GAIN vs. FREQUENCY



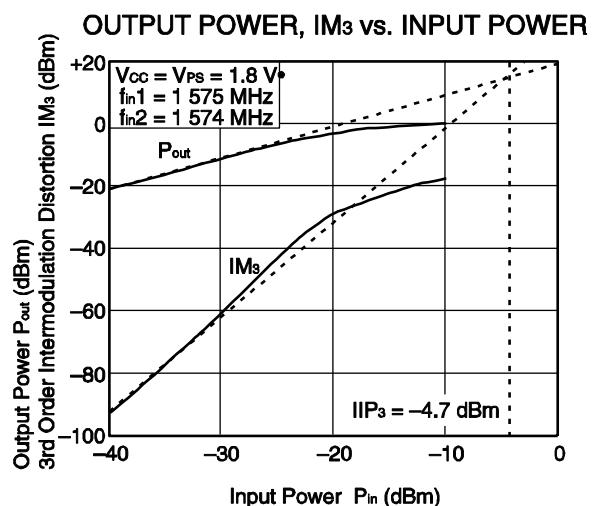
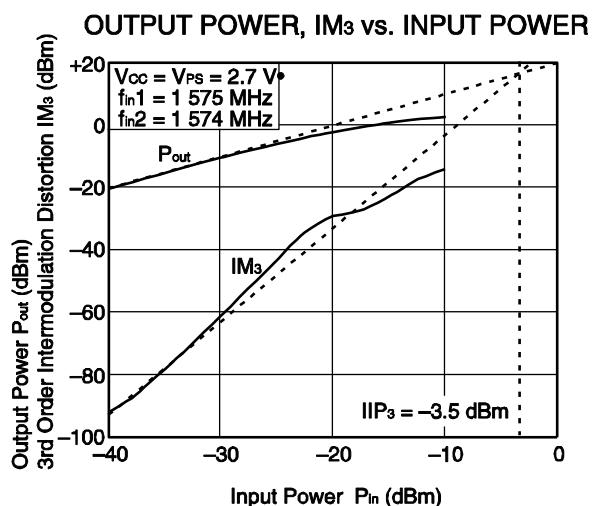
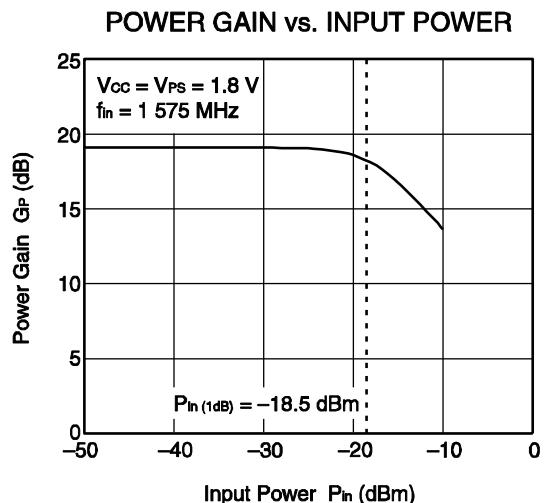
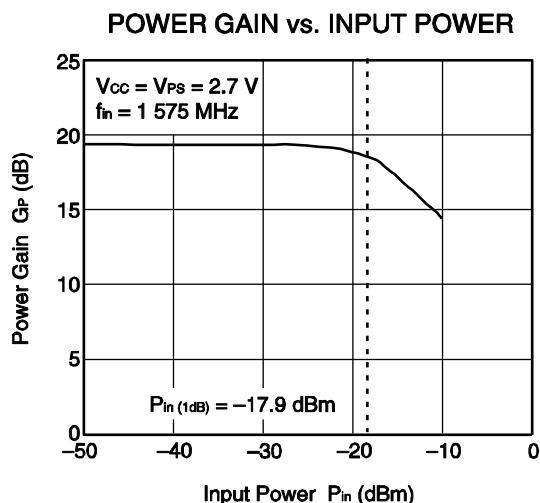
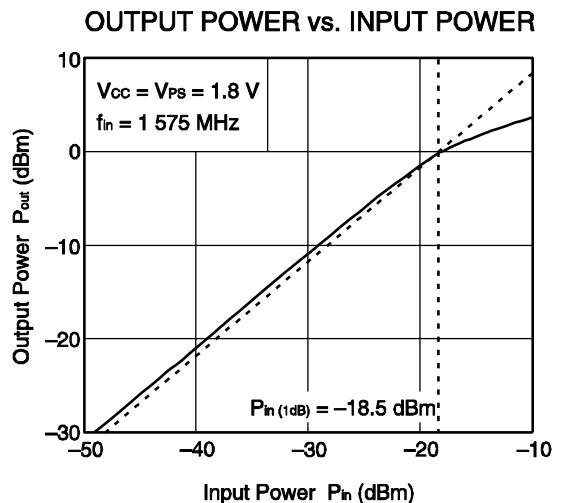
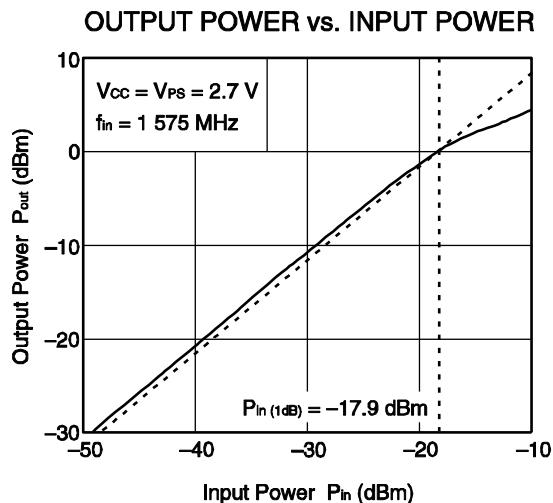
## NOISE FIGURE vs. FREQUENCY



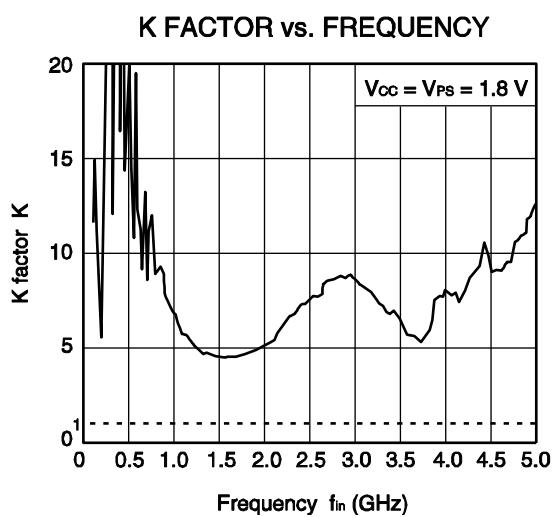
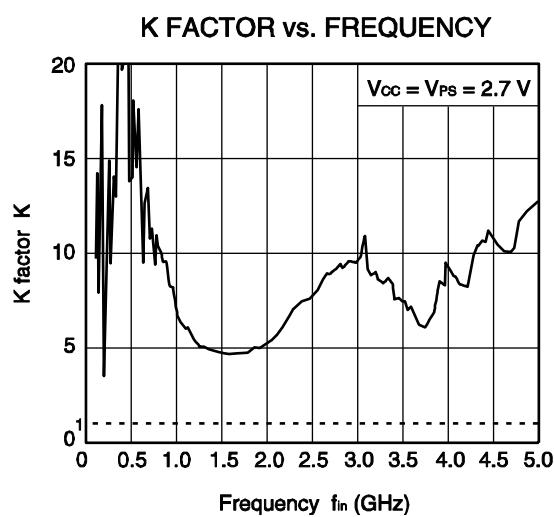
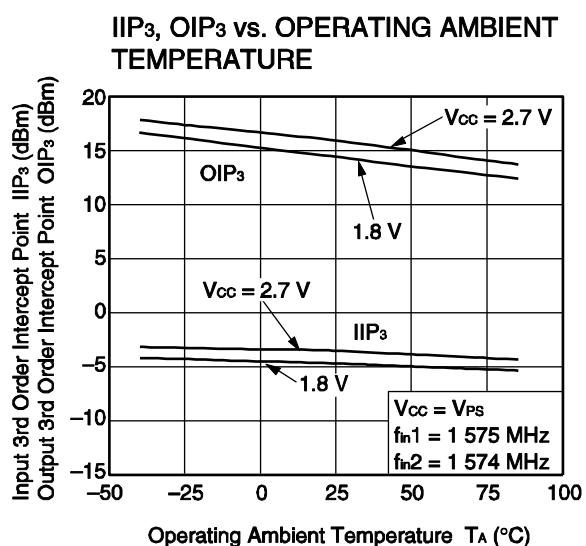
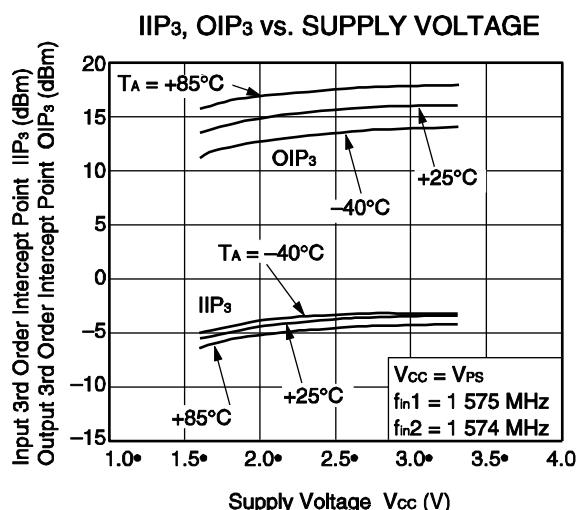
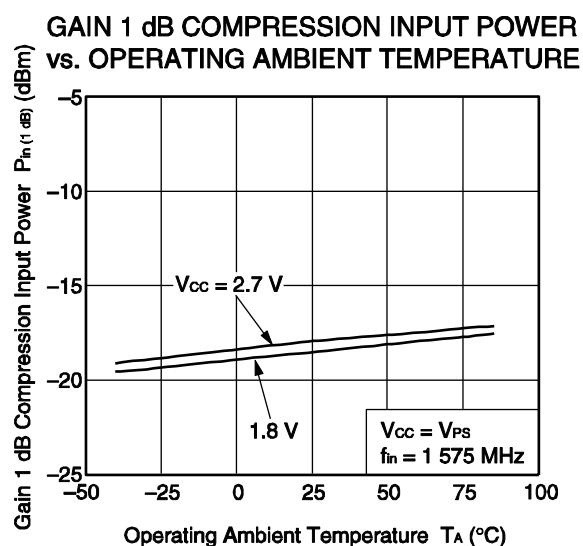
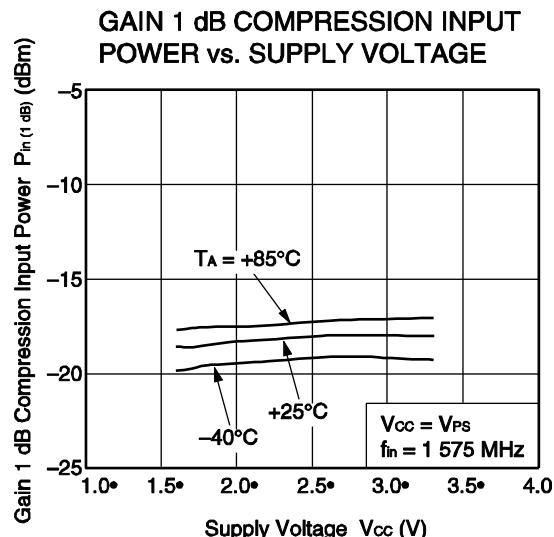
**Remark** The graphs indicate nominal characteristics.



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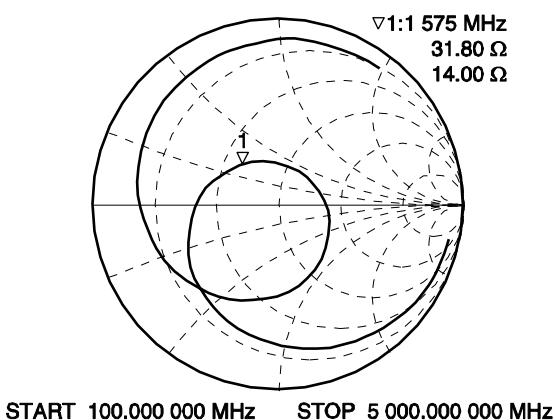
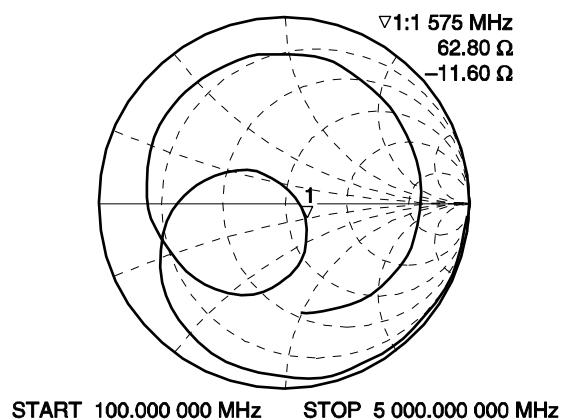


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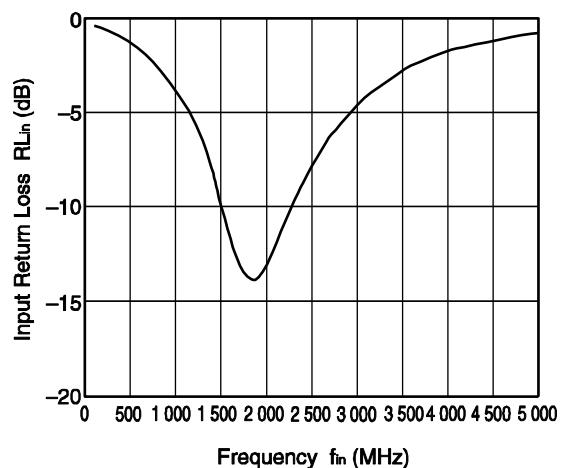


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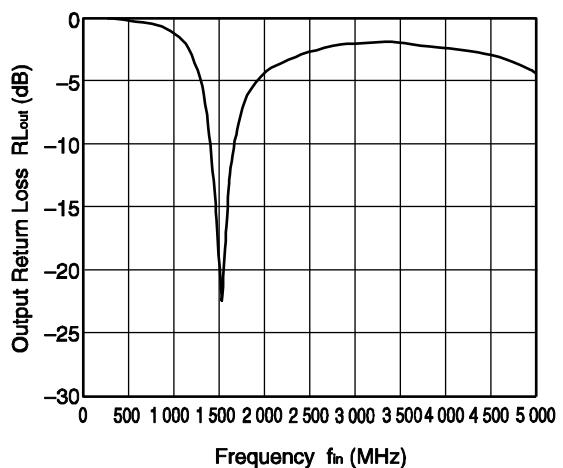
S-PARAMETERS ( $T_A = +25^\circ\text{C}$ ,  $V_{CC} = V_{PS} = 2.7$  V, monitored at connector on board)

S<sub>11</sub>-FREQUENCYS<sub>22</sub>-FREQUENCY

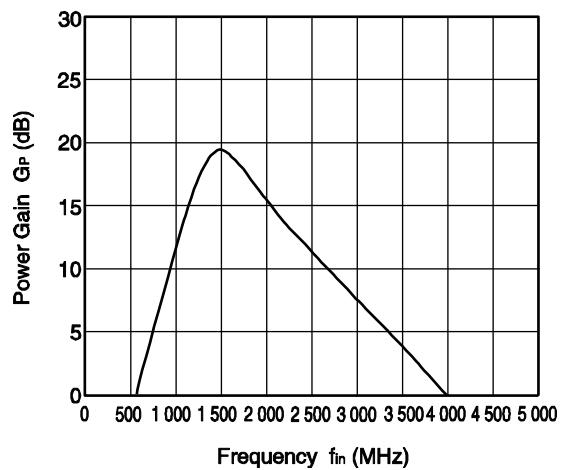
INPUT RETURN LOSS vs. FREQUENCY



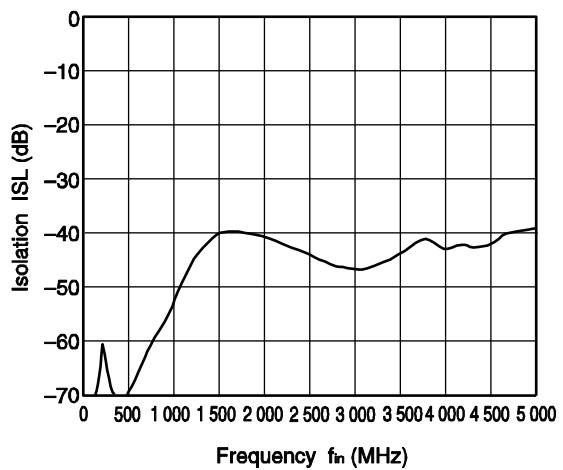
OUTPUT RETURN LOSS vs. FREQUENCY



POWER GAIN vs. FREQUENCY

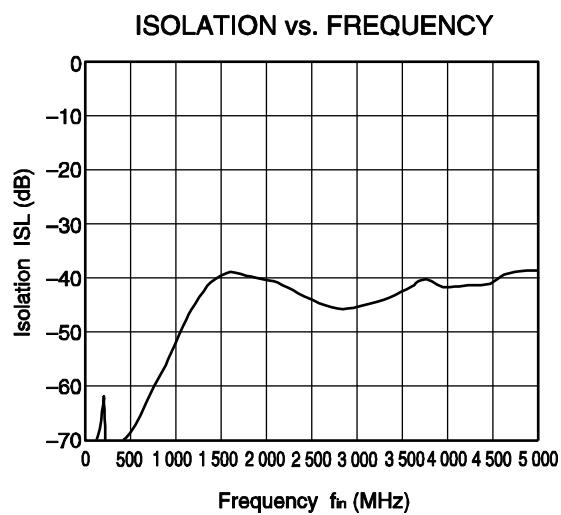
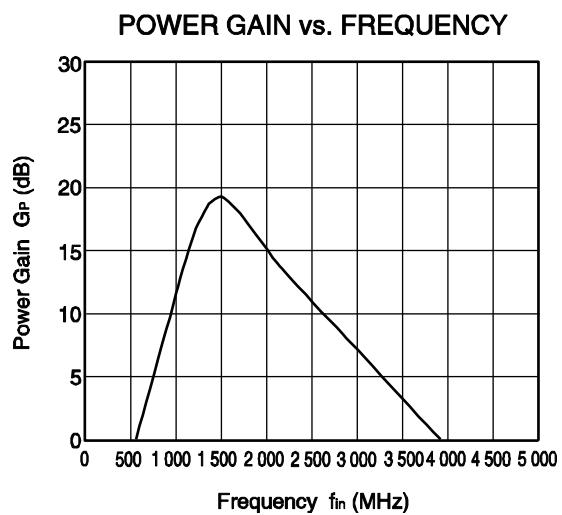
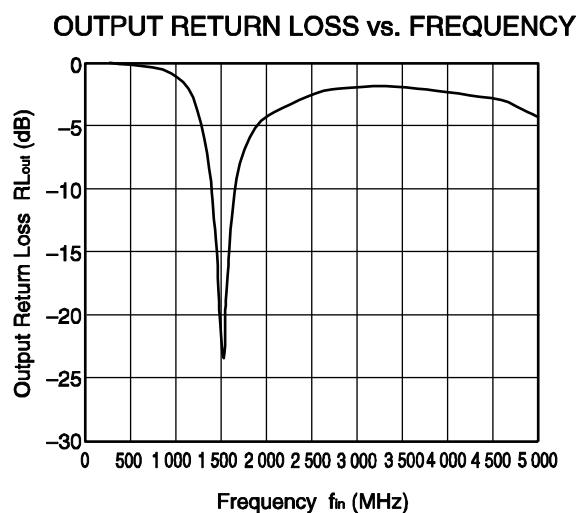
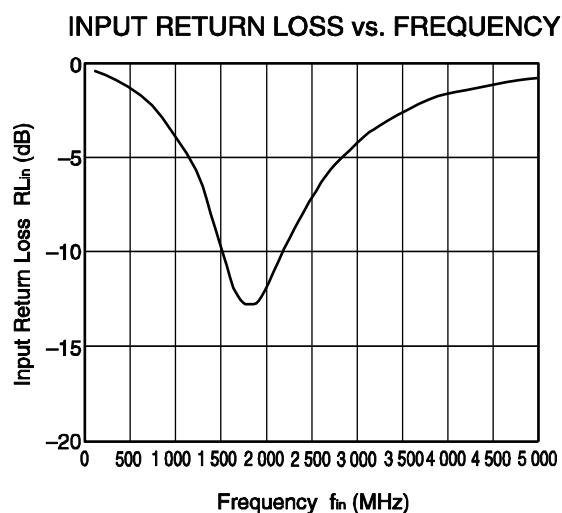
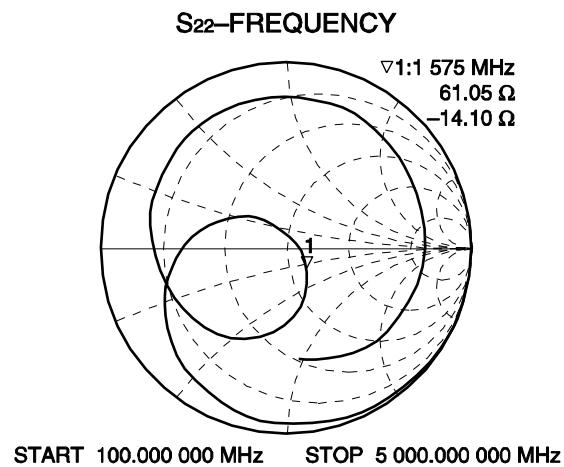
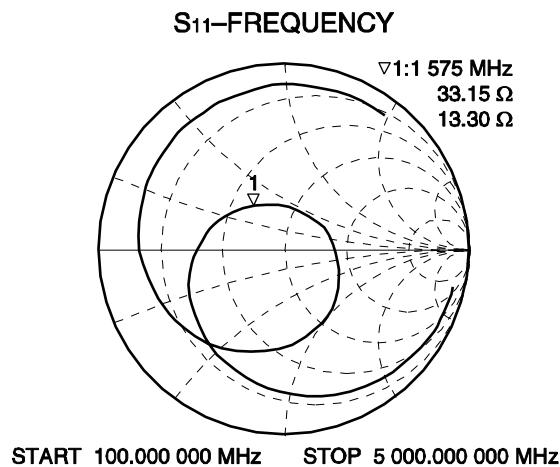


ISOLATION vs. FREQUENCY



**Remark** The graphs indicate nominal characteristics.

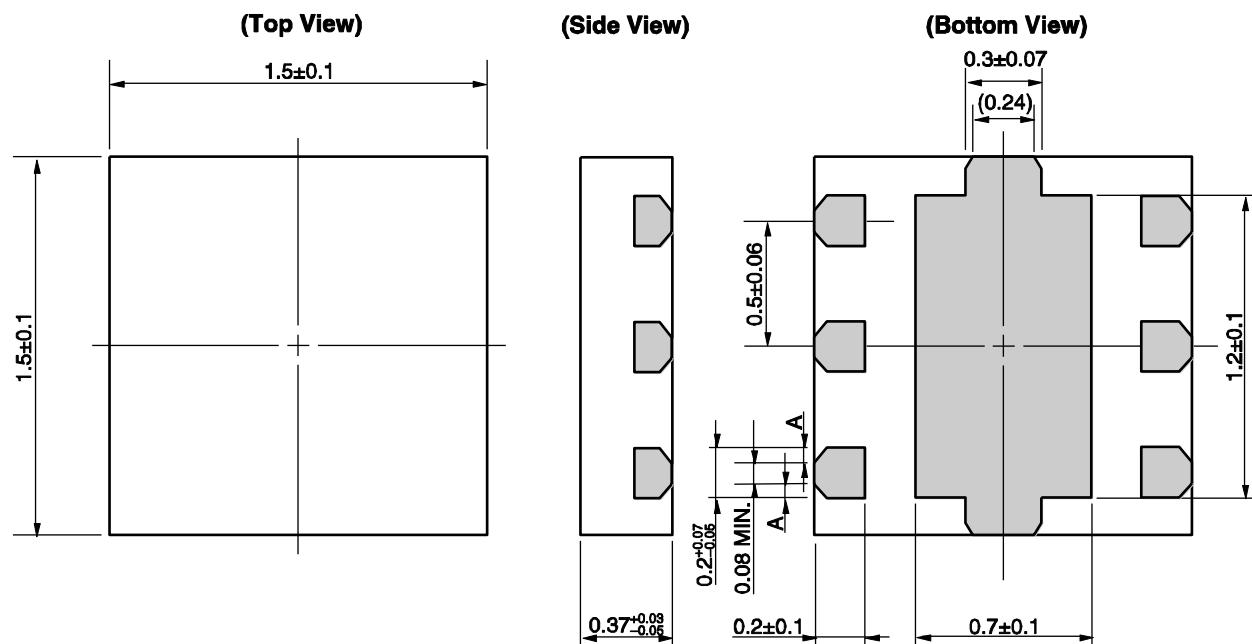
S-PARAMETERS ( $T_A = +25^\circ\text{C}$ ,  $V_{CC} = V_{PS} = 1.8$  V, monitored at connector on board)



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## PACKAGE DIMENSIONS

## 6-PIN PLASTIC TSON (T6N) (UNIT: mm)

**Remark** A>0

( ) : Reference value

**NOTES ON CORRECT USE**

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation). All the ground terminals must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to Vcc line.
- (4) Do not supply DC voltage to INPUT pin.

**RECOMMENDED SOLDERING CONDITIONS**

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions		Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below		IR260
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below		WS260
Partial Heating	Peak temperature (terminal temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below		HS350

**Caution Do not use different soldering methods together (except for partial heating).**

# Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

CEL:

[UPC8236T6N-A](#)

NEC:

[UPC8191K](#)