**TOSHIBA** 

## TOSHIBA ALLOY-FREE HIGHT SPEED THYRISTOR

# SH400R33B

## HIGH POWER CONTROL APPLICATIONS

• Repetitive Peak Off-State Voltage :  $V_{DRM}$  = 1300V

• Repetitive Peak Reverse Voltage: VRRM

• Average On-State Current  $: I_{T(AV)} = 400A$ 

• Turn-Off Time :  $t_q = 25 \mu s$  (Max.)

• Critical Rate of Rise of On-State Current

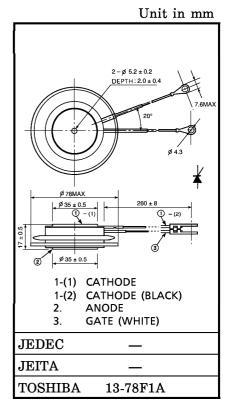
:  $di/dt = 200A/\mu s$ 

• Critical Rate of Rise of Off-State Voltage

:  $dv/dt = 500V/\mu s$ 

• Weight : 260g

• Flat Package



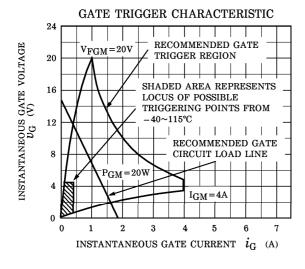
## **MAXIMUM RATINGS**

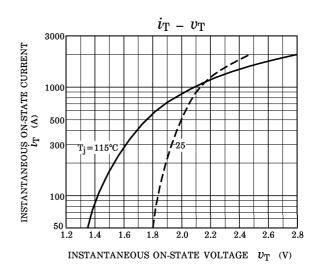
CHARACTERISTIC	SYMBOL	RATING	UNIT
Repetitive Peak Off-State Voltage and Repetitive Peak Reverse Voltage	$v_{ m DRM} \ v_{ m RRM}$	1300	V
Non-Repetitive Peak Reverse Voltage (Non-Repetitive < 5ms, T <sub>j</sub> = 0~125°C)	$v_{ m RSM}$	1400	V
R.M.S On-State Current	I <sub>T</sub> (RMS)	628	A
Average On-State Current	I <sub>T</sub> (AV)	400	Α
Peak One Cycle Surge On-State Current (Non-Repetitive)	ITSM	8000 (50Hz) 8800 (60Hz)	V
I <sup>2</sup> t Limit Value	${ m I}^2{ m t}$	$3.2 \times 10^{5}$	${\sf A}^2{\sf s}$
Critical Rate of Rise of On-State Current (Note)	di / dt	200	A/μs
Peak Gate Power Dissipation	$P_{GM}$	20	W
Average Gate Power Dissipation	P <sub>G</sub> (AV)	4	W
Peak Forward Gate Current	$I_{GM}$	4	A
Peak Forward Gate Voltage	$v_{FGM}$	20	V
Peak Reverse Gate Voltage	$v_{RGM}$	5	V
Junction Temperature	$T_{j}$	-40~115	$^{\circ}\mathrm{C}$
Storage Temperature Range	$\mathrm{T_{stg}}$	-40~115	$^{\circ}\mathrm{C}$
Mounting Force	_	$14.7 \pm 1.5$	kN

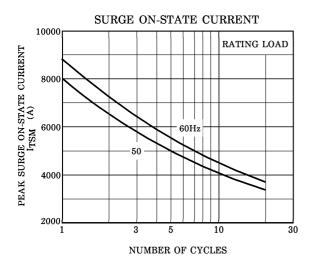
Note : V\_D=650V, f=50Hz, T\_j=110°C, Gate Supply (V\_G=15V, R\_G=8\Omega, t\_r  $\leq$   $1\mu s$ )

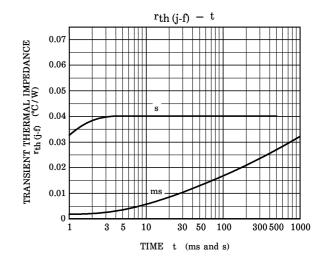
## **ELECTRICAL CHARACTERISTICS**

CHARACTERISTIC	SYMBOL	TEST CONDITION		MIN.	MAX.	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse Current	I <sub>DRM</sub> I <sub>RRM</sub>	$V_{ m DRM} = V_{ m RRM} = 1300 V$ $T_{ m j} = 115 { m ^{\circ} C}$		_	50	mA
Peak On-State Voltage	$V_{ extbf{TM}}$	$I_{TM} = 1250A, T_j = 25^{\circ}C$		_	2.2	V
Gate Trigger Voltage	$v_{GT}$	W 0W D 00	$T_j = -40^{\circ}C$ $T_i = 25^{\circ}C$	_	4.5 3.5	V
Gate Trigger Current	$I_{\mathrm{GT}}$	$V_{D}$ =6V, $R_{L}$ =6 $\Omega$	$T_j = -40^{\circ}C$ $T_j = 25^{\circ}C$	_ _	400 260	mA
Gate Non-Trigger Voltage	$v_{ m GD}$	$V_D = 650V, T_j = 115^{\circ}C$		0.2	_	V
Gate Non-Trigger Current	$I_{\mathrm{GD}}$			5	_	mA
Delay Time	<sup>t</sup> d	$V_D$ =650V, $T_j$ =25°C Gate Supply $(V_G$ =15V, $R_G$ =8 $\Omega$ , $t_r$ $\leq$ 1 $\mu$ s)			4	μs
Gate Turn-On Time	$t_{\mathrm{gt}}$			_	6	$\mu$ s
Turn-Off Time	$t_{\mathrm{q}}$	$I_{TM} = 800A, V_R \ge 50V$ $dv / dt = 20V / \mu s, T_j = 110$ °C $V_{DRM} = 650V$		1	25	μs
Holding Current	${ m I_{H}}$	$T_j = 25$ °C, $R_L = 6\Omega$			400	mA
Critical Rate of Rise of Off-State Voltage	dv / dt	V <sub>DRM</sub> =870V, T <sub>j</sub> =115°C Gate Open, Exponential Rise		500		V/μs
Thermal Resistance (Junction to Case)	$ m R_{th~(j-f)}$	DC		_	0.04	°C/W









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