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 $Products > ADC1413D065W0\ demoboard; compliant\ with\ Lattice,\ Altera,\ Xilinx\ FPGA\ boards\ through\ specific\ connectors$ 

# ADC1413D065WO • Obsolete

ADC1413D065W0 demoboard; compliant with Lattice, Altera, Xilinx FPGA boards through specific connectors

IDT's ADC demostration board is suitable for dynamic performances evaluation from low to high IF configuration. A FPGA mother board (Xilinx, Altera, and Lattice) could be connected to ease the evaluation and analysis of the ADC dynamic and enable usage of the JESD204A full features sets

### **Features**

- SMA connector for clock and analog inputs
- On board Data clock generator
- On board demodulator + LO synthesizer

### Single POWER supply (on board regulators)

## **Product Options**

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| Orderable Part       | Part     | Pkg. | Pkg.  | Lead Count | Temp. | Pb (Lead) | Buy          |
|----------------------|----------|------|-------|------------|-------|-----------|--------------|
| ID                   | Status   | Code | Type  | (#)        | Grade | Free      | Sample       |
| ADC1413D065WO-<br>DB | Obsolete | BD0  | BOARD | 0          | ı     | No        | Availability |

### **Related Products**

| Product ID    | Title ▼                    |  |  |  |  |
|---------------|----------------------------|--|--|--|--|
|               | Dual 14 bits ADC; 65 Msps; |  |  |  |  |
| ADC1413D065HN | serial JESD204A            |  |  |  |  |

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## **Images**

ADC1413D065WO - Evaluation Board



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