100 MHz M-LVDS Transceiver Evaluation Board

(ACTIVE) DS91D176EVK

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Key Document

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Description

The DS91D176EVK is an evaluation board design for demonstration of all features and functions of the DS91D176. The DS91D176 is a high-speed M-LVDS differential transceiver designed for multipoint applications with multiple drivers or receivers. The device conforms to TIA/EIA-899 standard. It utilizes M-LVDS technology for low power, high-speed and superior noise immunity.

The primary function of the board is to assist system designers in development and analysis of M-LVDS clock distribution networks in ATCA backplanes. The board also enables the user to examine performance and all functions of the DS91D176 as a standalone device or in a point-to-point configuration.

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DS91D176EVK/NOPB: 100 MHz M-LVDS Transceiver Evaluation Board	$115.00(USD) In Stock Typically Ships in 1 to 3 Business Days Buy from TI	ACTIVE	Yes	Yes

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