Continuous-Time Ratiometric Linear Hall Effect Sensors

Package LH, 3-pin Surface Mount

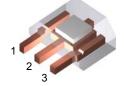
- 1. VCC
- 2. VOUT 3. GND



Package UA, 3-pin SIP

- 1. VCC 2. GND

3. VOUT



They are optimized to accurately provide a voltage output that is proportional to an applied magnetic field. These devices have a quiescent output voltage that is 50% of the supply voltage. Two output sensitivity options are provided: 2.5 mV/G typical for the A1301, and 1.3 mV/G typical for the A1302. The Hall-effect integrated circuit included in each device includes a Hall sensing

The A1301 and A1302 are continuous-time, ratiometric, linear Hall-effect sensors.

element, a linear amplifier, and a CMOS Class A output structure. Integrating the Hall sensing element and the amplifier on a single chip minimizes many of the problems normally associated with low voltage level analog signals.

High precision in output levels is obtained by internal gain and offset trim adjustments made at end-of-line during the manufacturing process.

These features make the A1301 and A1302 ideal for use in position sensing systems, for both linear target motion and rotational target motion. They are wellsuited for industrial applications over extended temperature ranges, from -40°C to 125°C.

Two device package types are available: LH, a 3-pin SOT23W type for surface mount, and UA, a 3-pin ultramini SIP for through-hole mount. Each package is available in a lead (Pb) free version (suffix, -T) with 100% matte tin plated leadframe.

ABSOLUTE MAXIMUM RATINGS

ı	Supply Voltage, V _{CC}	8 V
I	Output Voltage, V _{OUT}	8 V
I	Reverse-Supply Voltage, V _{RCC}	0.1 V
I	Reverse-Output Voltage, V _{ROUT}	0.1 V
I	Output Sink Current, I _{OUT}	10 mA
I	Operating Temperature	
I	Ambient, T _A , Range E	40°C to 85°C
I	Ambient, TA, Range K	40°C to 125°C
I	Maximum Junction, T _{J(max)}	165°C
I	Storage Temperature, T _S	

Features and Benefits

- Low-noise output
- Fast power-on time
- Ratiometric rail-to-rail output
- 4.5 to 6.0 V operation
- Solid-state reliability
- Factory-programmed at end-of-line for optimum performance
- Robust ESD performance





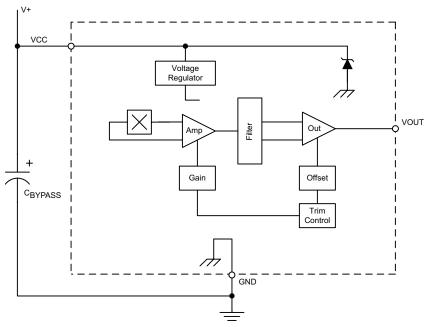
Continuous-Time Ratiometric Linear Hall Effect Sensors

Product Selection Guide

Part Number	Pb-free	Packing ¹	Package Ambient, T _A		Sensitivity (Typical)		
A1301ELHLT ²	-	7-in, tape and reel, 3000 pieces/reel Surface Mount					
A1301ELHLT-T	Yes	7-in. tape and reel, 3000 pieces/reel	Surface Mount	-40°C to 85°C			
A1301EUA2	ı	Bulk 500 pioces/bag	SIP	-40 C to 65 C			
A1301EUA-T	Yes	Bulk, 500 pieces/bag	312		2.5 m\//C		
A1301KLHLT2	ı	7-in. tape and reel, 3000 pieces/reel	Curface Mount		2.5 mV/G		
A1301KLHLT-T	Yes	7-III. tape and reel, 3000 pieces/reel	Surface Mount	400C to 1050C			
A1301KUA ²	-	Bulk, 500 pieces/bag	SIP	-40°C to 125°C			
A1301KUA-T	Yes	Bulk, 500 pieces/bag	SIF				
A1302ELHLT ²	-	7-in. tape and reel, 3000 pieces/reel	Surface Mount				
A1302ELHLT-T	Yes	7-iii. tape and reei, 3000 pieces/reei	Surface Mount	-40°C to 85°C			
A1302EUA ²	-	Pulk 500 pigggs/bag	CID	-40 C to 65 C			
A1302EUA-T	Yes	Bulk, 500 pieces/bag	SIP		4.2>//0		
A1302KLHLT ²	_	7 in tana and real 2000 piages/real	Surface Mount		1.3 mV/G		
A1302KLHLT-T	Yes	7-in. tape and reel, 3000 pieces/reel	Surface Mount	400C to 1250C			
A1302KUA2	_	Pulk 500 pioces/bag	Bulk. 500 pieces/bag SIP				
A1302KUA-T	T Yes Bulk, 500 pieces/bag		3IP				

¹Contact Allegro for additional packing options.

Functional Block Diagram



Terminal List

		Number		
Symbol	Description	Package LH		
VCC	Connects power supply to chip	1	1	
VOUT	Output from circuit	2	3	
GND	Ground	3	2	



²These parts are in production but have been determined to be LAST TIME BUY. This classification indicates that the product is obsolete and notice has been given. Sale of this device is currently restricted to existing customer applications. The device should not be purchased for new design applications because of obsolescence in the near future. Samples are no longer available. Date of status change: October 31, 2005. Deadline for receipt of LAST TIME BUY orders: April 28, 2006.

Continuous-Time Ratiometric Linear Hall Effect Sensors

DEVICE CHARACTERISTICS over operating temperature range, T_A , and V_{CC} = 5 V, unless otherwise noted

Characteristic	Symbol	Test Conditions	Min.	Тур.	Max.	Units		
Electrical Characteristics								
Supply Voltage	V _{CC}	Running, T _J < 165°C	4.5	_	6	V		
Supply Current	I _{CC}	Output open	_	_	11	mA		
Output Voltage	V _{OUT(High)}	I _{SOURCE} = -1 mA, Sens = nominal	4.65	4.7	_	V		
Output voltage	V _{OUT(Low)}	I _{SINK} = 1 mA, Sens = nominal	_	0.2	0.25	V		
Output Bandwidth	BW		_	20	_	kHz		
Power-On Time	t _{PO}	$V_{CC(min)}$ to 0.95 V_{OUT} ; B = ±1400 G; Slew rate = 4.5 V/µs to 4.5 V/100 ns	_	3	5	μs		
Output Resistance	R _{OUT}	I _{SINK} ≤ 1 mA, I _{SOURCE} ≥ −1 mA	_	2	5	Ω		
Wide Band Output Noise, rms	V _{OUTN}	External output low pass filter ≤ 10 kHz; Sens = nominal	_	150	_	μV		
Ratiometry	1							
Quiescent Output Voltage Error with respect to ΔV_{CC}^{1}	$\Delta V_{OUTQ(V)}$	T _A = 25°C	_	_	±3.0	%		
Magnetic Sensitivity Error with respect to ΔV_{CC}^2	ΔSens _(V)	T _A = 25°C	_	_	±3.0	%		
Output								
Linearity	Lin	$T_A = 25^{\circ}C$	_	-	±2.5	%		
Symmetry	Sym	$T_A = 25^{\circ}C$	_	-	±3.0	%		
Magnetic Characteristics								
Quiescent Output Voltage	V _{OUTQ}	B = 0 G; T _A = 25°C	2.4	2.5	2.6	V		
Quiescent Output Voltage over Operating Temperature Range	$V_{OUTQ(\Delta T_A)}$	B = 0 G	2.2	_	2.8	V		
Magnetic Consitivity	Sens	A1301; T _A = 25°C	2.0	2.5	3.0	mV/G		
Iwagnetic Sensitivity		A1302; T _A = 25°C	1.0	1.3	1.6	mV/G		
Magnetic Sensitivity over	Sons	A1301	1.8	-	3.2	mV/G		
Operating Temperature Range	Jens _(ATA)	A1302	0.85	_	1.75	mV/G		
	$Sens_{(\Delta T_A)}$	A1302; T _A = 25°C A1301 A1302	1.0	1.3	1.6 3.2			

¹Refer to equation (4) in Ratiometric section on page 4.



²Refer to equation (5) in Ratiometric section on page 4.

Continuous-Time Ratiometric Linear Hall Effect Sensors

Characteristic Definitions

Quiescent Output Voltage. In the quiescent state (no significant magnetic field: B = 0), the output, V_{OUTO} , equals one half of the supply voltage, V_{CC}, throughout the entire operating ranges of V_{CC} and ambient temperature, T_A. Due to internal component tolerances and thermal considerations, there is a tolerance on the quiescent output voltage, ΔV_{OUTO} , which is a function of both ΔV_{CC} and ΔT_A . For purposes of specification, the quiescent output voltage as a function of temperature, $\Delta V_{OUTO(\Delta T_{\Delta})}$, is defined as:

$$\Delta V_{\text{OUTQ}(\Delta T_{A})} = \frac{V_{\text{OUTQ}(T_{A})} - V_{\text{OUTQ}(25^{\circ}\text{C})}}{Sens_{(25^{\circ}\text{C})}}$$
(1)

where Sens is in mV/G, and the result is the device equivalent accuracy, in gauss (G), applicable over the entire operating temperature range.

Sensitivity. The presence of a south-polarity (+B) magnetic field, perpendicular to the branded face of the device package, increases the output voltage, V_{OUT}, in proportion to the magnetic field applied, from V_{OUTO} toward the V_{CC} rail. Conversely, the application of a north polarity (-B) magnetic field, in the same orientation, proportionally decreases the output voltage from its quiescent value. This proportionality is specified as the magnetic sensitivity of the device and is defined as:

$$Sens = \frac{V_{\text{OUT}(-B)} - V_{\text{OUT}(+B)}}{2B}$$
 (2)

The stability of the device magnetic sensitivity as a function of ambient temperature, $\Delta Sens_{(\Delta T_A)}$ (%) is defined as:

$$\Delta Sens_{(\Delta T_A)} = \frac{Sens_{(T_A)} - Sens_{(25^{\circ}C)}}{Sens_{(25^{\circ}C)}} \times 100\%$$
 (3)

Ratiometric. The A1301 and A1302 feature a ratiometric output. This means that the quiescent voltage output, V_{OUTO}, and the magnetic sensitivity, Sens, are proportional to the supply voltage, V_{CC}.

The ratiometric change (%) in the quiescent voltage output is defined as:

$$\Delta V_{\rm OUTQ(\Delta V)} = \frac{V_{\rm OUTQ(V_{CC})} / V_{\rm OUTQ(5V)}}{V_{\rm CC} / 5 \text{ V}} \times 100\%$$
 (4)

and the ratiometric change (%) in sensitivity is defined as:

$$\Delta Sens_{(\Delta V)} = \frac{Sens_{(VCC)} / Sens_{(5V)}}{V_{CC} / 5 V} \times 100\%$$
 (5)

Linearity and Symmetry. The on-chip output stage is designed to provide linear output at a supply voltage of 5 V. Although the application of very high magnetic fields does not damage these devices, it does force their output into a nonlinear region. Linearity in percent is measured and defined as:

$$Lin+ = \frac{V_{\text{OUT(+B)}} - V_{\text{OUTQ}}}{2(V_{\text{OUT(+B/2)}} - V_{\text{VOUTQ}})} \times 100\%$$
 (6)

$$Lin- = \frac{V_{\text{OUT(-B)}} - V_{\text{OUTQ}}}{2(V_{\text{OUT(-B½)}} - V_{\text{OUTQ}})} \times 100\%$$
 (7)

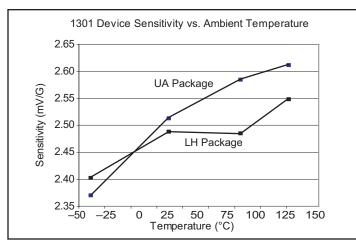
and output symmetry as:

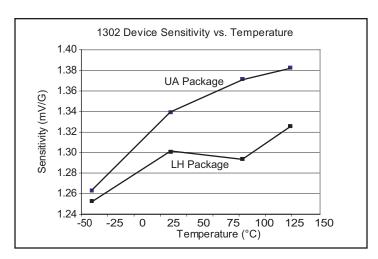
$$Sym = \frac{V_{\text{OUT}(+B)} - V_{\text{OUTQ}}}{V_{\text{OUTO}} - V_{\text{OUT}(-B)}} \times 100\%$$
 (8)

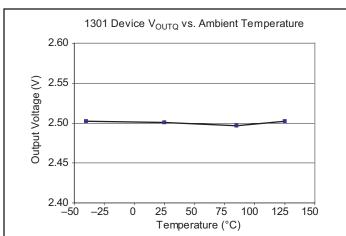


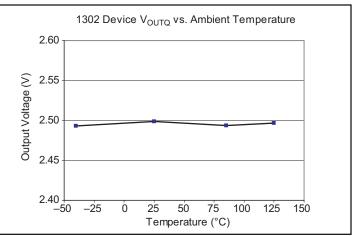
Continuous-Time Ratiometric Linear Hall Effect Sensors

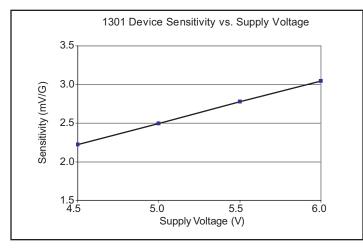
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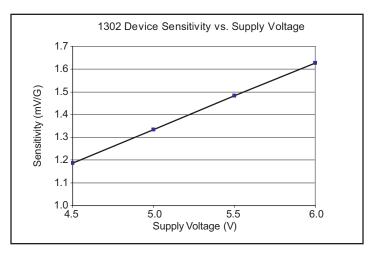












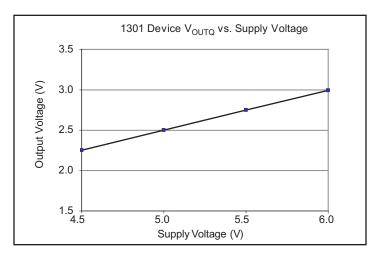
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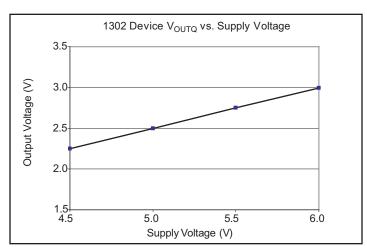
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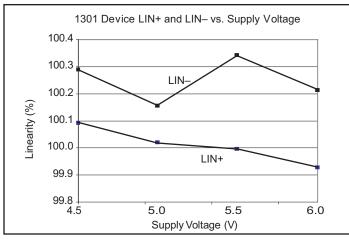


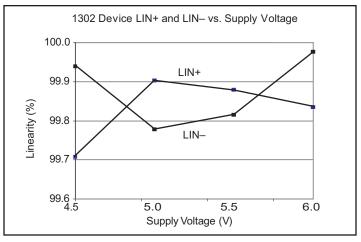
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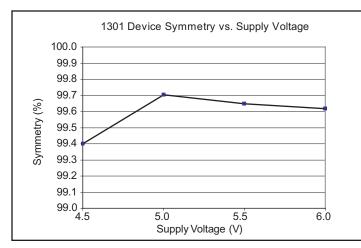
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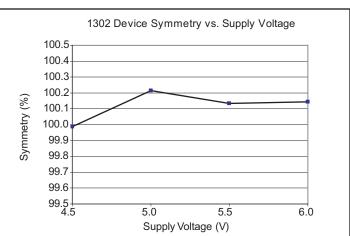






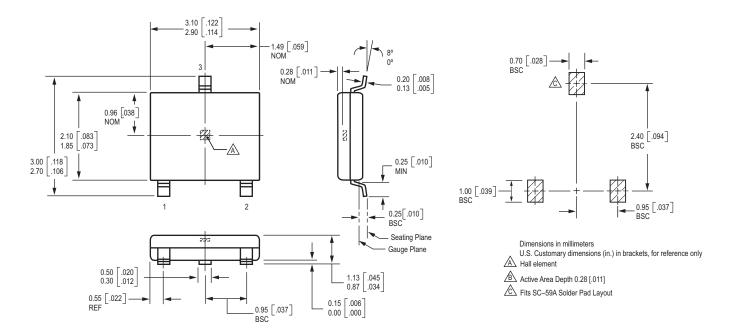




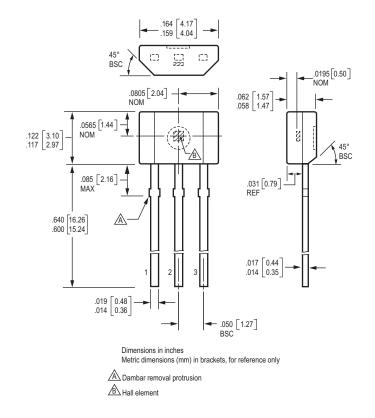


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Package LH, 3-Pin; (SOT-23W)



Package UA, 3-Pin; (TO-92)





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The products described herein are manufactured under one or more of the following U.S. patents: 5,045,920; 5,264,783; 5,442,283; 5,389,889; 5,581,179; 5,517,112; 5,619,137; 5,621,319; 5,650,719; 5,686,894; 5,694,038; 5,729,130; 5,917,320; and other patents pending.

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