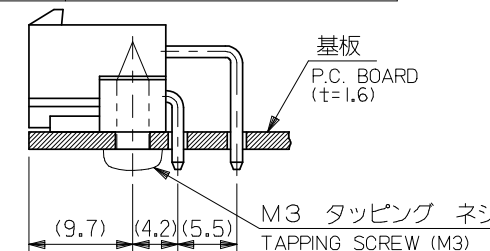
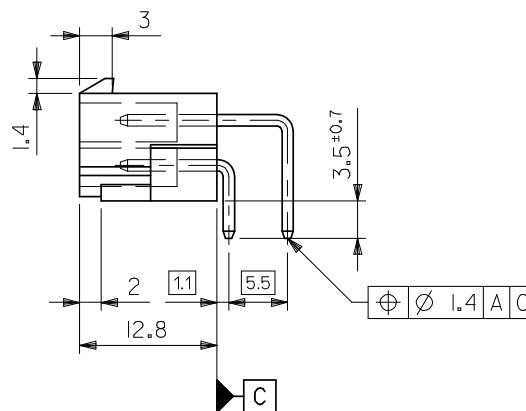
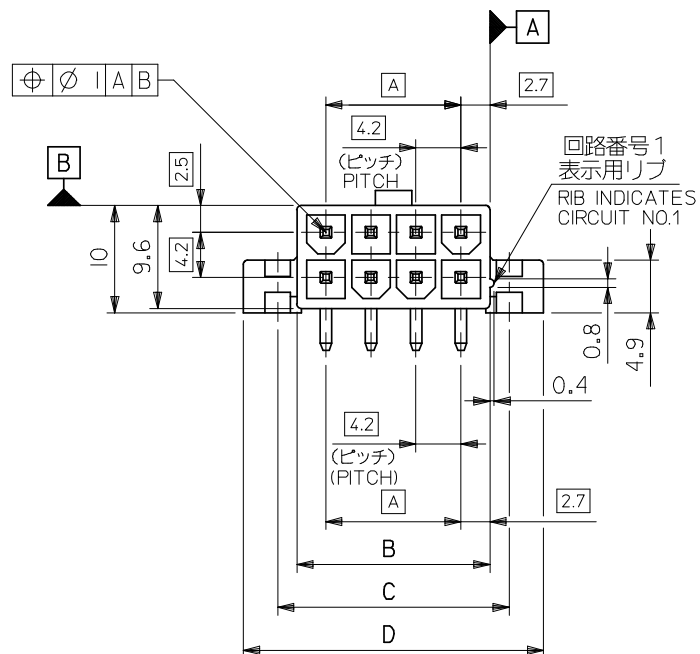
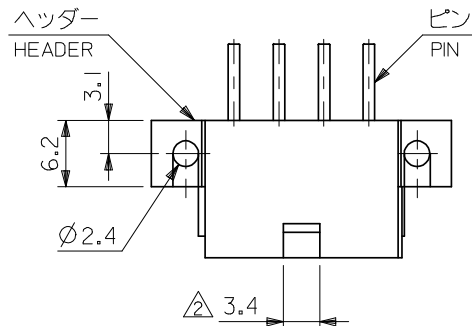


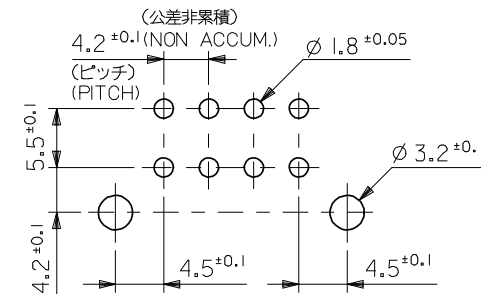
注記

1. 嵌合相手 : 5557 シリーズ
MATES WITH : 5557 SERIES
2. 2極の場合のみ本寸法は 4.2
IN CASE OF 2 CIRCUITS HEADER, THIS DIMENSION SHOULD BE 4.2mm.
3. 材料及びメッキ
MATERIAL & FINISH

ENG. NO.	ヘッダー材料 HEADER MATERIAL	ピン材料 PIN MATERIAL	ピンのメッキ FINISH OF PIN
5569-NA1	ナイロン66, UL94V-2 NYLON 66, UL94V-2	黄銅 ($\square 1.07$) BRASS (1.07 SQUARE)	銅下地錫メッキ TIN OVER COPPER
-NA1-210	ナイロン66, UL94V-0 NYLON 66, UL94V-0		
-NAG1	ナイロン66, UL94V-2 NYLON 66, UL94V-2		金 : $0.76\mu\text{m}$ MIN. GOLD $0.76\mu\text{m}$ MIN.
5569-NAG1-210	ナイロン66, UL94V-0 NYLON 66, UL94V-0		ニッケル下地 : $1.27\mu\text{m}$ MIN. OVER NICKEL $1.27\mu\text{m}$ MIN.



基板取付推奨寸法
RECOMMENDED INSTALLATION PATTERN



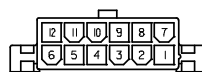
基板穴推奨寸法
RECOMMENDED P.C. BOARD PATTERN

61.6	55.2	51.6	46.2	24
57.4	51	47.4	42	22
53.2	46.8	43.2	37.8	20
49	42.6	39	33.6	18
44.8	38.4	34.8	29.4	16
40.6	34.2	30.6	25.2	14
36.4	30	26.4	21	12
32.2	25.8	22.2	16.8	10
28	21.6	18	12.6	8
23.8	17.4	13.8	8.4	6
19.6	13.2	9.6	4.2	4
15.4	9	5.4	—	2
D	C	B	A	極数 CIRCUIT

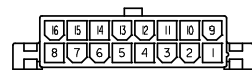
REVISED EC NO: J2008-3781 DRWN:WABEI CHKD:MISOGAYA APPR:NUKITA	DESCRIPTION 2008/05/12 2008/05/12 2008/05/12	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 2:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
		10 UNDER	±0.2	DRAWN BY H.HIRAMOTO	DATE '90/04/23	TITLE NEW MINI FIT CONN R/A HEADER ASSY WITH FLANGE				
		10 OVER 30 UNDER	±0.3	CHECKED BY M.FUKUSHIMA	DATE '91/07/04					
		30 OVER	±0.4	APPROVED BY M.ENOMOTO	DATE '91/07/04	molex MOLEX INCORPORATED				
		ANGULAR ±3 °		MATERIAL NO.		DOCUMENT NO.		SHEET NO.		
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE TABLE		SD-5569-002		1 OF 2		
REV J		SIZE A3	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

A diagram of a 16-bit shift register. It consists of two rows of seven cells each. The top row is labeled with inputs 14, 13, 12, 11, 10, 9, and 8 from left to right. The bottom row is labeled with outputs 7, 6, 5, 4, 3, 2, and 1 from left to right. Arrows indicate data flow from the input cells into the register and from the output cells out of the register.

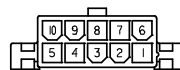
14 CKTS.



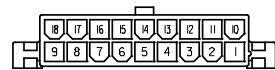
12極
12 CKTS.



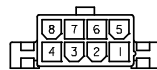
16極
16 CKTS.



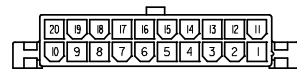
10極
10 CKTS.



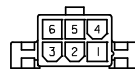
18極
18 CKTS.



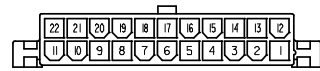
8極
8 CKTS.



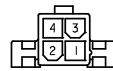
20極
20 CKTS.



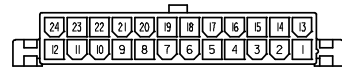
6極
6 CKTS.



22極
22 CKTS.



4極
4 CKTS.



24極
24 CKTS.



2極
2 CKTS

極数別レイアウト図
CIRCUIT SIZE LAYOUT
(SCALE 1:1)

39-29-4249	5569-24AGI-210	39-29-1247	5569-24AI-210	24
▲ -4229	▲ -22AGI-210	▲ -1227	▲ -22AI-210	22
-4209	-20AGI-210	-1207	-20AI-210	20
-4189	-18AGI-210	-1187	-18AI-210	18
-4169	-16AGI-210	-1167	-16AI-210	16
-4149	-14AGI-210	-1147	-14AI-210	14
-4129	-12AGI-210	-1127	-12AI-210	12
-4109	-10AGI-210	-1107	-10AI-210	10
-4089	-08AGI-210	-1087	-08AI-210	8
-4069	-06AGI-210	-1067	-06AI-210	6
▼ -4049	▼ -04AGI-210	▼ -1047	▼ -04AI-210	4
39-29-4029	5569-02AGI-210	39-29-1027	5569-02AI-210	2
EDP NO.	ENG. NO.	EDP NO.	ENG. NO.	極数
5569-NAGI-210		5569-NAI-210		CIRCUIT
39-29-5243	5569-24AGI	39-29-1248	5569-24AI	24
▲ -5223	▲ -22AGI	▲ -1228	▲ -22AI	22
-5203	-20AGI	-1208	-20AI	20
-5183	-18AGI	-1188	-18AI	18
-5163	-16AGI	-1168	-16AI	16
-5143	-14AGI	-1148	-14AI	14
-5123	-12AGI	-1128	-12AI	12
-5103	-10AGI	-1108	-10AI	10
-5083	-08AGI	-1088	-08AI	8
-5063	-06AGI	-1068	-06AI	6
▼ -5043	▼ -04AGI	▼ -1048	▼ -04AI	4
39-29-5023	5569-02AGI	39-29-1028	5569-02AI	2
EDP NO.	ENG. NO.	EDP NO.	ENG. NO.	極数
5569-NAGI		5569-NAI		CIRCUIT