2-channel Analog Switch

# **HITACHI**

ADE-205-566B (Z)

Rev.2 July 2001

#### **Description**

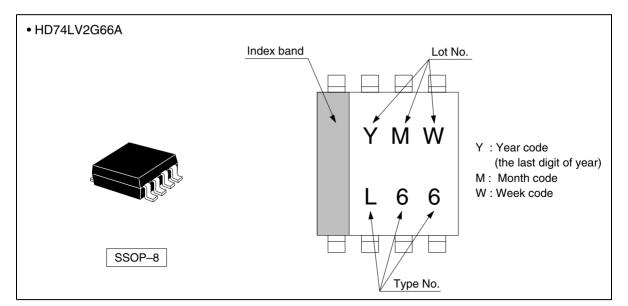
The HD74LV2G66A has 2–channel analog switch in a 8 pin package. Each switch section has its own enable input control (C). High level voltage applied to C turns on the associated switch section. Applications include signal gating, chopping, modulation or demodulation (modem), and signal multiplexing for analog to digital and digital to analog conversion systems. Low voltage and high speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

#### **Features**

- The basic gate function is lined up as hitachi uni logic series.
- Supplied on emboss taping for high speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV4066A Supply voltage range: 1.65 to 5.5 V
  Operating temperature range: -40 to +85°C
- Control inputs  $V_{H}$  (Max.) = 5.5 V (@ $V_{CC}$  = 0 V to 5.5 V)
- Control inputs has hysteresis voltage for the slow transition.



#### **Outline and Article Indication**

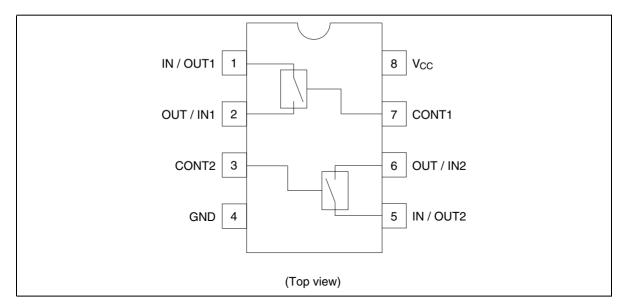


#### **Function Table**

Control	Switch
L	OFF
Н	ON

H : High level L : Low level

## **Pin Arrangement**



#### **Absolute Maximum Ratings**

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V <sub>cc</sub>	-0.5 to 7.0	V	
Input voltage range *1	V <sub>I</sub>	-0.5 to 7.0	V	
Output voltage range *1,2	V <sub>o</sub>	$-0.5$ to $V_{cc} + 0.5$	V	Output : H or L
Input clamp current	I <sub>IK</sub>	-20	mA	V <sub>1</sub> < 0
Output clamp current	I <sub>ok</sub>	±50	mA	$V_o < 0 \text{ or } V_o > V_{cc}$
Continuous output current	Io	±25	mA	$V_o = 0$ to $V_{cc}$
Continuous current through $V_{cc}$ or GND	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA	
Maximum power dissipation at Ta = 25°C (in still air) <sup>3</sup>	P <sub>T</sub>	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes:

- The absolute maximum ratings are values which must not individually be exceeded, and furthermore no two of which may be realized at the same time.
- 1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

### **Recommended Operating Conditions**

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V <sub>cc</sub>	1.65	5.5	V	
Input voltage range	V <sub>i</sub>	0	5.5	V	
Input / output voltage range	V <sub>I/O</sub>	0	V <sub>cc</sub>	V	
Input transition rise or fall rate	Δt / Δν	0	300	ns / V	V <sub>cc</sub> = 1.65 to 1.95 V
		0	200		$V_{cc} = 2.3 \text{ to } 2.7 \text{ V}$
		0	100		$V_{cc} = 3.0 \text{ to } 3.6 \text{ V}$
		0	20		$V_{cc} = 4.5 \text{ to } 5.5 \text{ V}$
Operating free-air temperature	T <sub>a</sub>	-40	85	°C	

Note: Unused or floating control inputs must be held high or low.

## **Electrical Characteristic**

Item	Symbol	$V_{cc}$ (V)	$T_a = 2$	25°C		$T_a = -40$	to 85°0		Unit	Test
			Min	Тур	Max	Min	Тур	Max	_	Conditions
Input voltage	V <sub>IH</sub>	1.65 to 1.95	_	_	_	V <sub>cc</sub> ×0.75	_	_	V	Control input
		2.3 to 2.7	_	_	_	V <sub>cc</sub> ×0.7	_	_	_	only
		3.0 to 3.6	_	_	_	V <sub>cc</sub> ×0.7	_	_	_	
		4.5 to 5.5	_	_	_	V <sub>cc</sub> ×0.7	_	_	-	
	V <sub>IL</sub>	1.65 to 1.95	_	_	_	_	_	V <sub>cc</sub> ×0.25	-	
		2.3 to 2.7	_	_	_	_	_	V <sub>cc</sub> ×0.3	-	
		3.0 to 3.6	_	_	_	_	_	V <sub>cc</sub> ×0.3	-	
		4.5 to 5.5	_	_	_	_	_	V <sub>cc</sub> ×0.3	-	
Hysteresis	V <sub>H</sub>	1.8	_	_		_	0.25	_	٧	$V_{T}^{+} - V_{T}^{-}$
voltage		2.5	_	_		_	0.30	_	=	
		3.3	_	_		_	0.35	_	=	
		5.0	_	_	_	_	0.45	_	=	
On-state switch	R <sub>on</sub>	1.65	_	120	360	_	_	450	Ω	$V_{IN} = V_{CC}$ or GND
resistance		2.3	_	60	180	_	_	225	=	$V_{c} = V_{H}$
		3.0	_	50	150	_	_	190	_	$I_{T} = 1 \text{ mA}$
		4.5	_	40	75	_	_	100	_	
Peak on	R <sub>ON (P)</sub>	1.65	_	400	1100	_	_	1400	Ω	$V_{IN} = V_{CC}$ to GND
resistance		2.3	_	200	500	_	_	600	=	$V_{c} = V_{H}$
		3.0	_	90	180	_	_	225	=	$I_{\tau} = 1 \text{ mA}$
		4.5	_	50	100	_	_	125	_	
Difference of	$\Delta R_{on}$	1.65	_	40	120	_	_	160	Ω	$V_{IN} = V_{CC}$ to GND
on- state		2.3	_	20	30	_	_	40	=	$V_{c} = V_{H}$
resistance		3.0	_	10	20	_	_	30	_	$I_{\tau} = 1 \text{ mA}$
between switches		4.5	_	7	15	_	_	20	_	
Off-state switch leakage current	l <sub>s (OFF)</sub>	5.5	_	_	±0.1	_	_	±1.0	μА	$\begin{aligned} &V_{\text{IN}} = V_{\text{CC}}, \\ &V_{\text{OUT}} = GND \\ &\text{or } V_{\text{IN}} = GND, \\ &V_{\text{O}} = V_{\text{CC}}, V_{\text{C}} = V_{\text{IL}} \end{aligned}$
On-state switch leakage current	I <sub>s (ON)</sub>	5.5	_	_	±0.1	_	_	±1.0	μΑ	$V_{IN} = V_{CC}$ or GND $V_{C} = V_{IH}$
Input current	I <sub>IN</sub>	0 to 5.5	_	_	±0.1	_	_	±1.0	μΑ	V <sub>IN</sub> = 5.5 V or GND
Quiescent supply current	I <sub>cc</sub>	5.5	_	_	_	_	_	10	μΑ	$V_{IN} = V_{CC}$ or GND
Control input capacitance	C <sub>IC</sub>	_	_	3.5	_	_	_	_	pF	
Switch terminal capacitance	$C_{_{IN}/OUT}$	_	_	4.0	_	_	_	_	pF	
Feedthrough capacitance	C <sub>IN-OUT</sub>	_	_	0.5	_	_	_	_	pF	

## **Switching Characteristics**

•  $V_{cc} = 1.8 \pm 0.15 \text{ V}$ 

Item	Symbol	$T_a = 25^{\circ}C$		$T_a = -40 \text{ to } 85^{\circ}\text{C}$ U		Unit	Test	FROM	то	
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	4.0	13.0	_	19.0	ns	C <sub>∟</sub> = 15 pF	IN/OUT	OUT/IN
delay time	$t_{_{PHL}}$	_	11.0	23.0	_	29.0	_	C <sub>∟</sub> = 50 pF	or OUT/IN	or IN/OUT
Enable time	t <sub>zH</sub>	_	11.0	24.0	_	29.0	ns	C <sub>L</sub> = 15 pF	С	IN/OUT
	$\mathbf{t}_{\scriptscriptstyle ZL}$	_	18.0	44.0	_	51.0		C <sub>L</sub> = 50 pF		or OUT/IN
Disable time	$t_{\scriptscriptstyle{HZ}}$	_	11.0	21.0	_	29.0	ns	C <sub>L</sub> = 15 pF	С	IN/OUT
	$\mathbf{t}_{LZ}$	_	18.0	46.0	_	53.0	_	C <sub>L</sub> = 50 pF	_	or OUT/IN

•  $V_{cc} = 2.5 \pm 0.2 \text{ V}$ 

• • cc - 2.3	± 0.2 V									
Item	Symbol	$T_a = 2$	5°C		$T_a = -40$	) to 85°C	Unit	Test	FROM	то
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>		2.0	10.0	_	16.0	ns	C <sub>L</sub> = 15 pF	IN/OUT	OUT/IN
delay time	$t_{\scriptscriptstylePHL}$	_	5.0	12.0	_	18.0		C <sub>L</sub> = 50 pF	or OUT/IN	or IN/OUT
Enable time	t <sub>zH</sub>	_	6.0	15.0	_	20.0	ns	C <sub>L</sub> = 15 pF	С	IN/OUT
	$\mathbf{t}_{_{\mathrm{ZL}}}$	_	8.0	25.0	_	32.0		C <sub>L</sub> = 50 pF	_	or OUT/IN
Disable time	t <sub>HZ</sub>	_	7.0	15.0	_	23.0	ns	C <sub>L</sub> = 15 pF	С	IN/OUT
	$\mathbf{t}_{LZ}$	_	11.0	25.0	_	32.0	_	C <sub>L</sub> = 50 pF	_	or OUT/IN

•  $V_{cc} = 3.3 \pm 0.3 \text{ V}$ 

Item	Symbol	$T_a = 25^{\circ}C$		$T_a = -40 \text{ to } 85^{\circ}\text{C}$		Unit	Test	FROM	то	
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	1.5	6.0	_	10.0	ns	C <sub>L</sub> = 15 pF	IN/OUT	OUT/IN
delay time	$t_{_{PHL}}$	_	4.0	9.0	_	12.0	_	C <sub>L</sub> = 50 pF	or OUT/IN	or IN/OUT
Enable time	t <sub>zH</sub>	_	4.0	11.0	_	15.0	ns	C <sub>L</sub> = 15 pF	С	IN/OUT
	$\mathbf{t}_{_{\mathrm{ZL}}}$	_	6.0	18.0	_	22.0	_	C <sub>L</sub> = 50 pF	_	or OUT/IN
Disable time	t <sub>HZ</sub>		5.0	11.0	_	15.0	ns	C <sub>L</sub> = 15 pF	С	IN/OUT
	$\mathbf{t}_{LZ}$	_	8.0	18.0	_	22.0		C <sub>L</sub> = 50 pF	=	or OUT/IN

## **Switching Characteristics** (cont)

•  $V_{cc} = 5.0 \pm 0.5 \text{ V}$ 

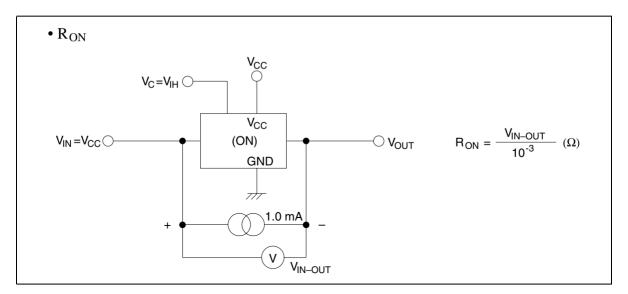
Item	Symbol	$T_a = 2$	25°C		$T_a = -40 \text{ to } 85^{\circ}\text{C}$		Unit	Test	FROM	то
		Min	Тур	Max	Min	Max	_	Conditions	(Input)	(Output)
Propagation	t <sub>PLH</sub>	_	1.0	4.0	_	7.0	ns	C <sub>∟</sub> = 15 pF	IN/OUT	OUT/IN
delay time	$t_{_{PHL}}$	_	3.0	6.0	_	8.0	_	C <sub>L</sub> = 50 pF	or OUT/IN	or IN/OUT
Enable time	t <sub>zH</sub>		3.0	7.0	_	10.0	ns	C <sub>L</sub> = 15 pF	С	IN/OUT
	$\mathbf{t}_{\scriptscriptstyle ZL}$	_	5.0	12.0		16.0	_	C <sub>∟</sub> = 50 pF	_	or OUT/IN
Disable time	t <sub>HZ</sub>		4.0	7.0	_	10.0	ns	C <sub>L</sub> = 15 pF	С	IN/OUT
	$\mathbf{t}_{LZ}$	_	6.0	12.0	_	16.0	_	C <sub>L</sub> = 50 pF	_	or OUT/IN

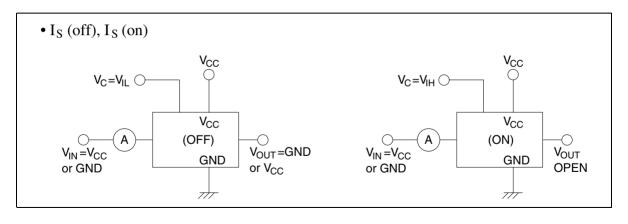
## **Operating Characteristics**

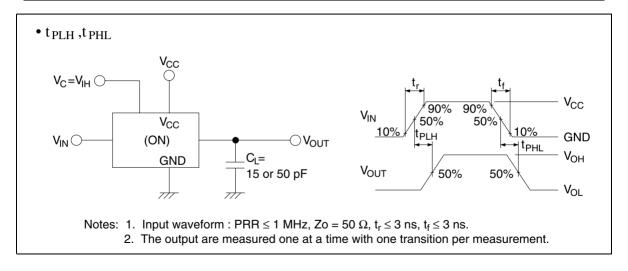
•  $C_L = 50 \text{ pF}$ 

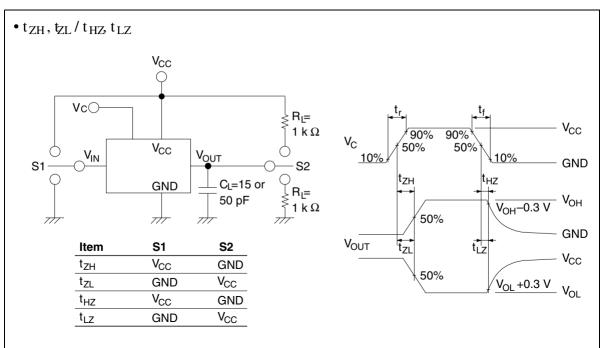
Item	Symbol	$V_{cc}(V)$	T <sub>a</sub> = 25	5°C		Unit	<b>Test Conditions</b>
			Min	Тур	Max		
Power dissipation capacitance	$C_{\scriptscriptstylePD}$	3.3	_	3.5	_	pF	f = 10 MHz
		5.0	_	4.0	_		

#### **Test Circuit**



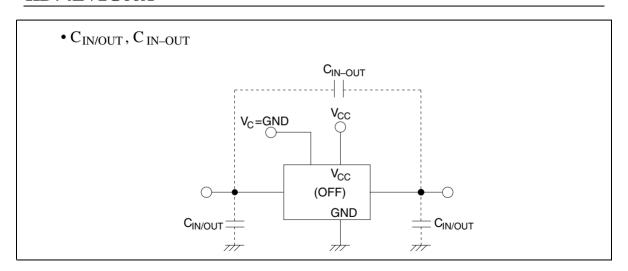




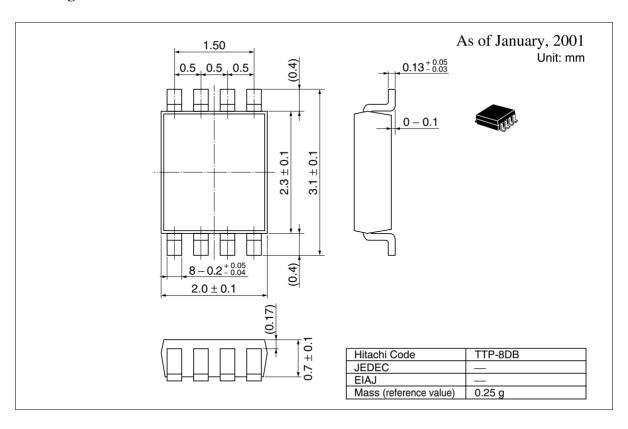


Notes: 1. Input waveform : PRR  $\leq$  1 MHz, Zo = 50  $\Omega$ ,  $t_r \leq$  3 ns,  $t_f \leq$  3 ns.

- 2. Waveform A is for an output with internal conditions such that the output is low except when disabled by the output control.
- 3. Waveform B is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. The output are measured one at a time with one transition per measurement.



## **Package Dimensions**



#### Disclaimer

- 1. Hitachi neither warrants nor grants licenses of any rights of Hitachi's or any third party's patent. copyright, trademark, or other intellectual property rights for information contained in this document. Hitachi bears no responsibility for problems that may arise with third party's rights, including intellectual property rights, in connection with use of the information contained in this document.
- 2. Products and product specifications may be subject to change without notice. Confirm that you have received the latest product standards or specifications before final design, purchase or use.
- 3. Hitachi makes every attempt to ensure that its products are of high quality and reliability. However, contact Hitachi's sales office before using the product in an application that demands especially high quality and reliability or where its failure or malfunction may directly threaten human life or cause risk of bodily injury, such as aerospace, aeronautics, nuclear power, combustion control, transportation, traffic, safety equipment or medical equipment for life support.
- 4. Design your application so that the product is used within the ranges guaranteed by Hitachi particularly for maximum rating, operating supply voltage range, heat radiation characteristics, installation conditions and other characteristics. Hitachi bears no responsibility for failure or damage when used beyond the guaranteed ranges. Even within the guaranteed ranges, consider normally foreseeable failure rates or failure modes in semiconductor devices and employ systemic measures such as failsafes, so that the equipment incorporating Hitachi product does not cause bodily injury, fire or other consequential damage due to operation of the Hitachi product.
- 5. This product is not designed to be radiation resistant.
- 6. No one is permitted to reproduce or duplicate, in any form, the whole or part of this document without written approval from Hitachi.
- 7. Contact Hitachi's sales office for any questions regarding this document or Hitachi semiconductor products.

#### Sales Offices

# IITACHI

Semiconductor & Integrated Circuits Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Tel: (03) 3270-2111 Fax: (03) 3270-5109

NorthAmerica http://semiconductor.hitachi.com/ Europe http://www.hitachi-eu.com/hel/ecg Asia http://sicapac.hitachi-asia.com Japan : http://www.hitachi.co.jp/Sicd/indx.htm

#### For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive San Jose, CA 95134 Tel: <1> (408) 433-1990 Maidenhead

Hitachi Europe Ltd. Electronic Components Group Whitebrook Park Lower Cookham Road Fax: <1>(408) 433-0223 Berkshire SL6 8YA, United Kingdom Fax: <65>-538-6933/538-3877

Tel: <44> (1628) 585000 Fax: <44> (1628) 585200

Hitachi Europe GmbH Electronic Components Group Dornacher Straße 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd. Hitachi Tower 16 Collyer Quay #20-00 Singapore 049318 Tel: <65>-538-6533/538-8577

URL: http://www.hitachi.com.sg Hitachi Asia Ltd (Taipei Branch Office)

4/F. No. 167. Tun Hwa North Road Hung-Kuo Building Taipei (105), Taiwan Tel: <886>-(2)-2718-3666

Fax: <886>-(2)-2718-8180 Telex: 23222 HAS-TP URL: http://www.hitachi.com.tw Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower World Finance Centre. Harbour City, Canton Road Tsim Sha Tsui, Kowloon

Hong Kong Tel: <852>-(2)-735-9218 Fax: <852>-(2)-730-0281

URL: http://semiconductor.hitachi.com.hk

Copyright © Hitachi, Ltd., 2001. All rights reserved. Printed in Japan. Colophon 4.0