



High-Efficiency Step-Up Current Regulator for LEDs

General Description

The MAX1698 and MAX1698A are efficient drivers for white or color LEDs. They are ideal for large LED backlit displays in PDAs and laptop computers. Numerous benefits include greater simplicity, lower cost, higher efficiency, longer bulb life, and greater reliability when compared to fluorescent (CCFL) and electroluminescent (EL) lamps.

The MAX1698/MAX1698A are switch-mode boost controllers in which LED current, rather than output voltage, is regulated. The devices drive series-connected LEDs with a controlled current that is measured with a typically 15Ω sense resistor, not an expensive fractional-ohm value. LED current control and dimming are accomplished with an adjust input (ADJ). For larger light output, multiple LED banks can be connected in parallel with up to 5W total output power.

The MAX1698A has a higher, and more accurately specified, CS current limit than the MAX1698 for higher-power applications of 5W or more. Both devices are supplied in a space-saving 10-pin μMAX package that occupies half the space of an 8-pin SO. An evaluation kit (MAX1698EVKIT) is available to speed designs.

Applications

Battery-Powered Backlight Applications
Backlight for LCD Panels
Notebook PCs
Handy Terminals
PDAs

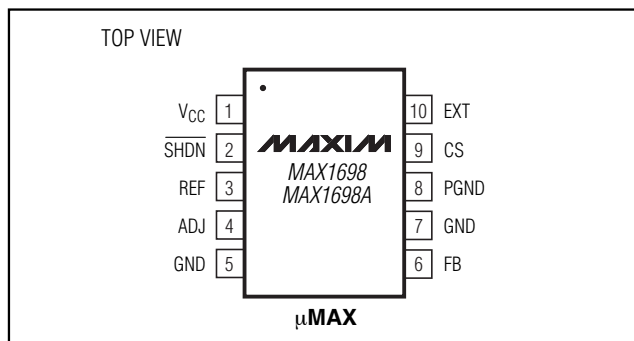
Features

- ◆ Over 90% Efficiency
- ◆ Lossless, Adjustable LED Brightness
- ◆ Space-Saving 10-Pin μMAX Package
- ◆ Simpler, Lower Cost, More Reliable Compared to CCFL or EL Backlights
- ◆ Up to 5W Output Power

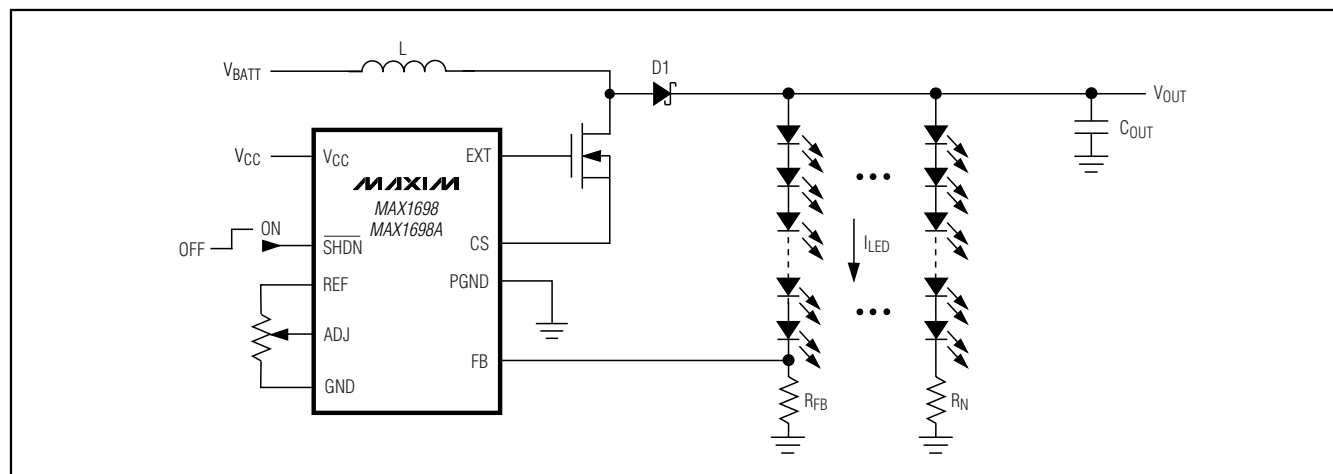
Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	PKG CODE
MAX1698EUB	-40°C to +85°C	10μMAX	U10-2
MAX1698AEUB	-40°C to +85°C	10μMAX	U10-2

Pin Configuration



Typical Operating Circuit



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ABSOLUTE MAXIMUM RATINGS

V_{CC}, $\overline{\text{SHDN}}$ to GND -0.3V to +6V
 EXT, FB, CS, ADJ, REF to GND -0.3V to (V_{CC} + 0.3V)
 GND to PGND $\pm 0.3\text{V}$
 Continuous Power Dissipation (T_A = +70°C)
 10-Pin μMAX (derate 5.6mW/°C above +70°C) 444mW

Operating Temperature Range -40°C to +85°C
 Junction Temperature +150°C
 Storage Temperature Range -65°C to +150°C
 Lead Temperature Range (soldering, 10s) +300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{CC} = +3.3V, T_A = 0°C to +85°C, unless otherwise noted. Typical values are at T_A = +25°C.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Supply Voltage	V _{CC}			2.7		5.5	V
Quiescent Supply Current	I _{CC}	V _{FB} = 0.3V			260	500	μA
Undervoltage Lockout		Rising edge		2.25	2.4	2.55	V
Undervoltage Hysteresis					80		mV
FB Regulation Voltage	V _{FB}	ADJ = REF		285	300	315	mV
		V _{ADJ} = 100mV		18	24	30	
CS Trip Current		FB = GND	MAX1698A	2.0	2.4	2.8	A
			MAX1698	0.8	1.5	2.5	
CS Input Current	I _{CS}					0.8	A _{RMS}
Minimum Off Time	t _{OFF(MIN)}	FB = GND, ADJ = REF		0.8	1.0	1.2	μs
Maximum On Time	t _{ON(MAX)}	FB = GND, ADJ = REF, CS = GND		10	15	20	μs
ADJ Start Threshold				30	50	70	mV
ADJ Input Range	V _{ADJ}			0.03		REF	V
FB Input Bias Current	I _{FB}	V _{FB} = 300mV		-15		15	nA
ADJ Input Bias Current	I _{ADJ}	ADJ = REF		-50		50	nA
Shutdown Supply Current		SHDN = GND			0.01	1	μA
REF Output Voltage	V _{REF}	I _{REF} = 0		1.20	1.25	1.30	V
REF Load Regulation	ΔV _{REF}	I _{REF} = 0 to 150μA			-2	-25	mV
REF Short-Circuit Current		REF = GND			0.45	1	mA
REF Power-Supply Rejection Ratio	PSRR	V _{CC} = 2.7V to 5.5V			+0.3	+2	mV/V
EXT Driver Sink/Source Current		V _{CC} = 5V, EXT = 2V			0.2		A
EXT Driver On-Resistance		V _{CC} = 5V				8	Ω
SHDN Input High Voltage	V _{IH}	V _{CC} = 2.7V to 5.5V		2			V
SHDN Input Low Voltage	V _{IL}	V _{CC} = 2.7V to 5.5V				0.8	V
SHDN Input Bias Current	I _{SHDN}	V _{CC} = 2.7V to 5.5V		-1		1	μA

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MAX1698/MAX1698A

ELECTRICAL CHARACTERISTICS

($V_{CC} = +3.3V$, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted.) (Note 1)

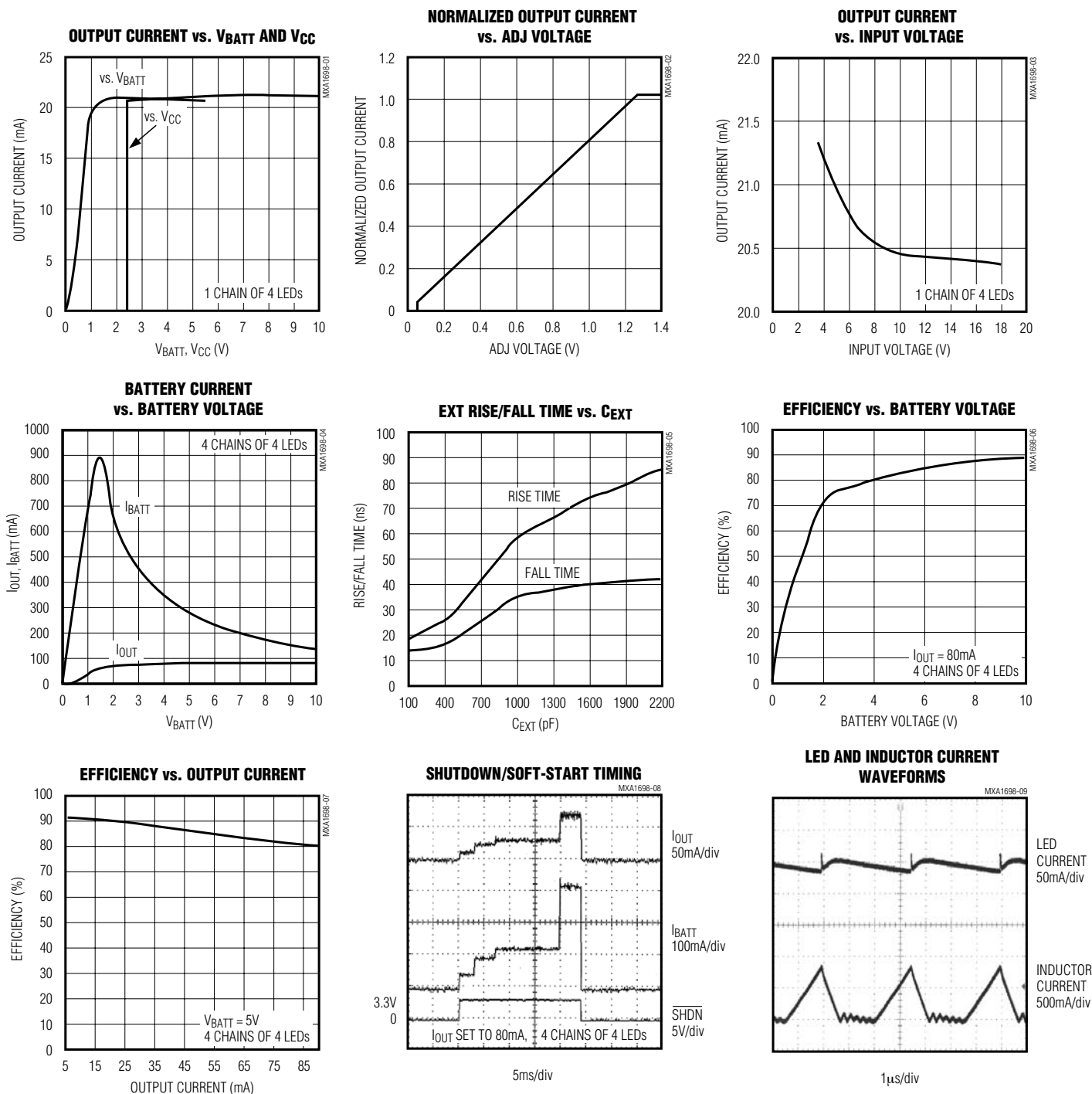
PARAMETER	SYMBOL	CONDITIONS		MIN	MAX	UNITS
Supply Voltage	V _{CC}			2.7	5.5	V
Quiescent Supply Current	I _{CC}	V _{FB} = 0.3V			500	μA
Undervoltage Lockout		Rising edge		2.20	2.65	V
FB Regulation Voltage	V _{FB}	ADJ = REF		280	320	mV
		V _{ADJ} = 100mV		13	35	
CS Trip Current		FB = GND	MAX1698A	2.0	3.0	A
			MAX1698	0.8	3.0	
Minimum Off Time	t _{OFF(MIN)}	FB = GND, ADJ = REF		0.6	1.4	μs
Maximum On Time	t _{ON(MAX)}	FB = GND, ADJ = REF, CS = GND		9	21	μs
ADJ Start Threshold				25	75	mV
FB Input Bias Current	I _{FB}	V _{FB} = 300mV		-20	20	nA
ADJ Input Bias Current	I _{ADJ}	ADJ = REF		-50	50	nA
Shutdown Supply Current		SHDN = GND			1	μA
REF Output Voltage	V _{REF}	I _{REF} = 0		1.17	1.33	V
REF Load Regulation	ΔV _{REF}	I _{REF} = 0 to 150μA			-30	mV
REF Short-Circuit Current		REF = GND			1	mA
REF Power-Supply Rejection Ratio	PSRR	V _{CC} = 2.7V to 5.5V			+2	mV/V
EXT On-Resistance		V _{CC} = 5V			8	Ω
SHDN Input High Voltage	V _{IH}	V _{CC} = 2.7V to 5.5V		2		V
SHDN Input Low Voltage	V _{IL}	V _{CC} = 2.7V to 5.5V			0.8	V
SHDN Input Bias Current	I _{SHDN}	V _{CC} = 2.7V to 5.5V		-1	1	μA

Note 1: Specifications to $-40^{\circ}C$ are guaranteed by design, not production tested.

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Typical Operating Characteristics

(Circuit of Figure 2, $V_{CC} = V_{BATT} = 3.3V$, $V_{ADJ} = V_{REF}$, $T_A = +25^\circ C$, unless otherwise noted.)



High-Efficiency Step-Up Current Regulator for LEDs

Pin Description

PIN	NAME	FUNCTION
1	V _{CC}	IC Supply Voltage Input. Power for internal circuitry. Input range is 2.7V to 5.5V.
2	$\overline{\text{SHDN}}$	Active-Low Shutdown Input. In shutdown, the MOSFET turns off, but a current path still exists between the input and output. The minimum forward voltage of the LED array must exceed the maximum V _{BATT} to ensure that the LEDs are off in shutdown.
3	REF	1.25V Reference Output. Capable of sourcing 150μA for external loads. This pin is internally compensated. Do not connect any bypass capacitors at REF.
4	ADJ	Adjust Input. Allows dynamic adjustment of the output current. FB regulates to 300mV when ADJ = REF.
5, 7	GND	Ground
6	FB	Feedback Input. Connect to the external LED current-sense feedback resistor.
8	PGND	Power Ground
9	CS	FET Current-Sense Input
10	EXT	Gate Driver Output

MAX1698/MAX1698A

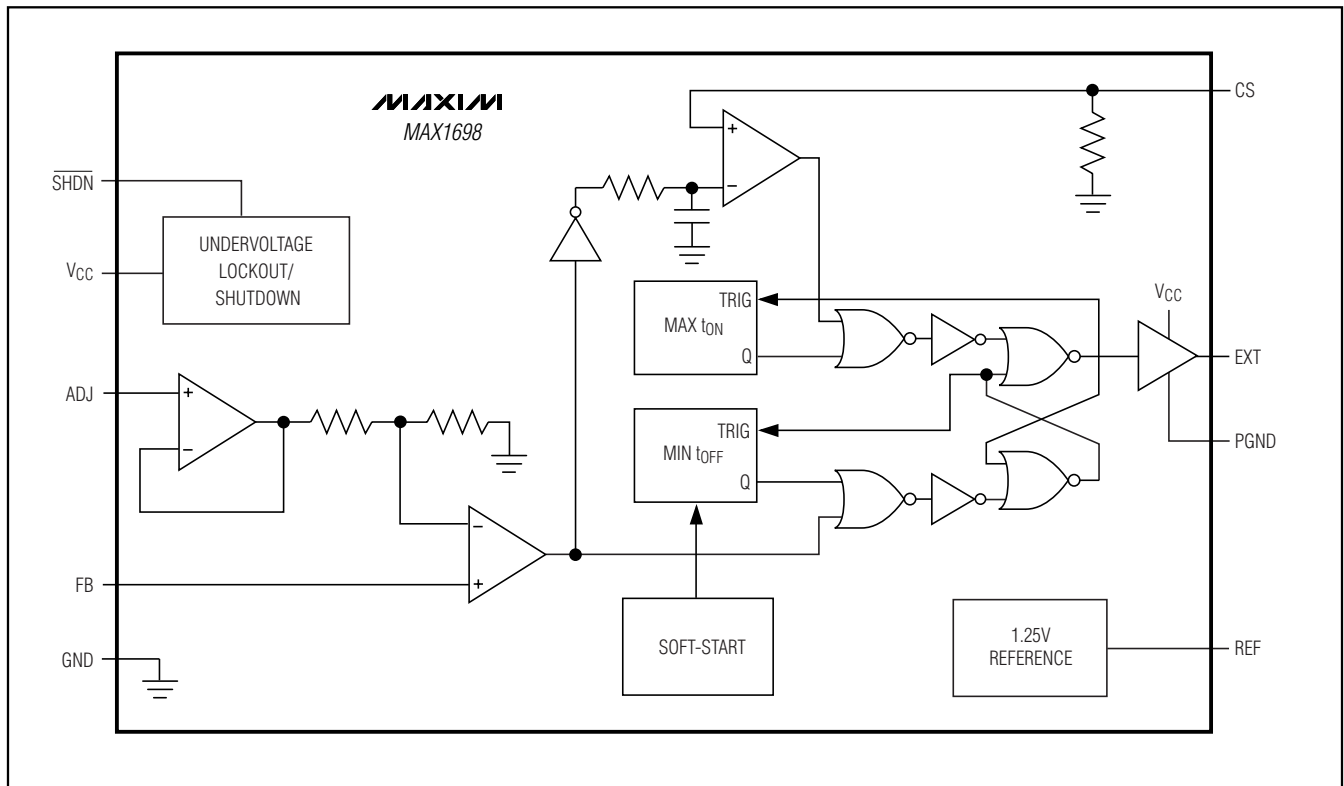


Figure 1. Functional Diagram

MAX1698/MAX1698A



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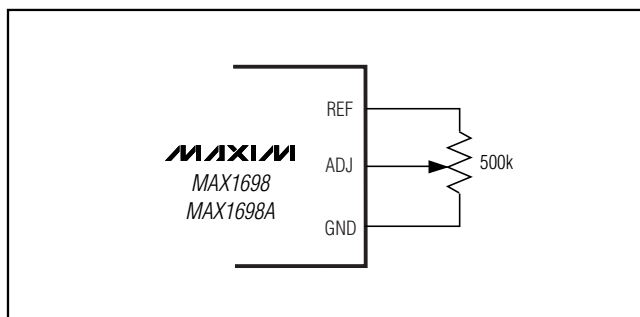


Figure 3. Adjusting LED Current

shows the standard method of setting the ADJ voltage. Use the following equation to determine I_{LED} :

$$I_{LED} = \frac{V_{ADJ}}{4.16 \cdot R_{FB}}$$

where V_{ADJ} is the voltage at ADJ. Note that ADJ voltages below 50mV turn the LEDs off.

Inductor Selection

Choose an inductor with low DC resistance (in the neighborhood of 100mΩ) to minimize losses. A typical inductance value for L is 10μH; however, values from 3.3μH to 100μH can also be used. Higher inductor values reduce the MAX1698's switching frequency. The typical operating frequency is given by:

$$f = \frac{0.67 \cdot V_{BATT}}{L}$$

The MAX1698/MAX1698A limit peak inductor current to 1.5A, but also contains a control loop that reduces inductor current as a function of output power. For a given output power, the required inductor peak current rating is approximately set by:

$$I_L(PEAK) = 1.0 \cdot P_{OUT}$$

where P_{OUT} is the output power to all LED banks in watts and $I_L(PEAK)$ is in amperes.

Capacitor Selection

The exact value of output capacitance is not critical. Typical values for the output capacitor are 0.1μF to 10μF. Larger values help reduce output ripple at the expense of size and higher cost.

The requirements of the input capacitor depend on the type of the input voltage source. However, in many applications, the same capacitor type and value are used for both the input and output capacitors.

Transistor Selection

The MAX1698/MAX1698A drive an external N-channel MOSFET. Since the gate drive voltage is derived from V_{CC} , best performance is achieved with low-threshold NFETs that specify on-resistance with gate-source voltages (V_{GS}) at the voltage supplied at V_{CC} or less. For best results, minimize the FET's $R_{DS(ON)}$. The external NFET's maximum drain-to-source voltage ($V_{DS(MAX)}$) must exceed the output voltage.

Catch Diode (D1) Selection

The MAX1698/MAX1698As' high-switching frequency demands a high-speed rectifier. Schottky diodes are recommended for most applications, due to their fast recovery time and low forward-voltage drop. Ensure that the diode's average and peak current ratings exceed the average output current and peak inductor current, respectively. In addition, the diode's reverse breakdown voltage must exceed V_{OUT} . For output voltages exceeding 40V, high-speed silicon rectifiers may be required for their higher breakdown voltages.

Zener Diode

For applications requiring open-circuit protection if one of the LEDs in the primary chain opens, add a zener diode as shown in Figure 2. The zener diode protects the MOSFET and output capacitor if the current feedback signal is lost. The zener voltage should exceed the maximum forward voltage of the LED network by at least 2V.

Applications Information

PCB Layout

Due to fast switching waveforms and high-current paths, careful PCB layout is required. Protoboards and wire-wrap boards should not be used for evaluation. An EV kit (MAX1698EVKIT) is available to aid most designs.

When laying out a board, minimize trace lengths to CS, the inductor, diode, input capacitor, and output capacitor. Keep traces short, direct, and wide. Keep noisy traces, such as the inductor's traces, away from FB. V_{CC} 's bypass capacitor should be placed as close to the IC as possible.

Refer to the MAX1698 EV kit for an example of proper layout.

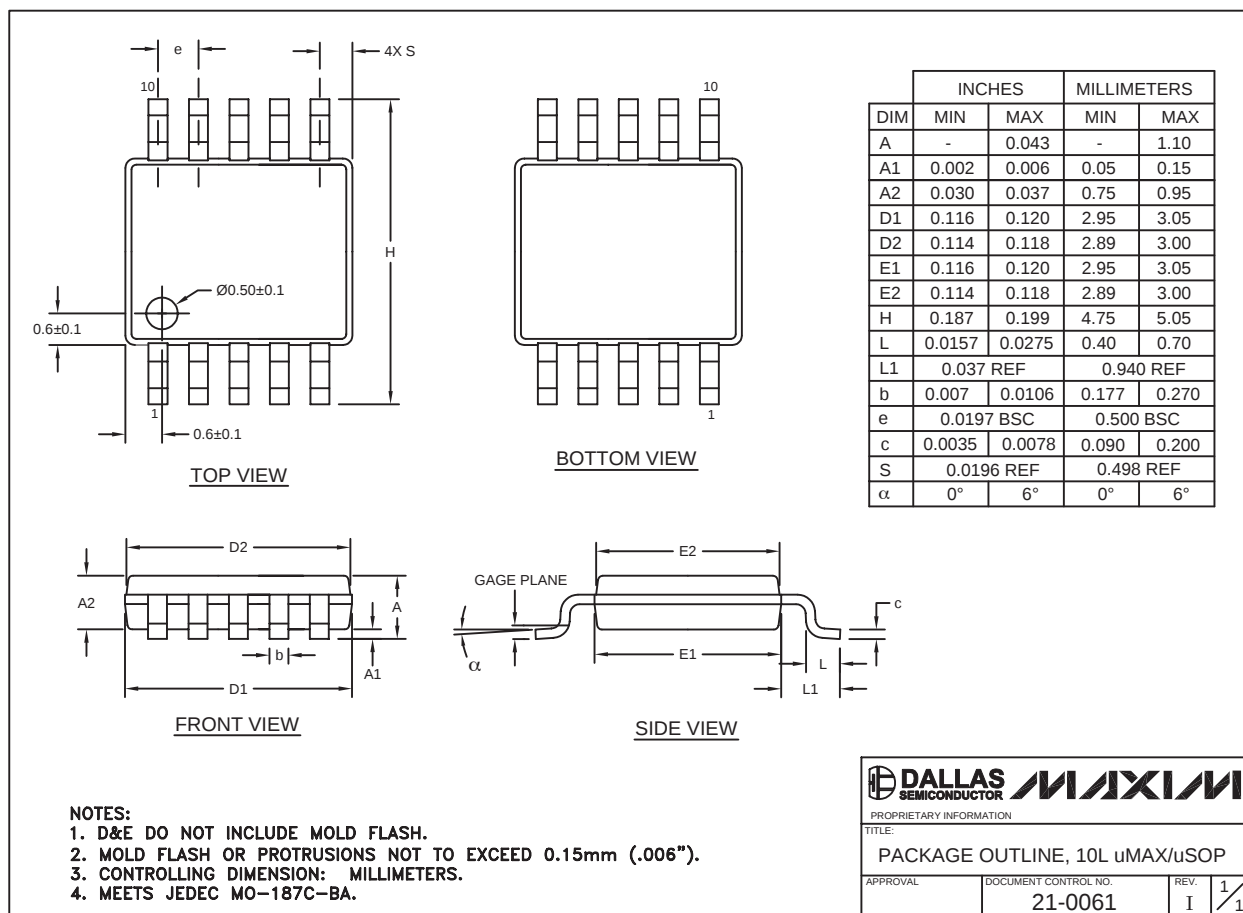
Chip Information

TRANSISTOR COUNT: 2180

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Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)



Note: The MAX1698 does not have an exposed pad.

Revision History

Pages changed at Rev 2: 1, 2, 8

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