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## ProcessorPM Development Kit

Get started integrating multiple power management ICs fast with the ProcessorPM Development Kit. The kit is a versatile, ready to use hardware platform for evaluating and designing with ProcessorPM power management device. The kit is based on a 2.5" x 2" evaluation board that features the [ProcessorPM - POWR605](#) device in a lead-free **-pin QFN** package, a [Power Manager II - POWR6AT6](#), evaluation circuits that emulate a power supply bus and processor interface, and an expansion header.

To help you get started quickly, the kit includes a pre-configured processor support demonstration design that will support hundreds of **microprocessor**, **DSP**, **ASSP**, or **ASIC** power management scenarios. The demo integrates three key support functions for a processor: **voltage supervisor**, **watchdog timer** (WDT), and **reset generator**. The board is controlled with switches and pushbuttons. And a slide potentiometer emulates brown-out conditions or 2.5V supply rail. A pin header provides access to IOs of the ProcessorPM and the POWR6AT6. You may extend or modify the pre-configured demo using [PAC-Designer®](#) software.

The board can be modified to support programming with Lattice [ISP Download Cables](#) with a parallel PC connecti and demonstrate power supply margin/trim scenarios with the POWR6AT6.



### Development Kit Contents

The ProcessorPM Evaluation Board features the following:

- Pre-Configured Processor Support Demo
- ProcessorPM-POWR605
- Power Manager II POWR6AT6
- 3.3V, 2.5V, and 1.8V Supply Rails
- LEDs
- Slide Potentiometer
- 2x14 Expansion Header
- USB Mini Jack Socket (Program/Power)
- 2 Push Buttons
- 4-Bit DIP Switch

### Ordering Part Number

PACPOWR605-P-EVN  
**\$49 -- Order Now**

JTAG and I2C Header Landings  
RoHS-compliant packaging and process

USB connector cable

QuickSTART Guide

### ProcessorPM Development Kit Resources

	Download
<b>Windows Hardware Drivers</b> USB drivers and interface support for the ProcessorPM Evaluation Board.	<a href="#">Windows Hardware Drivers</a>
<b>ProcessorPM Development Kit Documentation</b> Complete documentation including the development kit QuickSTART and User's Guide.	<a href="#">QuickSTART Guide</a>  <a href="#">User's Guide</a>
<b>Demo Applications</b> Click on a link to download demos.  <b>Processor Support and Voltage Monitoring Demo Source</b>  This demo includes the PAC-Designer project source for the default ProcessorPM Development Kit processor support demonstration. It programs the ProcessorPM-POWR605 to monitor up to 6 supply rails, selectable watchdog timer period, and optional result pulse stretch. The project source for the ispPAC-POWR6AT6 is also included.	<a href="#">Demo Source</a>
<b>PAC-Designer Software</b> To modify a demo or download your own designs into the ProcessorPM Evaluation Board requires PAC-Designer software. To program the ProcessorPM Evaluation Board requires <a href="#">ispVM System Device Programming Software</a> .	<a href="#">PAC-Designer for Windows</a>
<b>Schematic Design File</b> OrCAD Capture (.dsn) format schematics for the ProcessorPM Evaluation Board.	<a href="#">Capture DSN</a>
<b>PCB Artwork Files</b> Gerber format PCB artwork for the ProcessorPM Evaluation Board.	<a href="#">PCB Artwork</a>