

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
12V	3.8mΩ @ V _{GS} = 4.5V	70A
	5.1mΩ @ V _{GS} = 2.5V	55A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

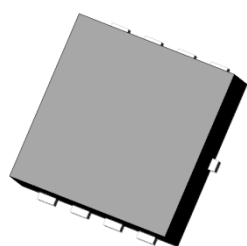
- Power Management Functions
- DC-DC Converters
- Battery

Features

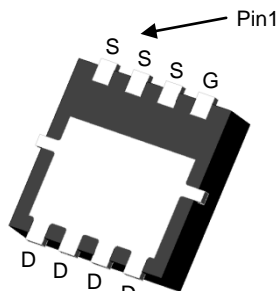
- Low R_{DS(ON)} – Ensures On-State Losses are Minimized
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- Occupies just 33% of the Board Area Occupied by SO-8 Enabling Smaller End Product
- ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**

Mechanical Data

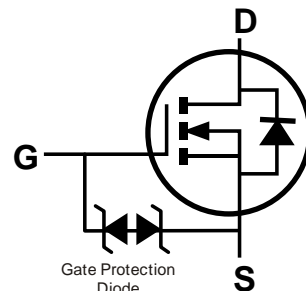
- Case: PowerDI[®] 3333-8 (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.072 grams (Approximate)



Top View



Bottom View



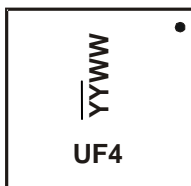
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN1004UFV-7	PowerDI3333-8 (Type UX)	2,000/Tape & Reel
DMN1004UFV-13	PowerDI3333-8 (Type UX)	3,000/Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



UF4 = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 16 = 2016)
 WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	12	V
Gate-Source Voltage	V _{GSS}	±8	V
Continuous Drain Current, V _{GS} = 4.5V (Note 7)	I _D	70	A
		50	
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)	I _{DM}	80	A
Maximum Continuous Body Diode Forward Current (Note 7)	I _S	70	A
Avalanche Current, L = 0.1mH (Note 8)	I _{AS}	34	A
Avalanche Energy, L = 0.1mH (Note 8)	E _{AS}	60	mJ

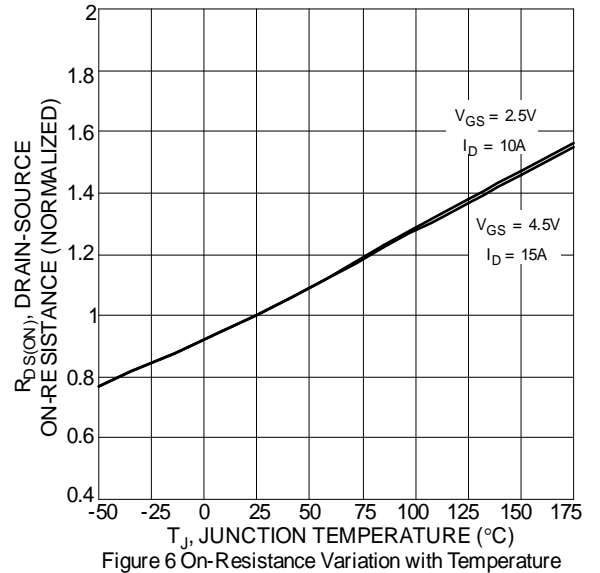
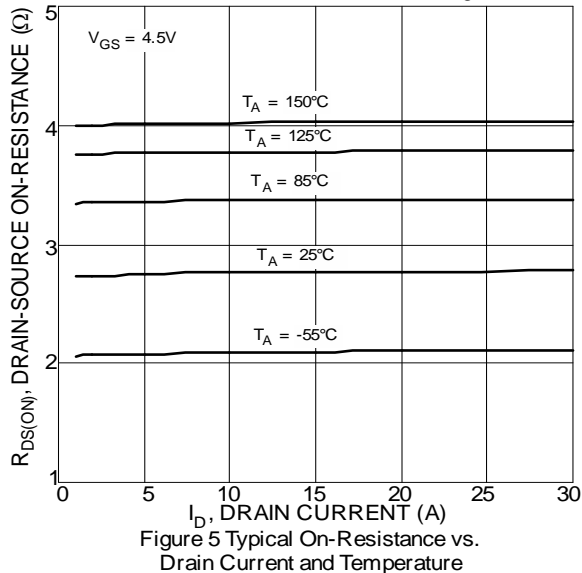
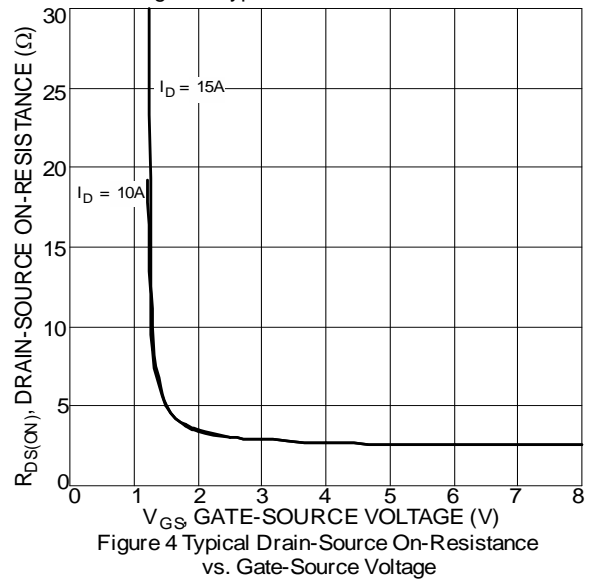
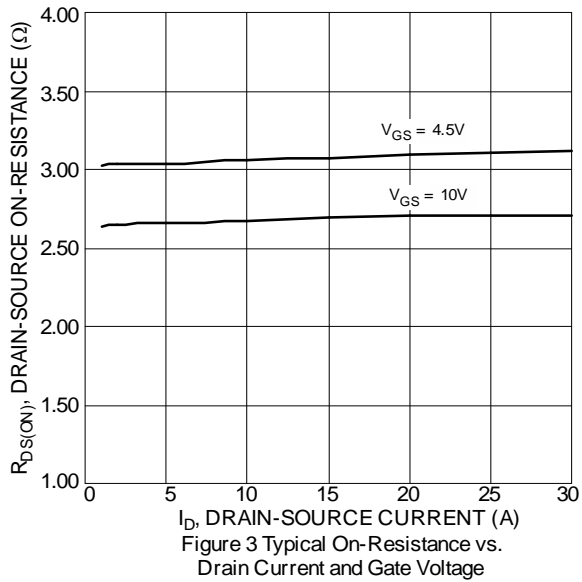
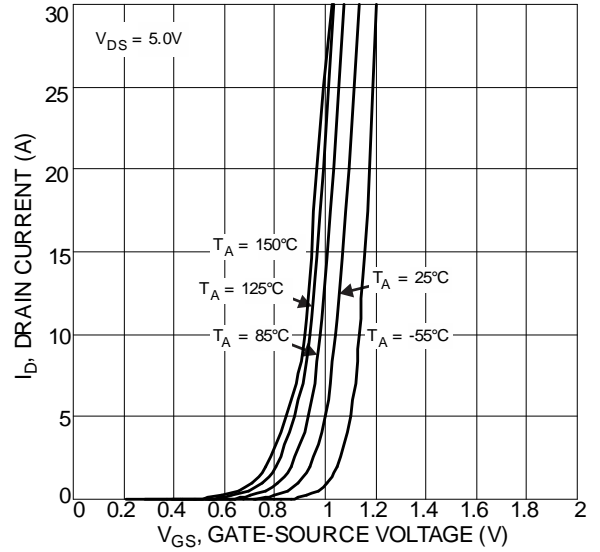
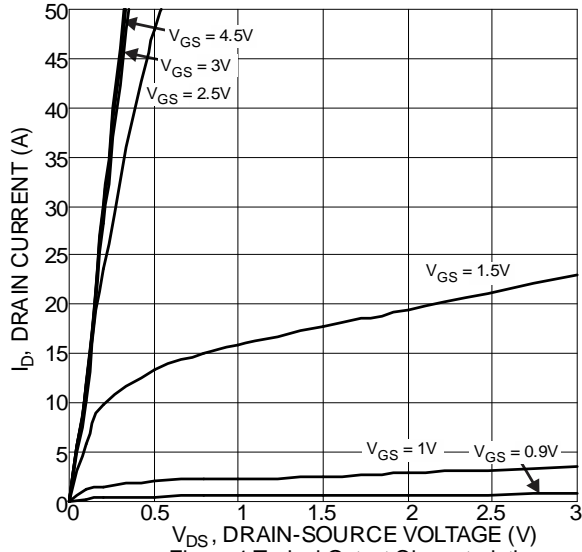
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

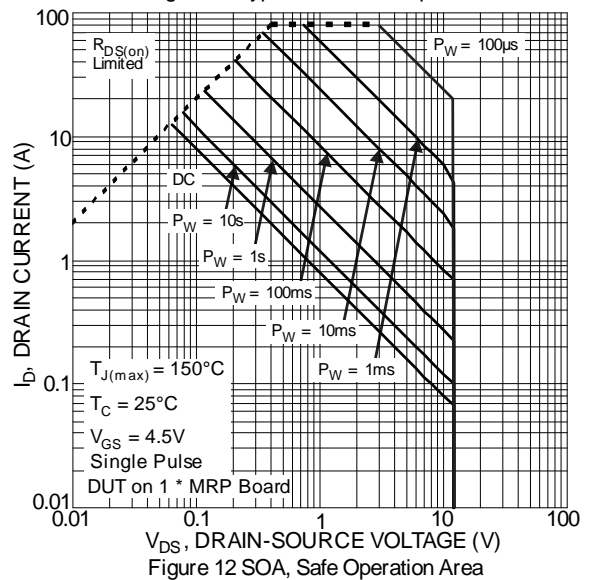
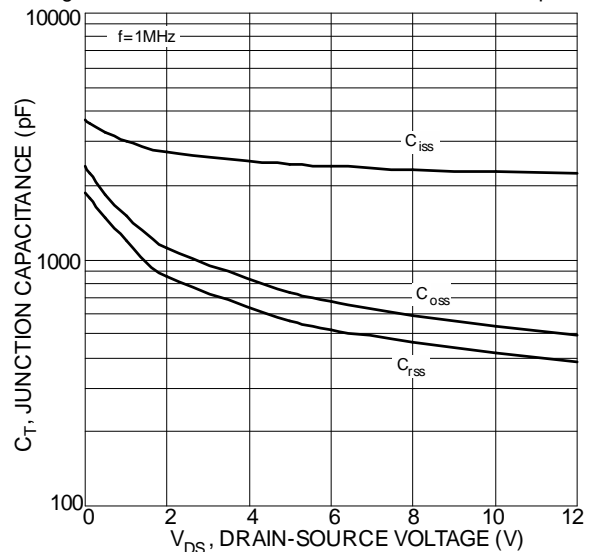
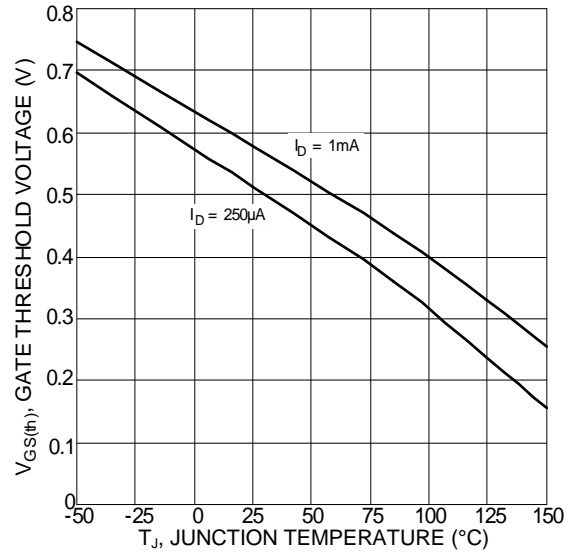
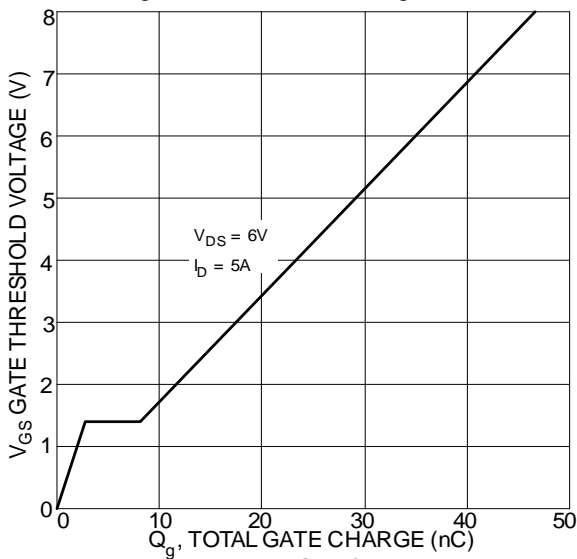
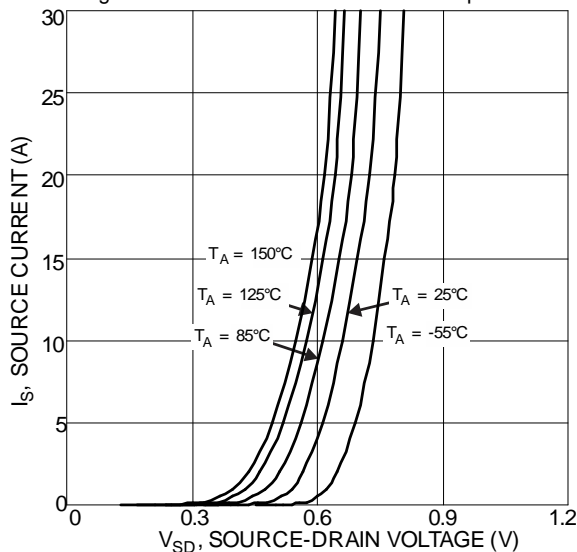
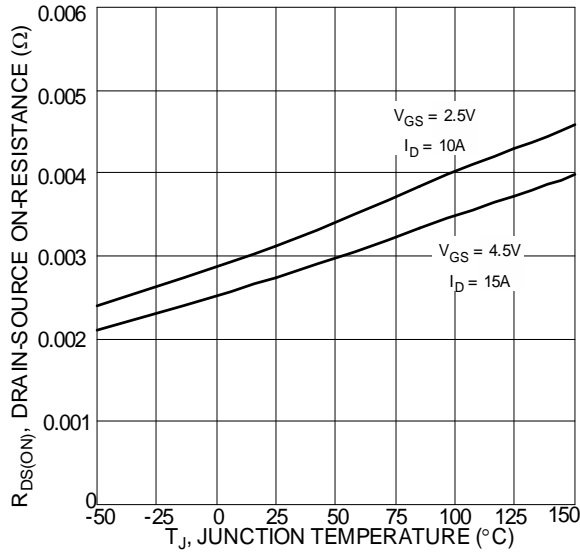
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	0.9	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	134	°C/W
Total Power Dissipation (Note 6)	P _D	1.9	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	66	°C/W
Thermal Resistance, Junction to Case (Note 7)	R _{θJC}	3.4	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

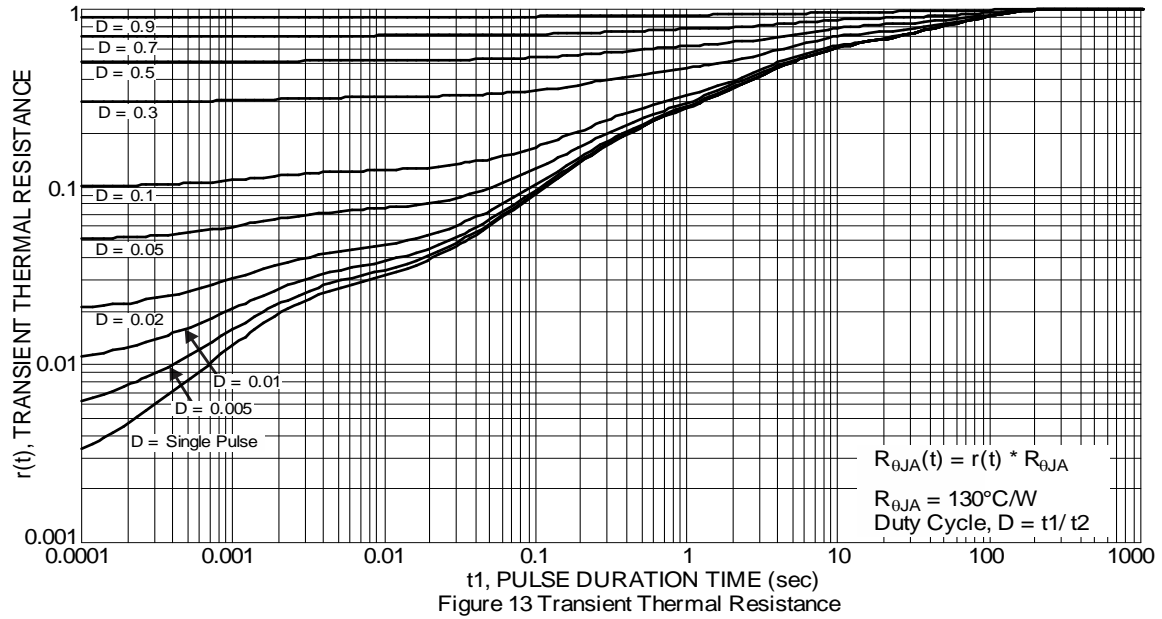
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)						
Drain-Source Breakdown Voltage	BV _{DSS}	12	—	—	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	µA	V _{DS} = 9.6V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±10	µA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	V _{GS(TH)}	0.3	—	1.0	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	2.8	3.8	mΩ	V _{GS} = 4.5V, I _D = 15A
		—	3.2	5.1		V _{GS} = 2.5V, I _D = 10A
Diode Forward Voltage	V _{SD}	—	0.75	1.2	V	V _{GS} = 0V, I _S = 3.2A
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	C _{iss}	—	2,385	—	pF	V _{DS} = 6V, V _{GS} = 0V, f = 1MHz
Output Capacitance	C _{oss}	—	678	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	520	—	pF	
Gate Resistance	R _G	—	2.2	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _G	—	26	—	nC	V _{DS} = 6V, I _D = 10A
Total Gate Charge (V _{GS} = 8V)	Q _G	—	47	—	nC	
Gate-Source Charge	Q _{GS}	—	2.8	—	nC	
Gate-Drain Charge	Q _{GD}	—	5.3	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	5.3	—	ns	V _{DD} = 6V, V _{GS} = 4.5V, R _G = 1Ω, I _D = 5A
Turn-On Rise Time	t _R	—	10.7	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	31.6	—	ns	
Turn-Off Fall Time	t _F	—	16.9	—	ns	I _F = 2A, di/dt = 100A/µs
Reverse Recovery Time	t _{RR}	—	24.3	—	ns	
Reverse Recovery Charge	Q _{RR}	—	7.4	—	nC	

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.



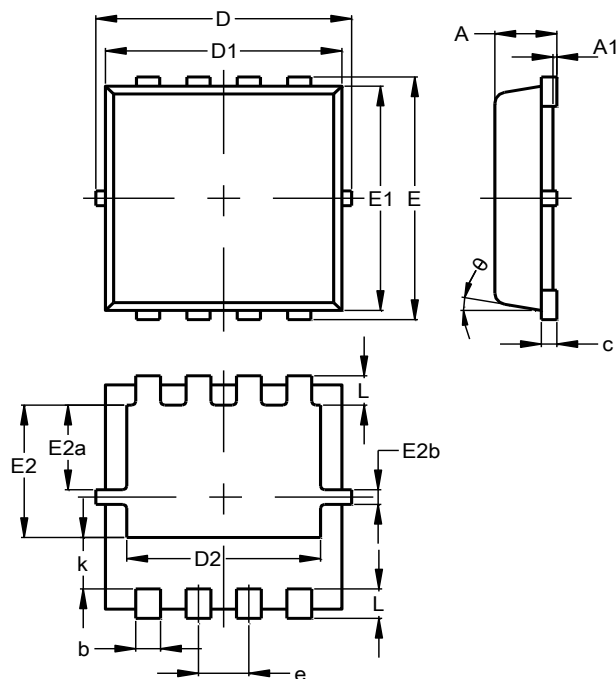




Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UX)

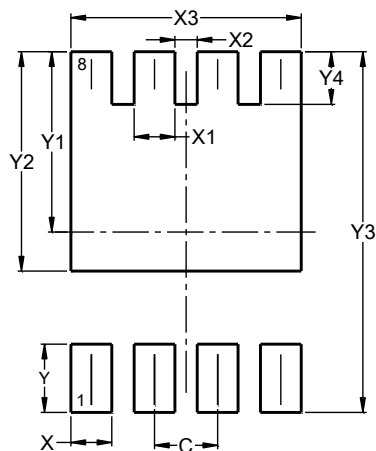


PowerDI3333-8 (Type UX)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	--
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	2.30	2.70	2.50
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	1.60	2.00	1.80
E2a	0.95	1.35	1.15
E2b	0.10	0.30	0.20
e	0.65 BSC		
k	0.50	0.90	0.70
L	0.30	0.50	0.40
θ	0°	12°	10°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540

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