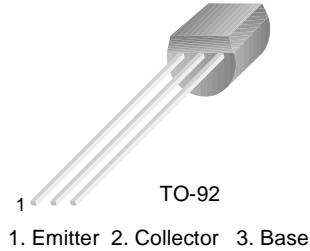


2N5308

NPN Darlington Transistor

- This device is designed for applications requiring extremely high current gain at currents to 1.0A.
- Sourced from process 05.
- See MPSA14 for characteristics.



Absolute Maximum Ratings * $T_A=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Value | Units |
|----------------|--|------------|-------|
| V_{CEO} | Collector-Emitter Voltage | 40 | V |
| V_{CBO} | Collector-Base Voltage | 40 | V |
| V_{EBO} | Emitter-Base Voltage | 12 | V |
| I_C | Collector Current - Continuous | 1.2 | A |
| T_J, T_{STG} | Operating and Storage Junction Temperature Range | -55 ~ +150 | °C |

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

- These ratings are based on a maximum junction temperature of 150 degrees C.
- These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Electrical Characteristics $T_A=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Test Condition | Min. | Typ. | Max. | Units |
|-------------------------------------|---------------------------------------|---|-----------------|------|-----------|---------------|
| Off Characteristics | | | | | | |
| $V_{(BR)CEO}$ | Collector-Emitter Breakdown Voltage * | $I_C = 10\text{mA}, I_B = 0$ | 40 | | | V |
| $V_{(BR)CBO}$ | Collector-Base Breakdown Voltage | $I_C = 0.1\mu\text{A}, I_E = 0$ | 40 | | | V |
| $V_{(BR)EBO}$ | Emitter-Base Breakdown Voltage | $I_E = 0.1\mu\text{A}, I_C = 0$ | 12 | | | V |
| I_{CBO} | Collector Cutoff Current | $V_{CB} = 40\text{V}, I_E = 0$ $V_{CB} = 40\text{V}, I_E = 0, T_a = 100^\circ\text{C}$ | | | 0.1 20 | μA |
| I_{EBO} | Emitter Cutoff Current | $V_{EB} = 12\text{V}, I_C = 0$ | | | 0.1 | μA |
| On Characteristics * | | | | | | |
| h_{FE} | DC Current Gain | $V_{CE} = 5.0\text{V}, I_C = 2.0\text{mA}$ $V_{CE} = 5.0\text{V}, I_C = 100\text{mA}$ | 7,000 20,000 | | 70,000 | |
| $V_{CE(\text{sat})}$ | Collector-Emitter Saturation Voltage | $I_C = 200\text{mA}, I_B = 0.2\text{mA}$ | | | 1.4 | V |
| $V_{BE(\text{sat})}$ | Base-Emitter Saturation Voltage | $I_C = 200\text{mA}, I_B = 0.2\text{mA}$ | | | 1.6 | V |
| $V_{BE(\text{on})}$ | Base-Emitter On Voltage | $I_C = 200\text{mA}, V_{CE} = 5.0\text{V}$ | | | 1.5 | V |
| Small Signal Characteristics | | | | | | |
| C_{cb} | Collector-Base Capacitance | $V_{CB} = 10\text{V}, f = 1.0\text{MHz}$ | | | 10 | pF |
| h_{fe} | Small-Signal Current Gain | $I_C = 2.0\text{mA}, V_{CE} = 5.0\text{V}, f = 1.0\text{KHz}$ $I_C = 2.0\text{mA}, V_{CE} = 5.0\text{V}, f = 10\text{MHz}$ | 7,000 6.0 | | | |

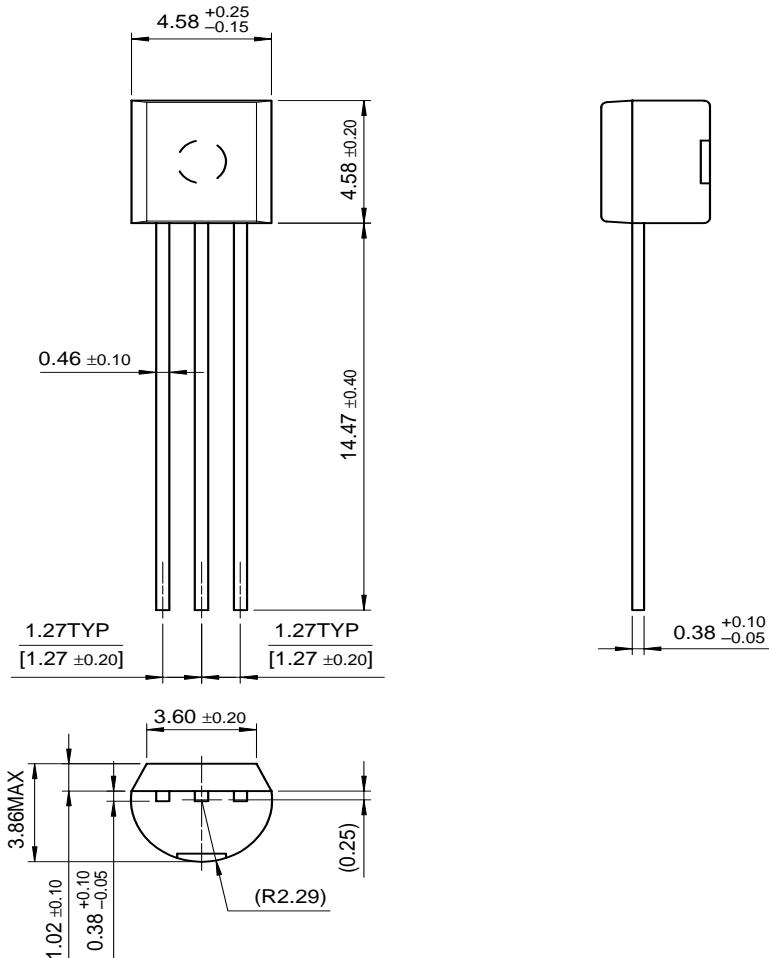
* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2.0\%$

Thermal Characteristics $T_A=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Max. | Units |
|-----------------------|---|------------|----------------------------|
| P_D | Total Device Dissipation Derate above 25°C | 625 5.0 | mW mW/ $^\circ\text{C}$ |
| $R_{\theta\text{JC}}$ | Thermal Resistance, Junction to Case | 83.3 | $^\circ\text{C}/\text{W}$ |
| $R_{\theta\text{JA}}$ | Thermal Resistance, Junction to Ambient | 200 | $^\circ\text{C}/\text{W}$ |

Package Dimensions

TO-92



Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

| | | | | |
|--|----------------------------------|---------------------------------|----------------------------------|------------------------------|
| ACE TM | FACT TM | ImpliedDisconnect TM | PACMAN TM | SPM TM |
| ActiveArray TM | FACT Quiet Series TM | ISOPLANAR TM | POP TM | Stealth TM |
| Bottomless TM | FAST [®] | LittleFET TM | Power247 TM | SuperSOT ^{TM-3} |
| CoolFET TM | FAST TM | MicroFET TM | PowerTrench [®] | SuperSOT ^{TM-6} |
| CROSSVOLT TM | FRFET TM | MicroPak TM | QFET TM | SuperSOT ^{TM-8} |
| DOME TM | GlobalOptoisolator TM | MICROWIRE TM | QS TM | SyncFET TM |
| EcoSPARK TM | GTO TM | MSX TM | QT Optoelectronics TM | TinyLogic TM |
| E ² CMOS TM | HiSeC TM | MSXPro TM | Quiet Series TM | TruTranslation TM |
| EnSigna TM | I ² C TM | OCX TM | RapidConfigure TM | UHC TM |
| Across the board. Around the world. TM The Power Franchise TM | | OCXPro TM | RapidConnect TM | UltraFET [®] |
| | | OPTOLOGIC [®] | SILENT SWITCHER [®] | VCX TM |
| | | OPTOPLANAR TM | SMART START TM | |

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

| Datasheet Identification | Product Status | Definition |
|--------------------------|------------------------|---|
| Advance Information | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. |
| Preliminary | First Production | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| Obsolete | Not In Production | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only. |

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

[Fairchild Semiconductor](#):

[2N5308_Q](#)