TOSHIBA Multichip Discrete Device

HN7G07FU

Power Management Switch Applications, Inverter Circuit Applications, Driver Circuit Applications and Interface Circuit Applications

• Combining transistor and BRT reduces the parts count, enabling the design of more compact equipment with a simpler system configuration.

Q1: 2SC5376F equivalent Q2: RN1115F equivalent

Q1 Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	15	V
Collector-emitter voltage	V _{CEO}	12	٧
Emitter-base voltage	V _{EBO}	5	٧
Collector current	IC	500	mA
Base current	ΙΒ	50	mA

Q2 Absolute Maximum Ratings (Ta = 25°C)

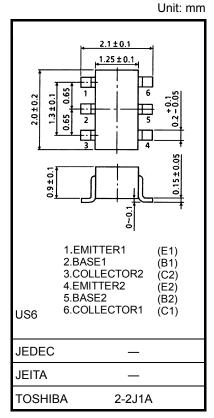
Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	10	V
Collector current	IC	100	mA

Q1, Q2 Common Ratings (Ta = 25°C)

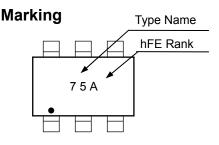
Characteristic	Symbol	Rating	Unit
Collector power dissipation	P _C *	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

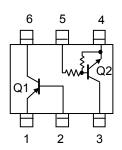
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.0068 g (typ.)



Equivalent Circuit (top view)



^{*:} Total rating. 130 mW per element should not be exceeded.

Q1 Electrical Characteristics (Ta = 25°C)

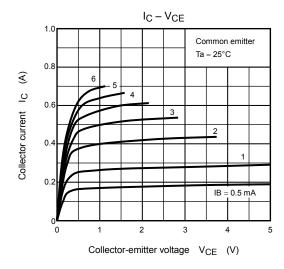
Character	ristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff current		I _{CBO}	$V_{CB} = 15 \text{ V}, I_{E} = 0$	_	_	100	nA
Emitter cutoff current		I _{EBO}	V _{EB} = 5 V, I _C = 0	_	_	100	nA
DC current gain		h _{FE} **	V _{CE} = 2 V, I _C = 10 mA	300	_	1000	
Collector-emitter saturation voltage		V _{CE} (sat) (1)	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$	_	15	30	- mV
		V _{CE} (sat) (2)	I _C = 200 mA, I _B = 10 mA	_	110	250	
Base-emitter saturation	voltage	V _{BE} (sat)	I _C = 200 mA, I _B = 10 mA	_	0.87	1.2	V
Transition frequency		f _T	V _{CE} = 2 V, I _C = 10 mA	_	130	_	MHz
Collector output capacit	tance	C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	4.2	_	pF
	Turn-on time	t _{on}	OUTPUT	_	85	_	ns
	Storage time	t _{stg}	0 V		170	_	ns
	Fall time	t _f	$V_{BB} = 3 \text{ V}$ Duty cycle $\leq 2\%$ $IB1 = IB2 = 5 \text{ mA}$		40	_	ns

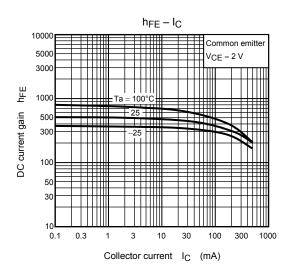
**: h_{FE} Classification A:300~600, B:500~1000

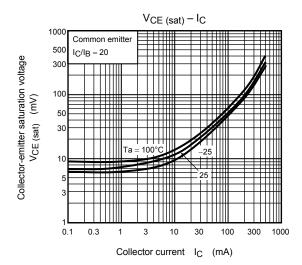
Q2 Electrical Characteristics (Ta = 25°C)

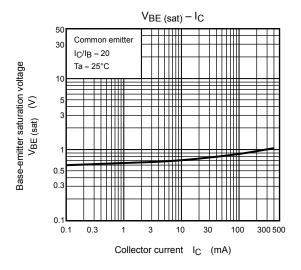
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = 50 \text{ V}, I_E = 0$	_	_	100	nA
	I _{CEO}	V _{CE} = 50 V, I _E = 0	_	_	500	nA
Emitter cutoff current	I _{EBO}	V _{EB} = 6 V, I _C = 0	0.37	_	0.71	mA
DC current gain	h _{FE}	V _{CE} = 5 V, I _C = 10 mA	50	_	_	
Collector-emitter saturation voltage	V _{CE (sat)}	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$	_	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	V _{CE} = 0.2 V, I _C = 5 mA	0.7	_	2.5	V
Input voltage (OFF)	V _{I(OFF)}	V _{CE} = 5 V, I _C = 0.1 mA	0.3	_	1.0	V
Transition frequency	f _T	V _{CE} = 10 V, I _C = 5 mA	_	250	_	MHz
Collector output capacitance	C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	3	_	pF
Input resistor	R1	_	1.54	2.2	2.86	kΩ
Resistor ratio	R1/R2	_	_	0.22	_	

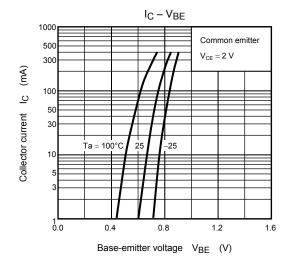
Q1

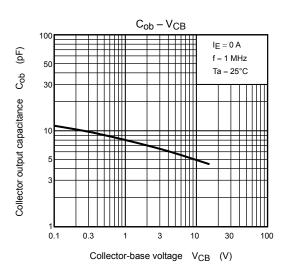




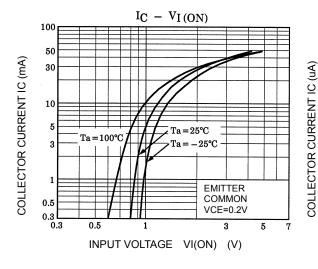


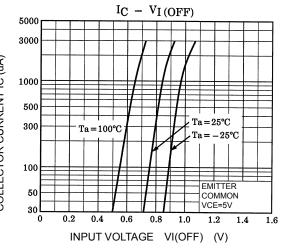


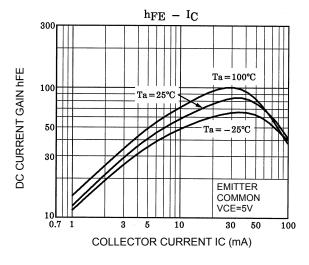


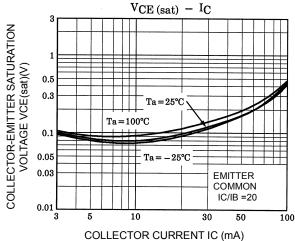


Q2

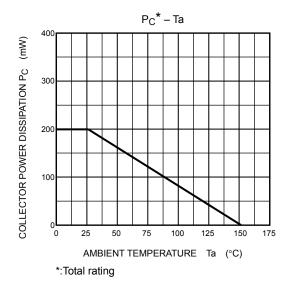








Q1, Q2 common



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20070701-EN GENERAL

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