



Micro Commercial Components

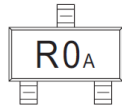


Micro Commercial Components
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SI3400A

Features

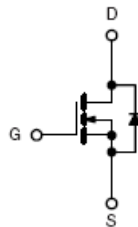
- Halogen free available upon request by adding suffix "-HF"
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- High dense cell design for extremely low RDS(ON)
- Exceptional on-resistance and maximum DC current capability
- Marking Code:



Maximum Ratings @ 25°C Unless Otherwise Specified

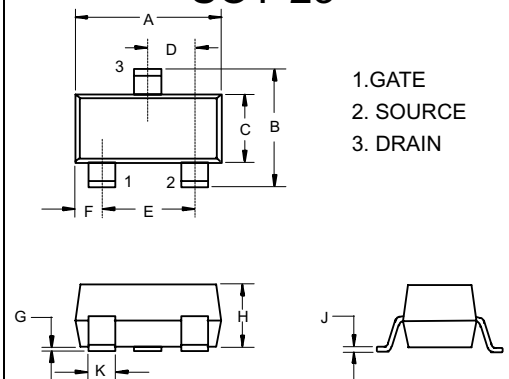
Symbol	Parameter	Rating	Unit
V_{DS}	Drain-source Voltage	30	V
I_D	Continuous Drain Current	5.8	A
I_{DM}	Pulsed Drain Current (note1)	30	A
V_{GS}	Gate-source Voltage	± 12	V
P_D	Total Power Dissipation	0.40	W
$R_{\theta JA}$	Thermal Resistance Junction to Ambient	313	$^{\circ}\text{C}/\text{W}$
T_J	Operating Junction Temperature	-55 to +150	$^{\circ}\text{C}$
T_{STG}	Storage Temperature	-55 to +150	$^{\circ}\text{C}$

Internal Block Diagram



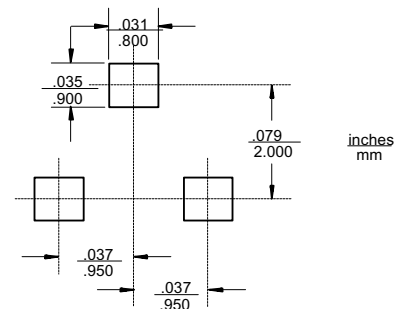
N-Channel Enhancement Mode Field Effect Transistor

SOT-23



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.110	.120	2.80	3.04	
B	.083	.104	2.10	2.64	
C	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
E	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
H	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

Suggested Solder Pad Layout



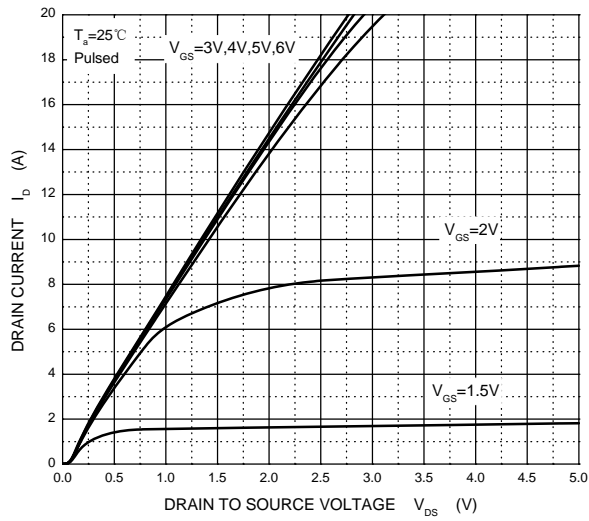
ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 24V, V_{GS} = 0V$			1	μA
Gate-source leakage current	I_{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$			± 100	nA
On characteristics (note 3)						
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 5.8A$		29	32	m Ω
		$V_{GS} = 4.5V, I_D = 5A$		32	38	m Ω
		$V_{GS} = 2.5V, I_D = 4A$		40	45	m Ω
Forward tranconductance	g_{FS}	$V_{DS} = 5V, I_D = 5A$	8			S
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.7		1.4	V
Dynamic Characteristics (note 4,5)						
Input capacitance	C_{iss}	$V_{DS} = 15V, V_{GS} = 0V, f = 1MHz$			1155	pF
Output capacitance	C_{oss}			108		pF
Reverse transfer capacitance	C_{rss}			84		pF
Gate resistance	R_g	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$			3.6	Ω
Switching Characteristics (note 4,5)						
Turn-on delay time	$t_{d(on)}$	$V_{GS} = 10V, V_{DS} = 15V,$ $R_L = 2.7\Omega, R_{GEN} = 3\Omega$			5	ns
Turn-on rise time	t_r				7	ns
Turn-off delay time	$t_{d(off)}$				40	ns
Turn-off fall time	t_f				6	ns
Drain-source diode characteristics and maximum ratings						
Diode forward voltage (note 3)	V_{SD}	$I_S = 1A, V_{GS} = 0V$			1	V

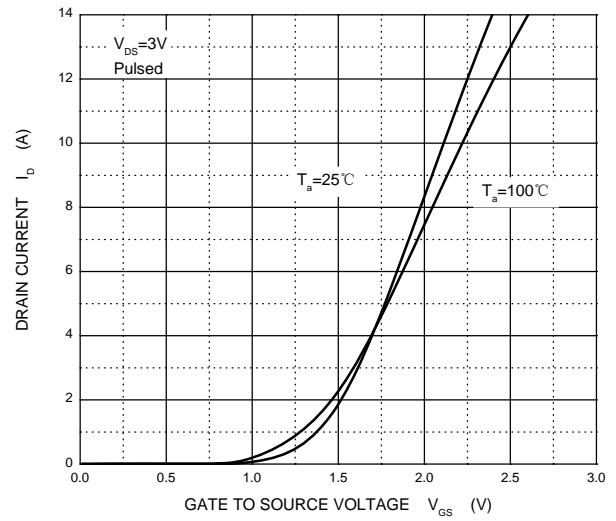
Note :

1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t < 5$ sec.
3. Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production testing.

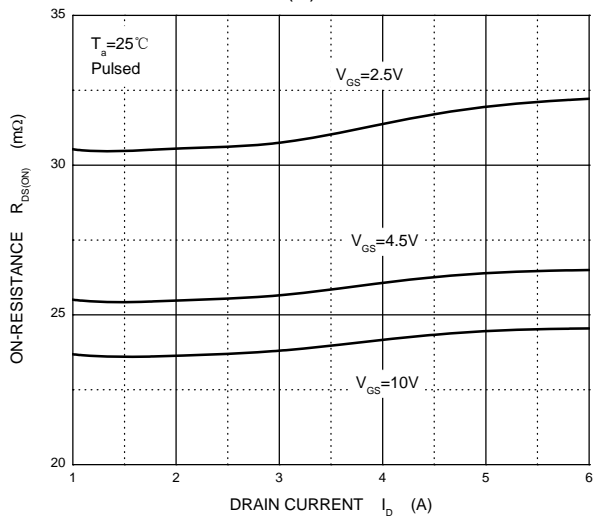
Output Characteristics



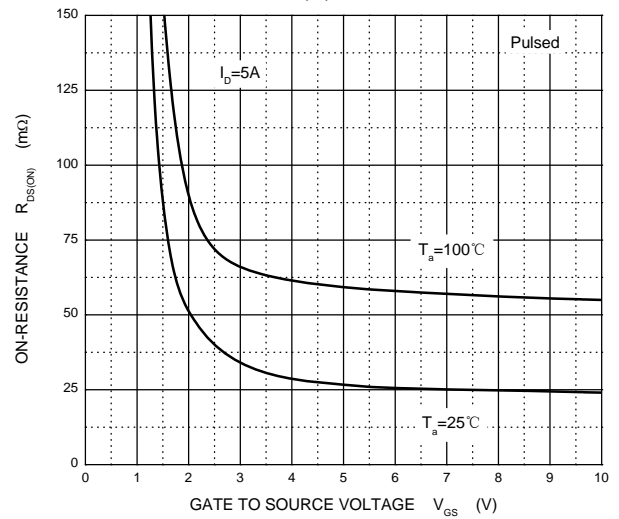
Transfer Characteristics



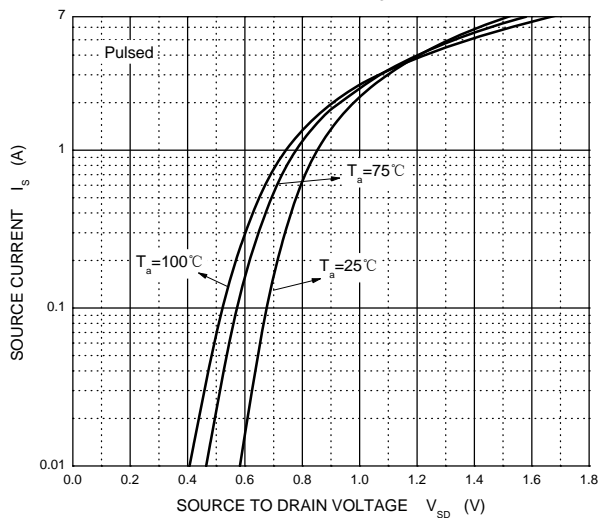
$R_{DS(ON)}$ — I_D



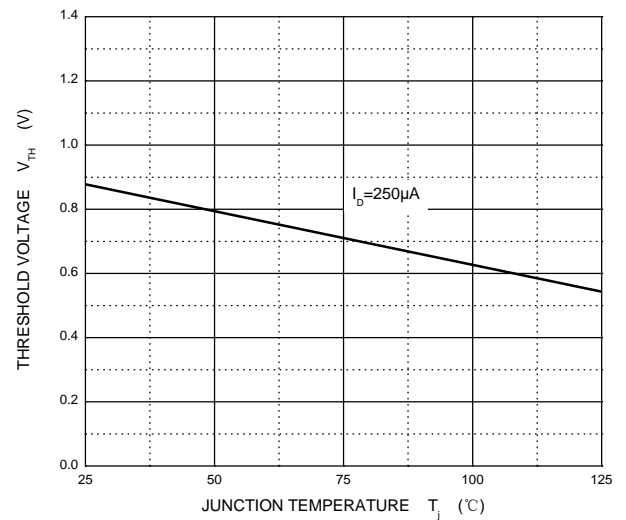
$R_{DS(ON)}$ — V_{GS}



I_S — V_{SD}



Threshold Voltage



Ordering Information :

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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