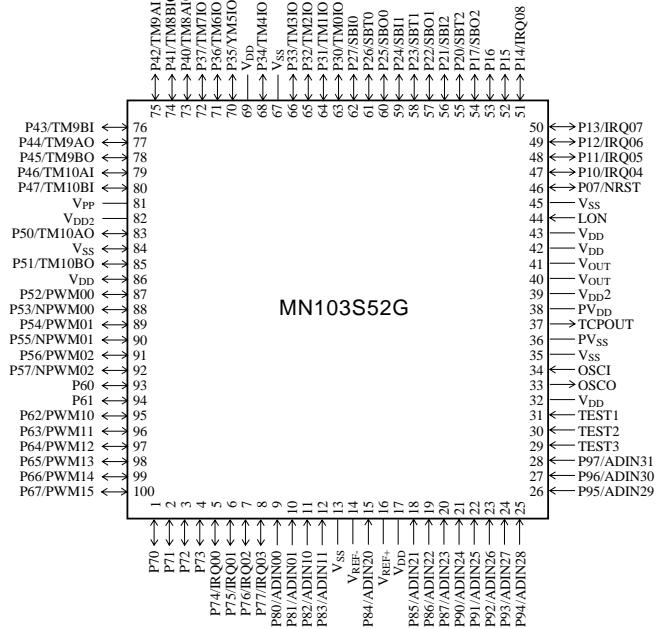


□ MN103S52G

Type	MN103S52G (under development)		
Internal ROM	128 K-byte		
Internal RAM	4 K-byte SRAM		
Package	QFP100-P-1818B *Lead-free		
Minimum Instruction Execution Time	25 ns (at 3.0 V to 3.6 V, 40 MHz)		
Interrupts	<ul style="list-style-type: none">• 9 external interrupts• 35 internal interrupts (watch dog timer, timer, serial I/F, PWM, system error)		
Timer Counter	<p>Eight 8-bit timers Interval timer, Event counter, Cascading</p> <p>Four 16-bit timers Interval timer, Event counter, PWM output, Double buffer</p>		
Serial Interface	UART / synchronous interfaces selective: 3		
I/O Pins	I/O	56	exclusive: 6, selective: 50
	Input	16	selective: 16
A/D Inputs	10-bit, 2 inputs: 2, 12 inputs: 1		
PWM	3-phase PWM output 16-bit counter, triangular waveform or jigsaw waveform dead time setup		
Electrical Characteristics	T.B.D.		

Pin Assignment



QFP100-P-1818B *Lead-free

Support Tool

In-circuit Emulator	PX-ICE101C / D + PX-PRB101D08-QFP100-P-1818B-M	
Flash Memory Built-in Type	Type ROM RAM Minimum instruction execution time Package	MN103SF52G (under development) 128 K-byte 4 K-byte SRAM 25 ns (at 3.0 V to 3.6 V, 40 MHz) QFP100-P-1818B *Lead-free

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