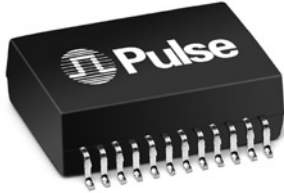


T1/E1/CEPT/ISDN-PRI INTERFACE MODULES

Four Transformer Modules for Dual SMT T1/E1 Ports, Extended Temperature Range



- ⦿ Optimized for enhanced EMC performance
- ⦿ Extended temperature range
- ⦿ SMT Dual Port package contains transformers with Optional Common Mode Chokes on both transmit and receive channels
- ⦿ Models matched to leading transceiver ICs
- ⦿ Patented Interlock Base construction for high reliability
- ⦿ Recognized to UL 1950, and EN 60950 – approval pending

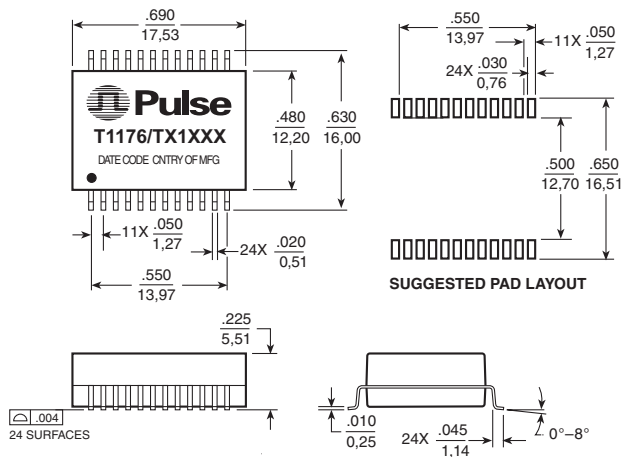
Electrical Specifications @ 25°C

Part Number		Turns Ratio ² (Pri:Sec ± 2%)		Secondary OCL @ 25°C (mH MIN)	LL (μH MAX)	Cw/w (pF MAX)	DCR Pri (Ω MAX)	Package/ Schematic	Primary Pins	
w/CMC	wo/CMC	Transmit	Receive						Transmit	Receive
EXTENDED TEMPERATURE RANGE MODELS ¹ – OPERATING TEMPERATURE -40°C TO +85°C										
T1176	TX1321	1CT:2.4CT	1CT:1CT	1.20	0.50	35	0.80	WAT/2	1-3 & 10-12	21-19 & 18-16
TX1192	TX1322	1CT:2.42CT	1CT:2.42CT	1.20	0.50	25	0.80	WAT/1	4-6 & 10-12	1-3 & 7-9
TX1193	TX1323	1CT:2CT	1CT:1CT	1.20	0.50	35	0.80	WAT/1	4-6 & 10-12	24-22 & 18-16
TX1194	TX1324	1CT:1.15CT	1CT:1.15CT	1.20	0.50	35	0.80	WAT/1	4-6 & 10-12	1-3 & 7-9
TX1195	TX1325	1CT:1.41CT	1CT:1.41CT	1.20	0.50	30	0.80	WAT/2	1-3 & 10-12	21-19 & 18-16
TX1196	TX1326	1CT:2CT	1CT:2CT	1.20	0.50	25	0.80	WAT/1	4-6 & 10-12	1-3 & 7-9
TX1197	TX1327	1CT:2CT	1CT:2CT	1.20	0.50	25	0.80	WAT/2	1-3 & 10-12	21-19 & 18-16
TX1198	TX1328	1CT:2.3CT	1CT:1CT	1.20	0.50	35	0.80	WAT/1	4-6 & 10-12	24-22 & 18-16
TX1199	TX1329	1CT:1.36CT	1CT:2CT	1.20	0.50	35	0.80	WAT/1	4-6 & 10-12	24-22 & 18-16
TX1300	TX1330	1CT:1.36CT	1CT:1CT	1.20	0.50	35	0.80	WAT/1	4-6 & 10-12	24-22 & 18-16
TX1301	TX1331	1CT:1.15CT	1CT:2CT	1.20	0.50	35	0.80	WAT/1	4-6 & 10-12	1-3 & 7-9

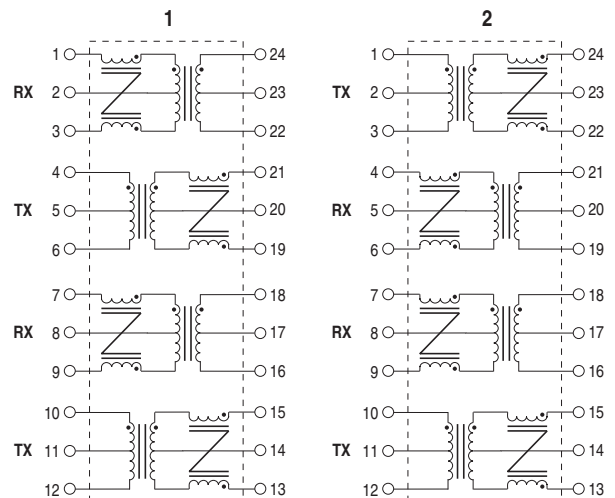
Mechanical

Schematics

WAT



Weight 4.0 grams
Tape & Reel250/reel
Tube30/tube



Dimensions: $\frac{\text{Inches}}{\text{mm}}$

Unless otherwise specified, all tolerances are $\pm \frac{.010}{0.25}$

T1/E1/CEPT/ISDN-PRI INTERFACE MODULES

Four Transformer Modules for Dual SMT T1/E1 Ports, Extended Temperature Range



Notes From Tables

- Extended Temperature Range Models** — For extended temperature range transformers (-40°C to +85°C operating temperature range), OCL (Open Circuit Inductance) is specified at both -40°C and +25°C. At -40°C, OCL is 600 μ H minimum. All other parameters are specified at +25°C only.
- Turns ratio is specified primary: secondary (CT = Center Tap).
- Standard packaging for the surface mount package is anti-static tubes. Optional Tape & Reel packaging can be ordered by adding a "T" suffix to the part number, (i.e. T1176T).

Application Notes

- Flammability** — Materials used in the products are recognized as UL94-VO approved. Products meet the requirements of IEC 695-2-2 (Needle Flame Test).
- Balance Characteristics** — The transformers meet the requirements for longitudinal balance of FCC part 68.
- Common Mode Rejection Ratio** — the CMRR for all transformers is better than 50 dB at 1 MHz. A typical test circuit is shown below.
- Crosstalk Attenuation** — In the packages which contain transmit and receive transformers side by side, sufficient crosstalk attenuation is achieved by the inherent characteristics of the toroid cores as well as by their proper positioning. The crosstalk attenuation is typically 65 dB or better. This result was established with the test circuit shown below.
- Return Loss** — ITU-T G.703 and European national regulatory documents specify minimum return loss levels. The transformers will allow these limits to be complied within the situations where they are applicable.

Frequency	50-100 KHz	100 KHz-2 MHz	2-3 MHz
Return Loss			
XMIT	9 dB	15 dB	11 dB
RCV	12 dB	18 dB	14 dB

- General Information** — The transformers are specifically designed for use in 1.544 Mbps (T1), 2.048 Mbps (CEPT) and ISDN Primary rate (PRI) interface applications. They are matched to the majority of the line interface transceiver ICs currently available. Use of the proper transformer allows the interface circuit to comply with ITU-T G.703 and other standards regarding pulse waveform, return loss, and balance.

For More Information :

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