



May 2014

FQA46N15

N-Channel QFET[®] MOSFET

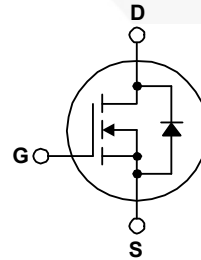
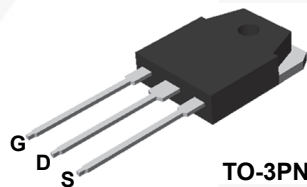
150 V, 50 A, 42 mΩ

Features

- 50 A, 150 V, $R_{DS(on)}$ = 42 mΩ (Max) @ V_{GS} = 10 V, I_D = 25 A
- Low Gate Charge (Typ. 85 nC)
- Low C_{rss} (Typ. 100 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating

Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	FQA46N15	Unit
V_{DSS}	Drain-Source Voltage	150	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$)	50	A
	- Continuous ($T_C = 100^\circ\text{C}$)	35.3	A
I_{DM}	Drain Current - Pulsed (Note 1)	200	A
V_{GSS}	Gate-Source Voltage	± 25	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	650	mJ
I_{AR}	Avalanche Current (Note 1)	50	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	25	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	6.0	V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	250	W
	- Derate above 25°C	1.67	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds	300	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	FQA46N15	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.6	$^\circ\text{C}/\text{W}$
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.24	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	$^\circ\text{C}/\text{W}$

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQA46N15	FQA46N15	TO-3PN	Tube	N/A	N/A	30 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	150	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	--	0.16	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 150 V, V _{GS} = 0 V	--	--	1	μA
		V _{DS} = 120 V, T _C = 150°C	--	--	10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 25 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -25 V, V _{DS} = 0 V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0	--	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 25A	--	0.033	0.042	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 25A	--	36	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	--	2500	3250	pF
C _{oss}	Output Capacitance		--	520	670	pF
C _{rss}	Reverse Transfer Capacitance		--	100	130	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 75 V, I _D = 45.6A, R _G = 25 Ω (Note 4)	--	35	80	ns
t _r	Turn-On Rise Time		--	320	650	ns
t _{d(off)}	Turn-Off Delay Time		--	210	430	ns
t _f	Turn-Off Fall Time		--	200	410	ns
Q _g	Total Gate Charge	V _{DS} = 120 V, I _D = 45.6A, V _{GS} = 10 V (Note 4)	--	85	110	nC
Q _{gs}	Gate-Source Charge		--	15	--	nC
Q _{gd}	Gate-Drain Charge		--	41	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	50	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	200	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S =50A	--	--	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 45.6 A, dI _F / dt = 100 A/μs	--	130	--	ns
Q _{rr}	Reverse Recovery Charge		--	0.55	--	μC

NOTES:

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. L = 0.43 mH, I_{AS} = 50 A, V_{DD} = 25 V, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 46.6 A, di/dt ≤ 300 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.
4. Essentially independent of operating temperature typical characteristics.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

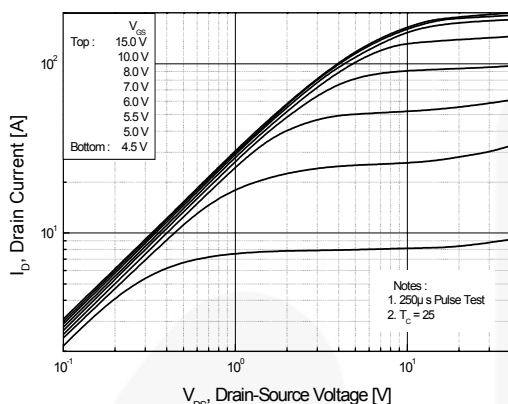


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

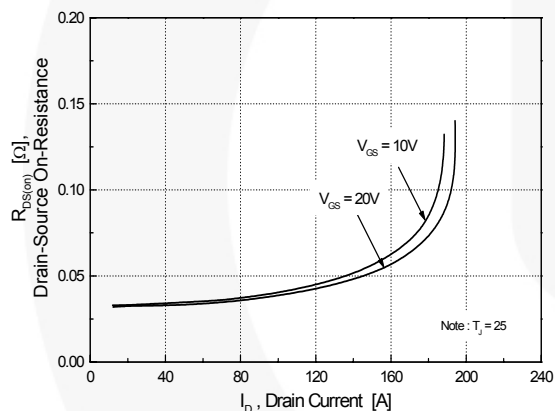


Figure 5. Capacitance Characteristics

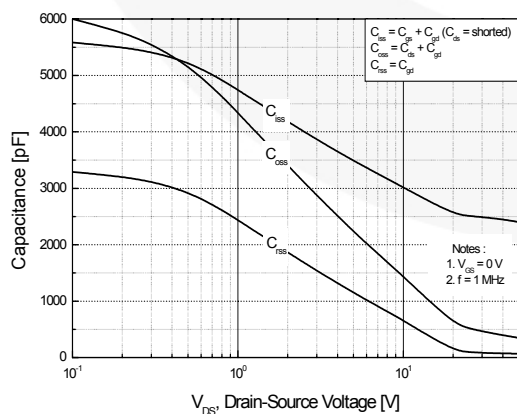


Figure 2. Transfer Characteristics

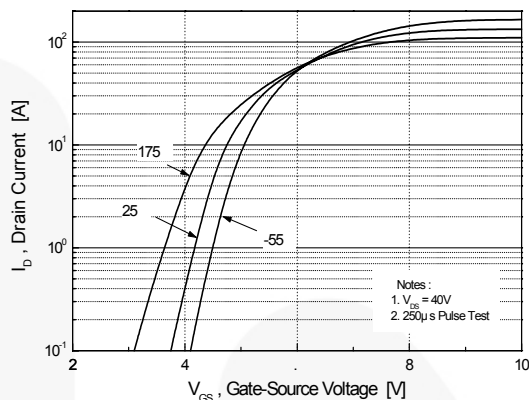


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

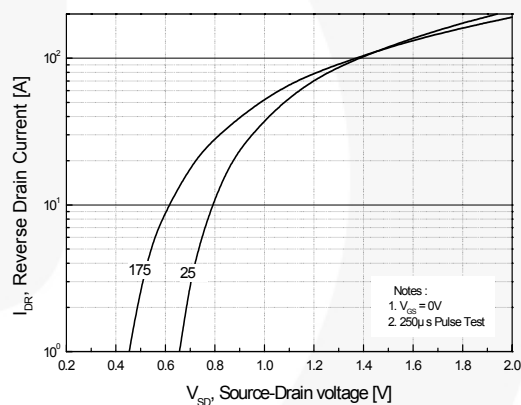
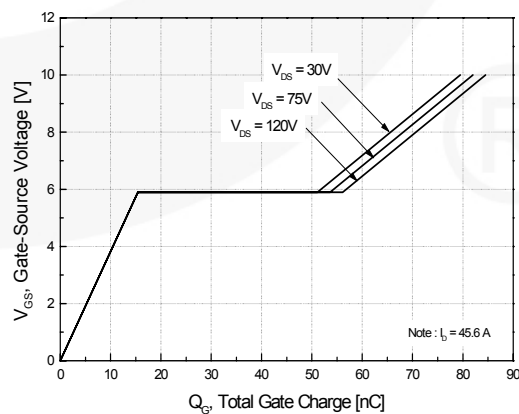


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

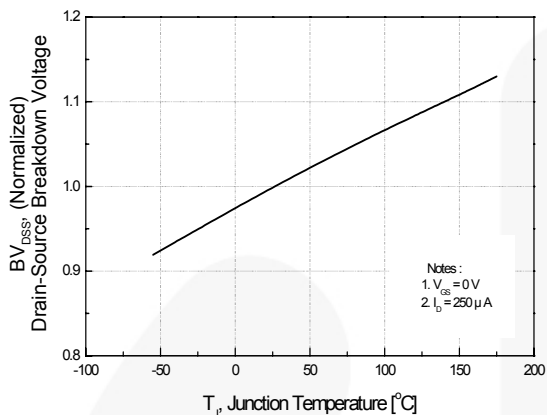


Figure 8. On-Resistance Variation vs. Temperature

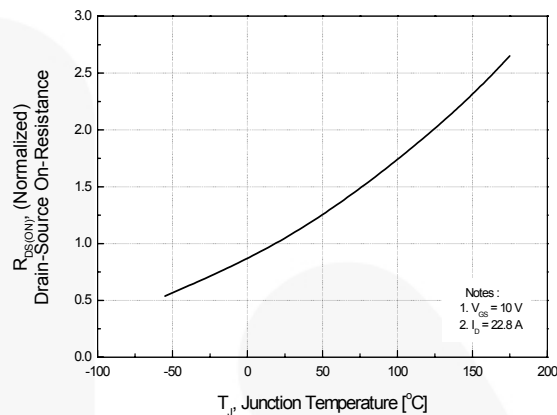


Figure 9. Maximum Safe Operating Area

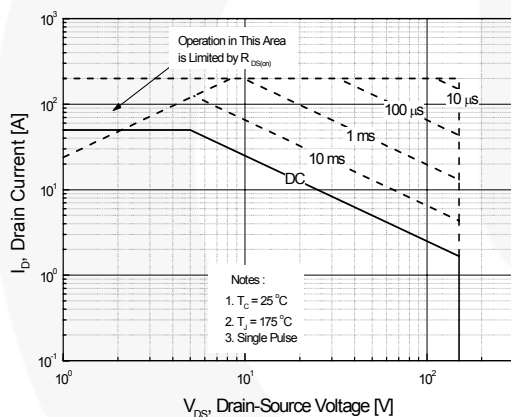


Figure 10. Maximum Drain Current vs. Case Temperature

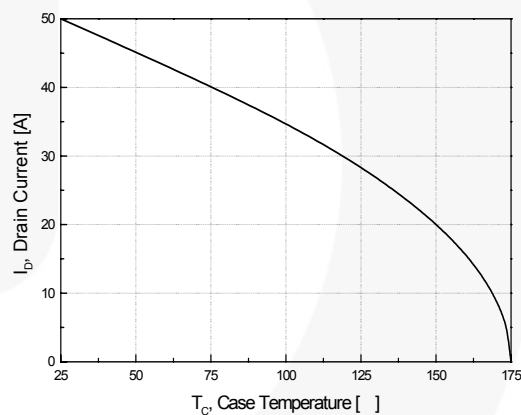
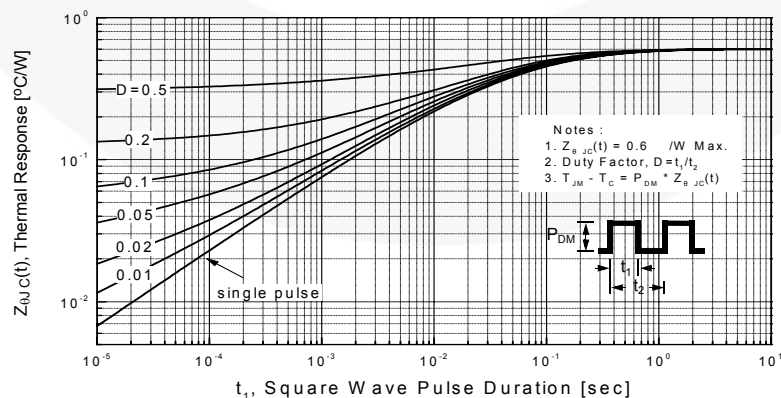


Figure 11. Transient Thermal Response Curve



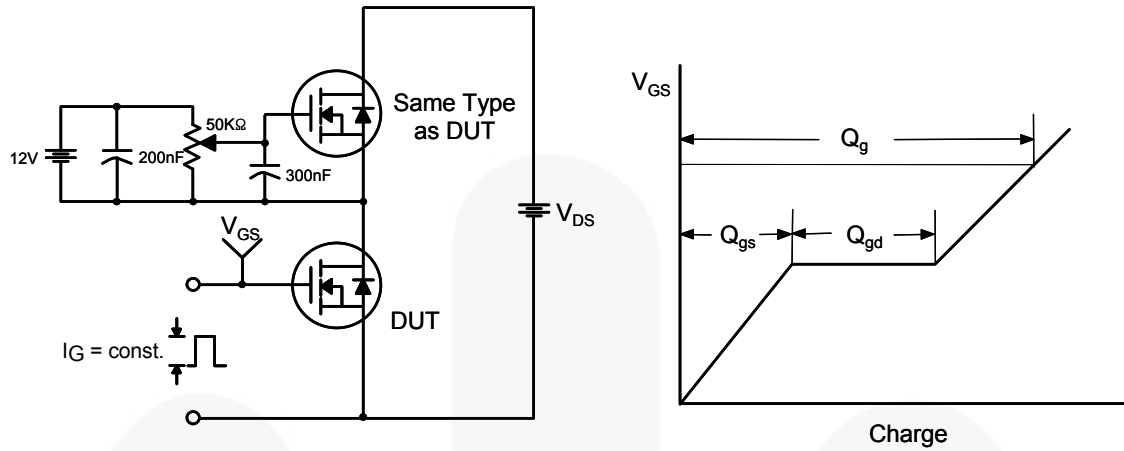


Figure 12. Gate Charge Test Circuit & Waveform

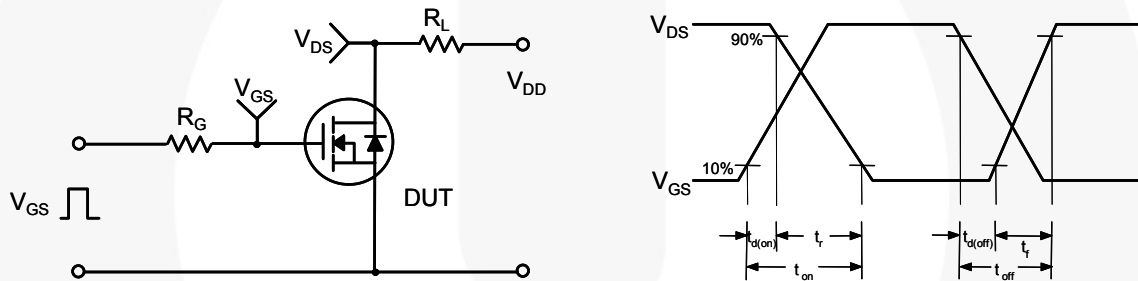


Figure 13. Resistive Switching Test Circuit & Waveforms

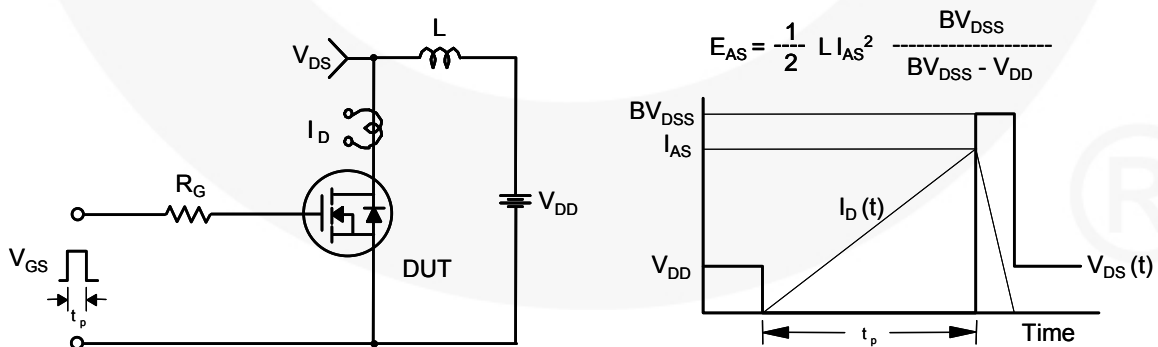
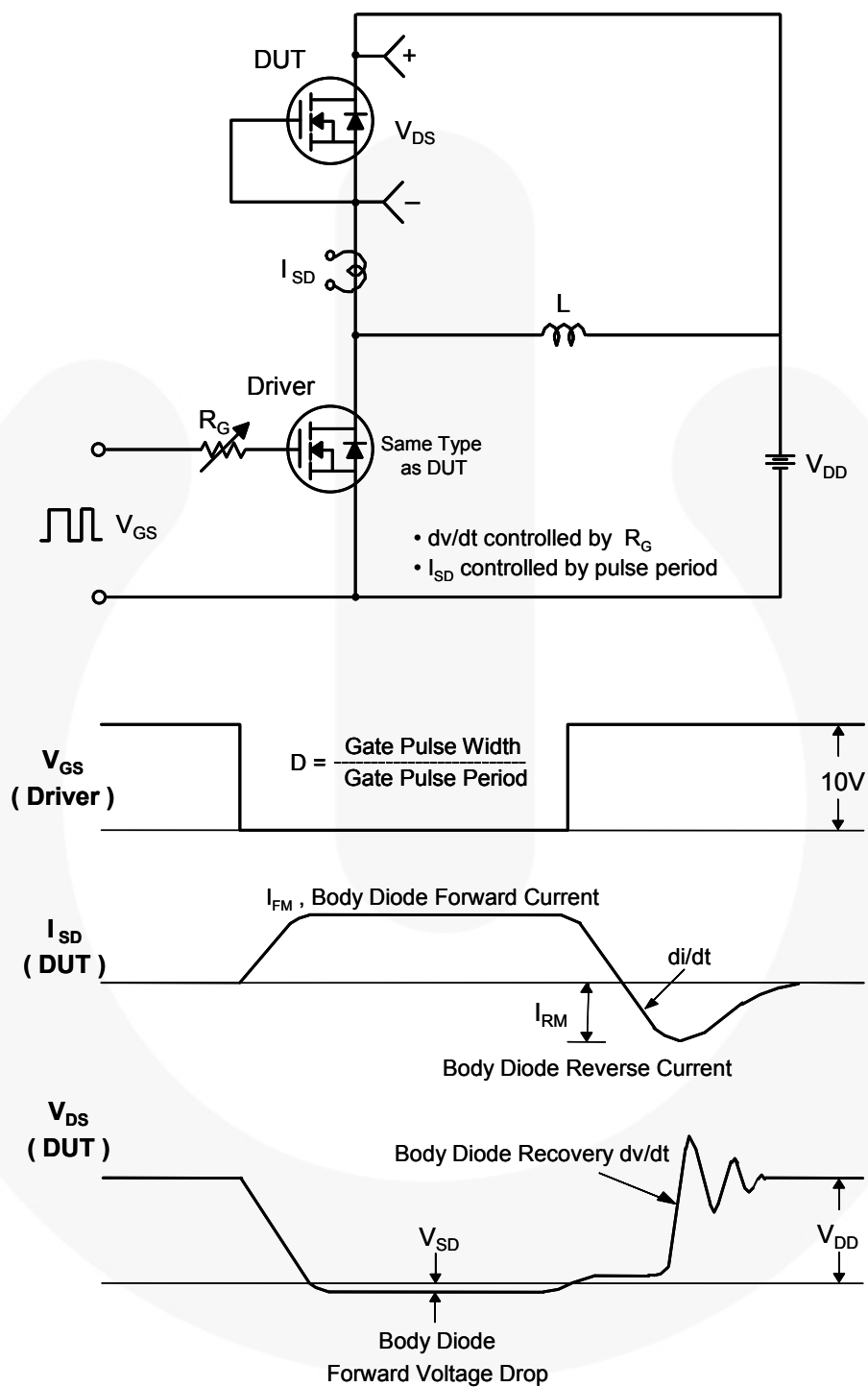
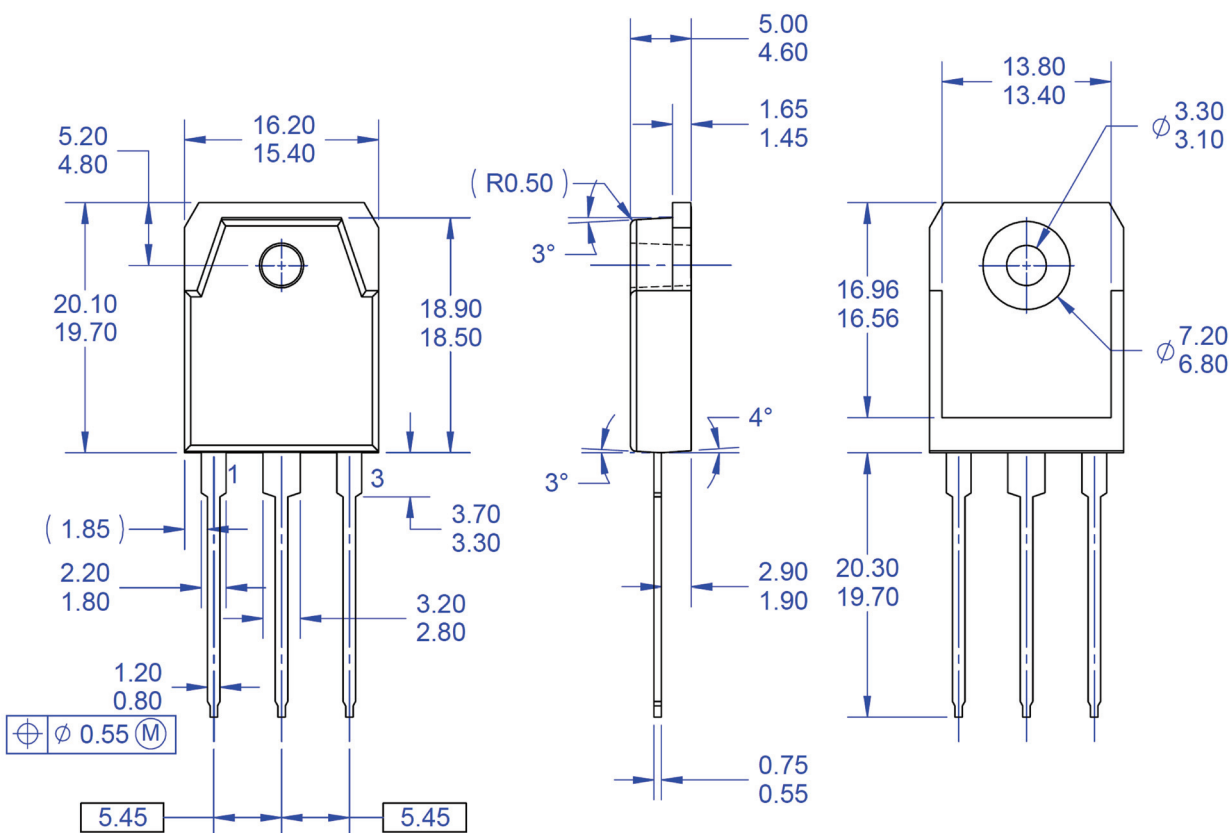


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions



NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE CONFORMS TO EIAJ SC-65 PACKAGING STANDARD.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSION AND TOLERANCING PER ASME14.5-2009.
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- E) DRAWING FILE NAME: TO3PN03AREV1.
- F) FAIRCHILD SEMICONDUCTOR.

Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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