Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$ Max	I_D Max T_A = +25°C
-30V	19mΩ @ V_{GS} = -10V	-8.6A
	45mΩ @ V_{GS} = -4.5V	-5.5A

Features and Benefits

- 0.6mm Profile – Ideal For Low Profile Applications
- Low Gate Threshold Voltage
- Low On-Resistance
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Description and Applications

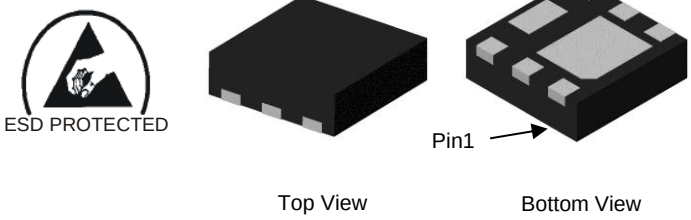
This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Battery Management Application
- Power Management Functions
- DC-DC Converters

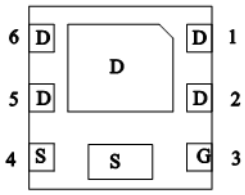
Mechanical Data

- Case: U-DFN2020-6 (Type F)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e4
- Weight: 0.007 grams (Approximate)

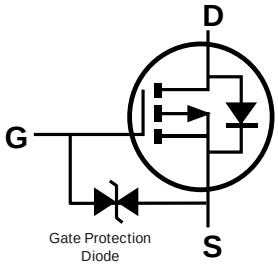
U-DFN2020-6 (Type F)



Top View Bottom View



Pin Out
Bottom View



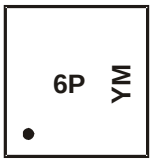
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3026SFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMP3026SFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



6P = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: D = 2016)
M = Month (ex: 9 = September)

Date Code Key

Year	2016	2017	2018	2019	2020	2021	2022	2023
Code	D	E	F	G	H	I	J	K

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±25	V
Continuous Drain Current (Note 6) V_{GS} = -10V	Steady State	T_A = +25°C T_A = +70°C	I_D	-8.6 -6.9	A
	t<10s	T_A = +25°C T_A = +70°C	I_D	-10.3 -8.3	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	-50	A
Continuous Source-Drain Diode Current (Note 6)		T_A = +25°C	I_S	-2.0	A
Avalanche Current (Note 7) L = 0.1mH			I_{AS}	-23	A
Avalanche Energy (Note 7) L = 0.1mH			E_{AS}	27	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T_A = +25°C	P_D	0.71	W
	T_A = +70°C		0.47	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	178	°C/W
	t<10s		125	
Total Power Dissipation (Note 6)	T_A = +25°C	P_D	2.0	W
	T_A = +70°C		1.3	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	62	°C/W
	t<10s		43	
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{\theta JC}$	7.4	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	-1	µA	V_{DS} = -24V, V_{GS} = 0V
Zero Gate Voltage Drain Current T_J = +150°C (Note 9)		—	—	-100		
Gate-Source Leakage	I_{GSS}	—	—	±10	µA	V_{GS} = ±25V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1	—	-3	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	15	19	mΩ	V_{GS} = -10V, I_D = -4.5A
			28	45		V_{GS} = -4.5V, I_D = -3.5A
			34	54		V_{GS} = -4.0V, I_D = -3.0A
Diode Forward Voltage	V_{SD}	—	-0.7	-1.2	V	V_{GS} = 0V, I_S = -1.0A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{ISS}	—	1,204	—	pF	V_{DS} = -15V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{OSS}	—	154	—		
Reverse Transfer Capacitance	C_{RSS}	—	112	—		
Gate Resistance	R_G	—	16	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = -10V)	Q_G	—	19.6	—	nC	V_{DS} = -15V, I_D = -9.5A
Total Gate Charge (V_{GS} = -4.5V)	Q_G	—	9.2	—		
Gate-Source Charge	Q_{GS}	—	4.3	—		
Gate-Drain Charge	Q_{GD}	—	3.9	—		
Turn-On Delay Time	$t_{D(ON)}$	—	5.3	—	ns	V_{DS} = -15V, V_{GS} = -10V, R_G = 6Ω, I_D = -9.5A
Turn-On Rise Time	t_R	—	23	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	34	—		
Turn-Off Fall Time	t_F	—	26	—		
Reverse Recovery Time	t_{RR}	—	10	—	ns	I_F = -9.5A, di/dt = 100A/µs
Reverse Recovery Charge	Q_{RR}	—	3.3	—	nC	

Notes: 8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

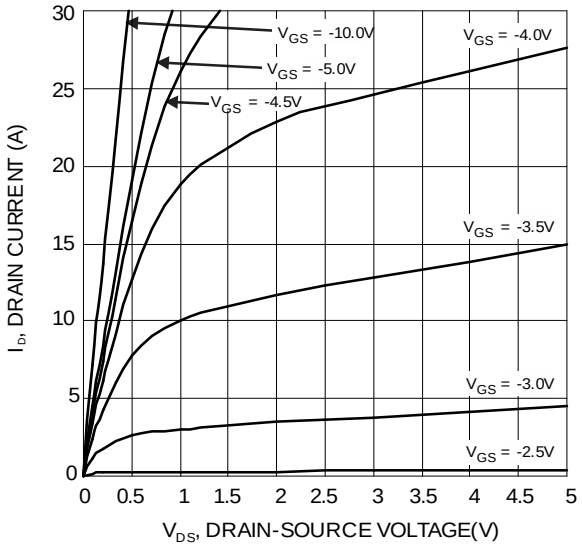


Figure1 Typical Output Characteristic

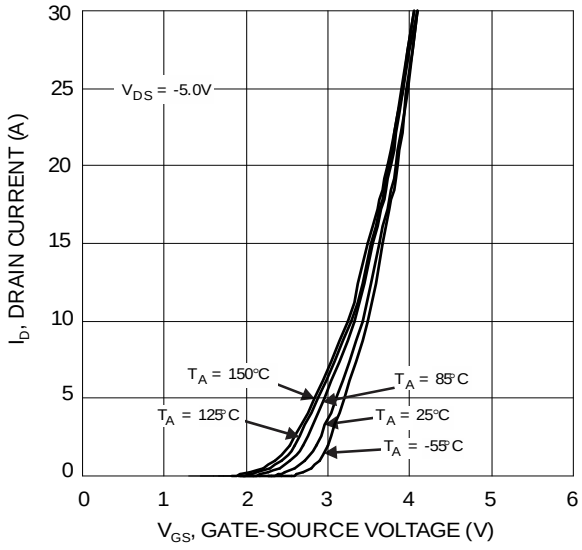


Figure 2 Typical Transfer Characteristic

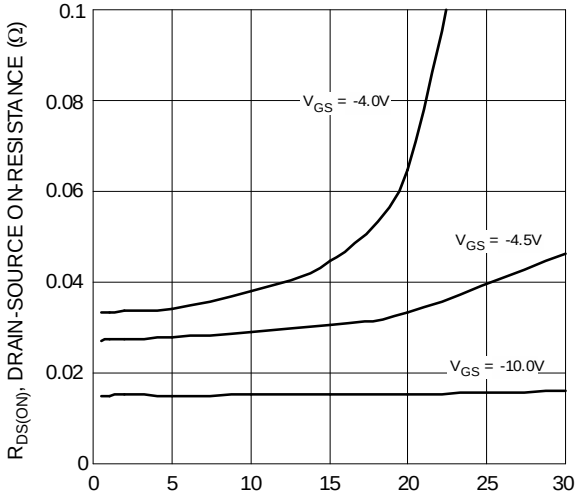


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

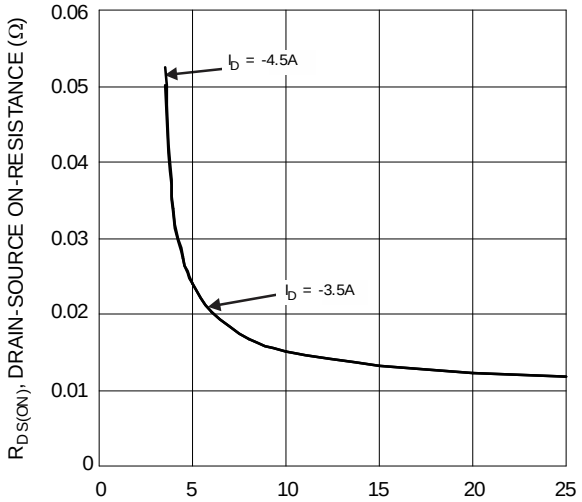


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

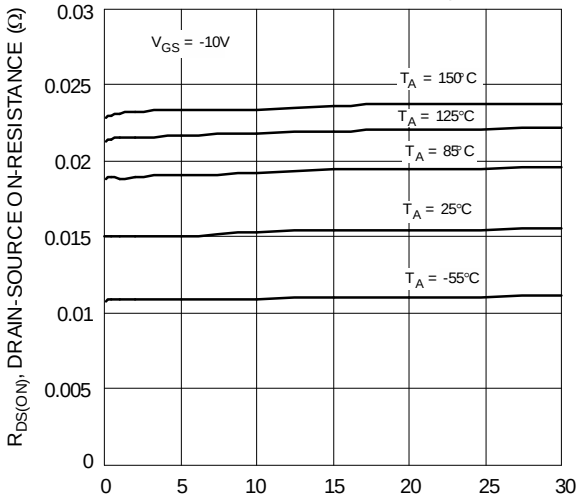


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

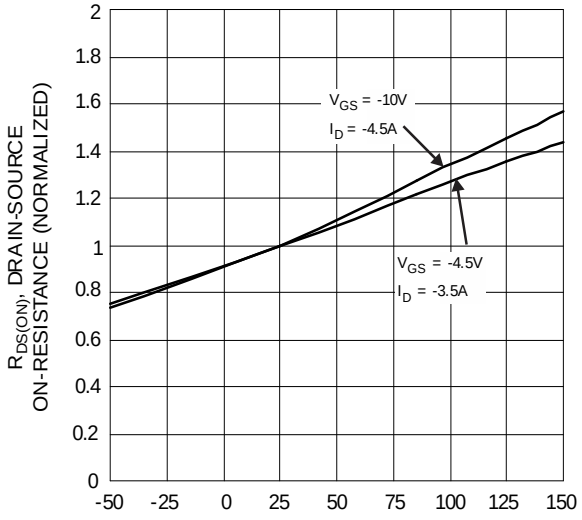


Figure 6 On-Resistance Variation with Temperature

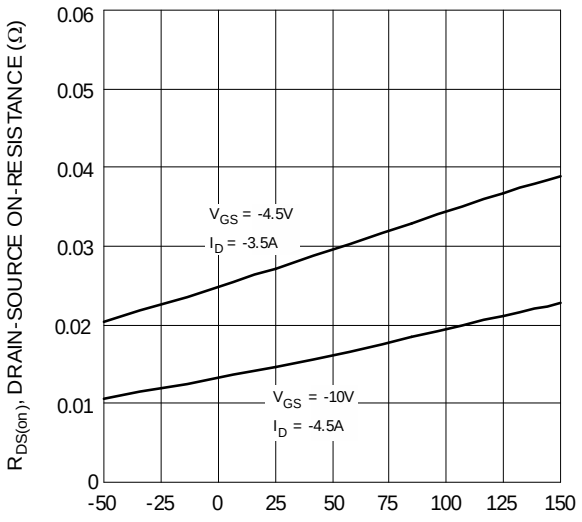


Figure 7 On-Resistance Variation with Temperature

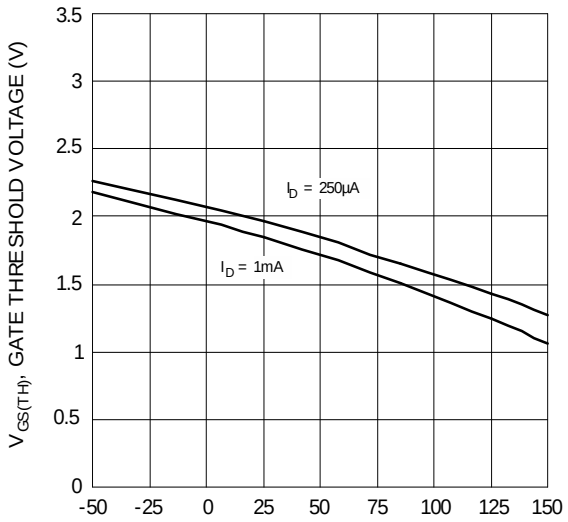


Figure 8 Gate Threshold Variation vs. Junction Temperature

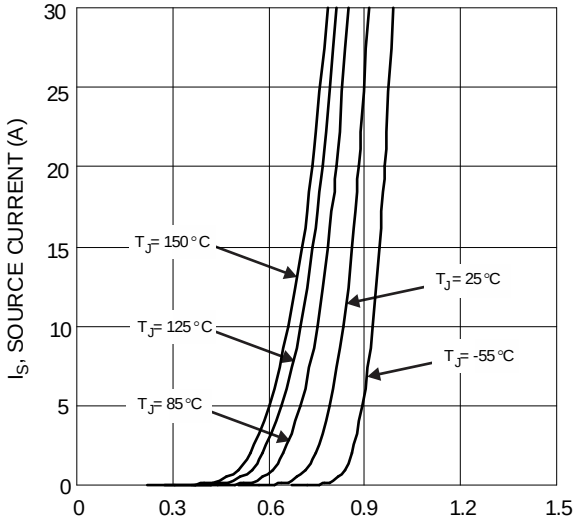


Figure 9 Diode Forward Voltage vs. Current

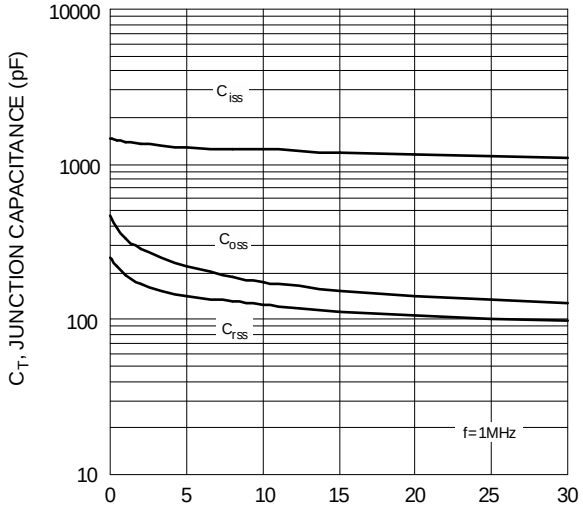


Figure 10 Typical Junction Capacitance

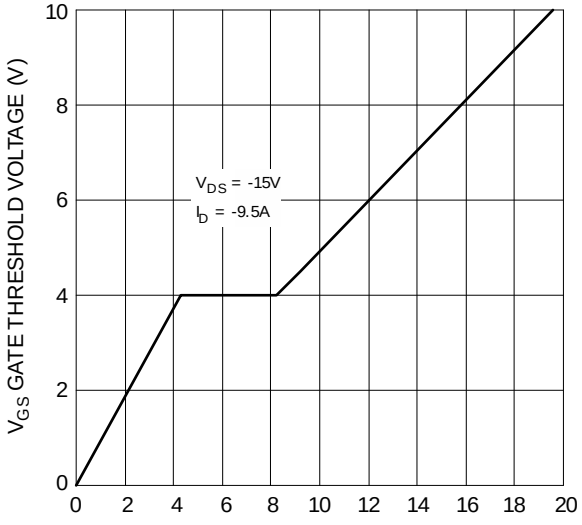


Figure 11 Gate Charge

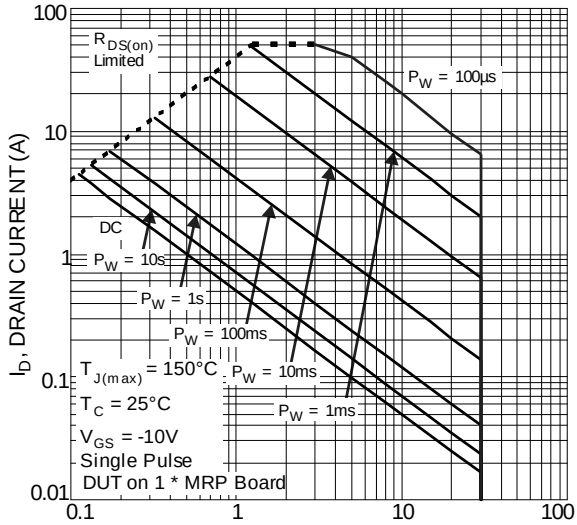


Figure 12 SOA, Safe Operation Area

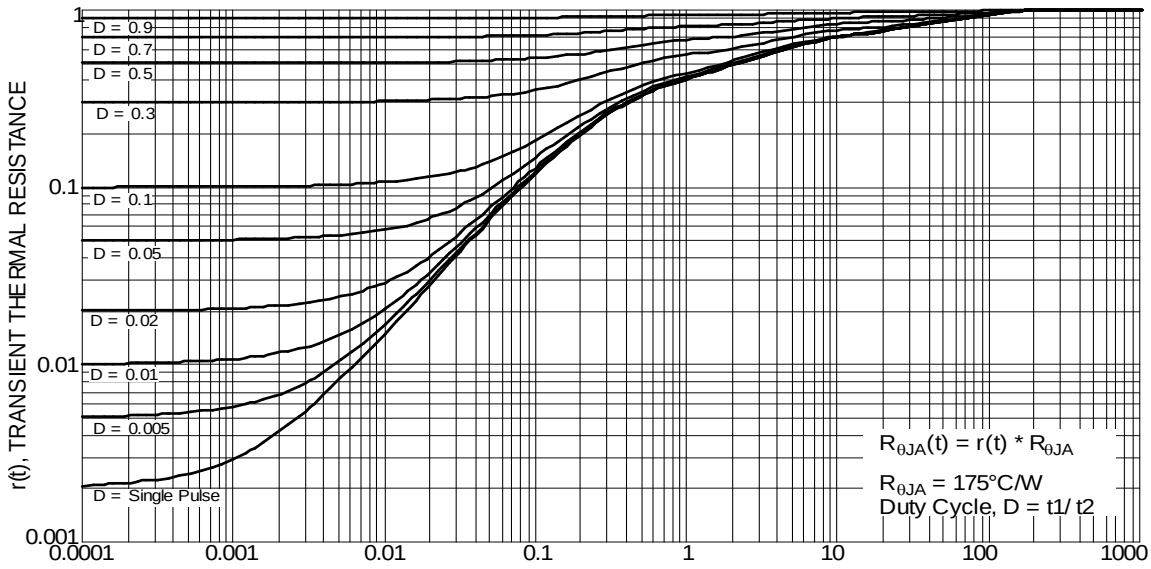
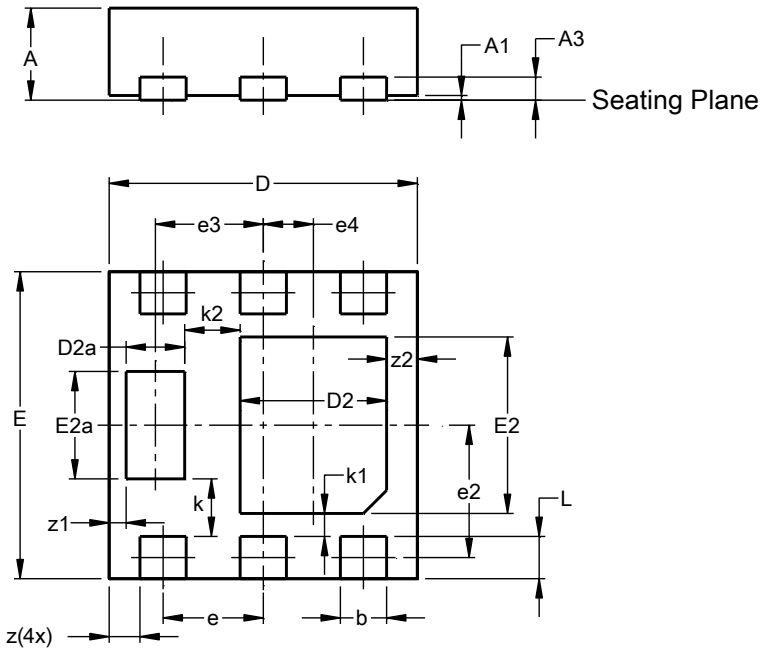


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)

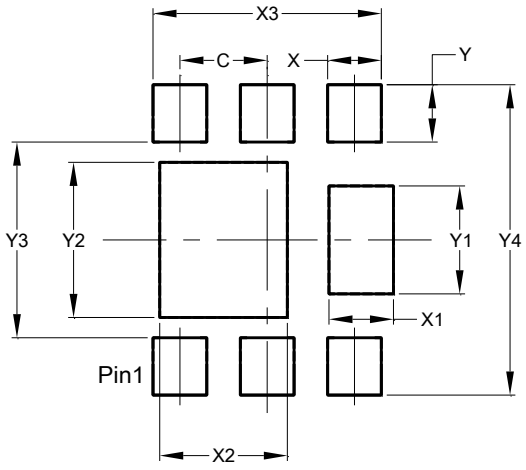


U-DFN2020-6 (Type F)			
Dim	**Min**	**Max**	**Typ**
A	0.57	0.63	0.60
A1	0.00	0.05	0.03
A3	-	-	0.15
b	0.25	0.35	0.30
D	1.95	2.05	2.00
D2	0.85	1.05	0.95
D2a	0.33	0.43	0.38
E	1.95	2.05	2.00
E2	1.05	1.25	1.15
E2a	0.65	0.75	0.70
e	0.65 BSC		
e2	0.863 BSC		
e3	0.70 BSC		
e4	0.325 BSC		
k	0.37 BSC		
k1	0.15 BSC		
k2	0.36 BSC		
L	0.225	0.325	0.275
z	0.20 BSC		
z1	0.110 BSC		
z2	0.20 BSC		
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
C	0.650
X	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300

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