## NOT RECOMMENDED FOR NEW DESIGNS USE ES2A-LTP~ES1J-LTP SERIES



**Micro Commercial Components** 



Micro Commercial Components 20736 Marilla Street Chatsworth CA 91311

Phone: (818) 701-4933 Fax: (818) 701-4939

## ES2A THRU ES2M

### **Features**

- Lead Free Finish/Rohs Compliant (Note1) ("P"Suffix designates Compliant. See ordering information)
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Easy Pick And Place
- High Temp Soldering: 260 °C for 10 Seconds At Terminals
- · Superfast Recovery Times For High Efficiency

## 2 Amp Ultra Fast Recovery Silicon Rectifier 50 to 1000 Volts

### **Maximum Ratings**

- Operating Temperature: -50°C to +150°C
- Storage Temperature: -50°C to +150°C
- Maximum Thermal Resistance; 20°C/W Junction To Lead

MCC	Device	Maximum	Maximum	Maximum
Catalog	Marking	Recurrent	Recurrent RMS	
Number		Peak Reverse	Voltage	Blocking
		Voltage		Voltage
ES2A	ES2A	50V	35V	50V
ES2B	ES2B	100V	70V	100V
ES2C	ES2C	150V	105V	150V
ES2D	ES2D	200V	140V	200V
ES2G	ES2G	400V	280V	400V
ES2J	ES2J	600V	420V	600V
ES2K	ES2K	800V	560V	800V
ES2M	ES2M	1000V	700V	1000V

### Electrical Characteristics @ 25°C Unless Otherwise Specified

Average Forward Current	I <sub>F(AV)</sub>	2.0A	T <sub>J</sub> = 75°C
Peak Forward Surge	$I_{FSM}$	50A	8.3ms, half sine
Current			·
Maximum			
Instantaneous			
Forward Voltage			
ES2A-D ES2G-J	$V_{F}$	.975V 1.35V	$I_{FM} = 2.0A;$
ES2K-M		1.70V	$T_{J} = 25^{\circ}C^{*}$
Maximum DC			
Reverse Current At	$I_R$	5μΑ	T <sub>.1</sub> = 25°C
Rated DC Blocking		150μΑ	T <sub>J</sub> = 100°C
Voltage			•
Maximum Reverse			
Recovery Time			
ES2A-D	$T_{rr}$	50ns	$I_F = 0.5A, I_R = 1.0A,$
ES2G-J ES2K-M		60ns 100ns	I <sub>rr</sub> =0.25A
Typical Junction	Сл	25pF	Measured at
Capacitance			1.0MHz, V <sub>R</sub> =4.0V

DO-214AC
(HSMA) (High Profile)

H
Cathode Band

DIMENSIONS

DIMENSIONS

INCHES

MM
MIN MAX MIN MAX NOTE

A .078 .116 1.98 2.95

B .067 .089 1.70 2.25

C .002 .008 .05 20

D --- .02 --- .51

E .035 .095 .89 1.40

F .065 .096 1.65 2.45

G .205 .224 5.21 5.69

H .160 .180 4.06 4.57

J .100 .112 2.57 2.84

SUGGESTED SOLDER

PAD LAYOUT

0.090"

0.070"

Note: 1. High Temperature Solder Exemptions Applied, see EU Directive Annex 7.

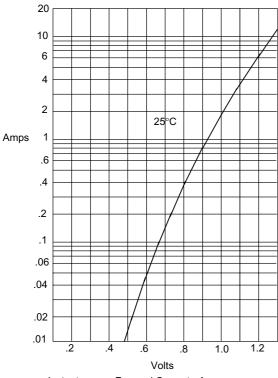
<sup>\*</sup>Pulse test: Pulse width 200 µsec, Duty cycle 2%

### ES2A thru ES2M

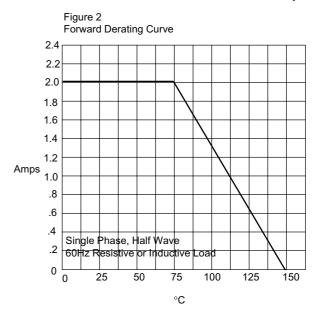
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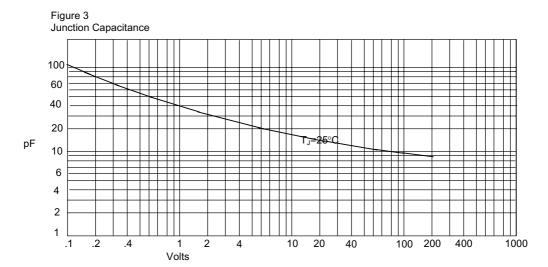




Volts
Instantaneous Forward Current - Amperesversus
Instantaneous Forward Voltage - Volts



Average Forward Rectified Current - Amperes/ersus Ambient Temperature -°C

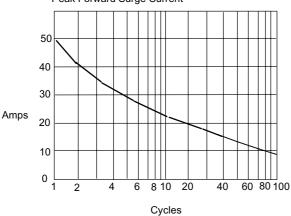


Junction Capacitance - pF*versus* Reverse Voltage - Volts

### ES2A thru ES2M



Figure 4
Peak Forward Surge Current

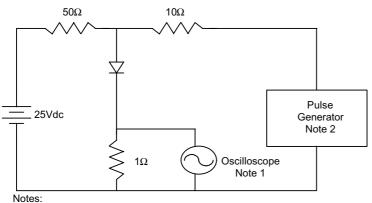


Peak Forward Surge Current - Amperes*versus* Number Of Cycles At 60Hz - Cycles

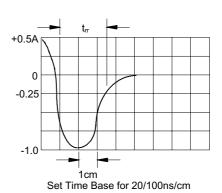
Figure 5
New SMA Assembly

Round Lead
Process

Figure 6
Reverse Recovery Time Characteristic And Test Circuit Diagram



- 1. Rise Time = 7ns max.
- Input impedance = 1 megohm, 22pF
- 2. Rise Time = 10ns max.
- Source impedance = 50 ohms
- 3. Resistors are non-inductive





# Ordering Information :

Revision: B

Device	Packing	
Part Number-TP	Tape&Reel: 3Kpcs/Reel	

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 $\frac{\text{Micro Commercial Components (MCC)}}{\text{ES2A-TP} \ \text{ES2B-TP} \ \text{ES2C-TP}}$