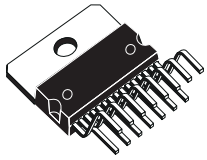
TDA7497S

8W+8W+15W TRIPLE AMPLIFIER

- 8+8W (RL = 8Ω) + 15W (RL = 4Ω) OUTPUT POWER @THD = 10%, Vcc = 25V
- INDEPENDENT MUTE FOR CENTER CHANNEL AND MAIN CHANNELS
- NO TURN-ON TURN-OFF POP NOISE
- NO BOUCHEROT CELL
- SINGLE SUPPLY RANGING UP TO 35V
- SHORT CIRCUIT PROTECTION
- THERMAL OVERLOAD PROTECTION
- INTERNALLY FIXED GAIN
- SOFT CLIPPING
- MULTIWATT 15 PACKAGE

MULTIPOWER BI50II TECHNOLOGY



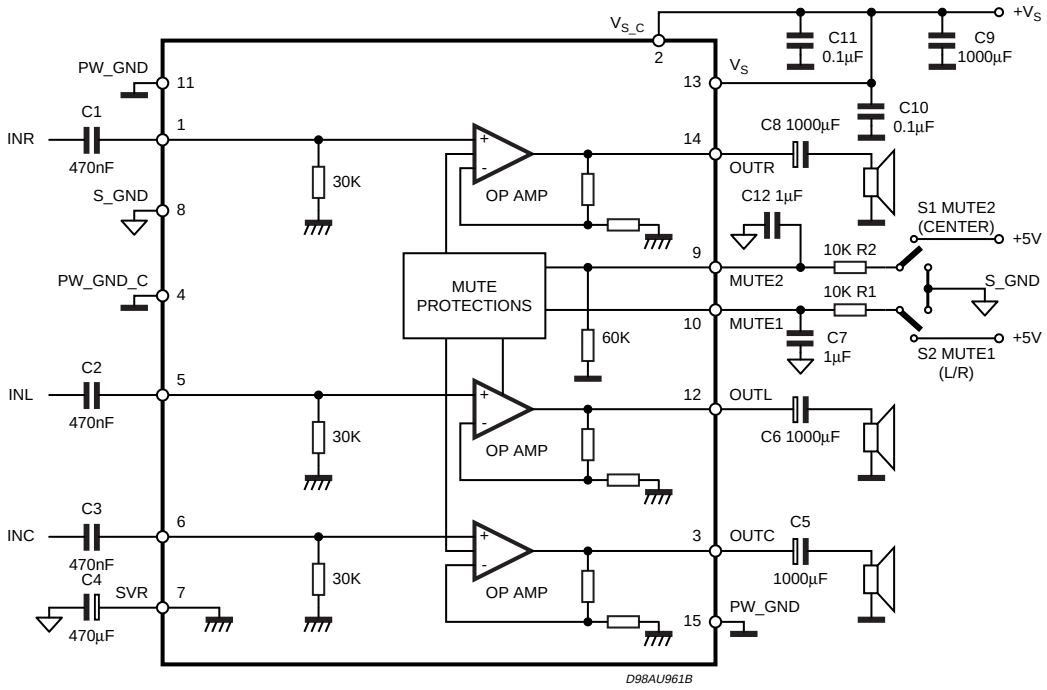
Multiwatt15
ORDERING NUMBER: TDA7497S

DESCRIPTION

The TDA7497S is a triple 8+8+15W class AB power amplifier assembled in the @ Multiwatt 15 package, specially designed for high quality sound, TV applications.

Features of the TDA7497S include mute functions, independently controller for main and center channels.

BLOCK DIAGRAM

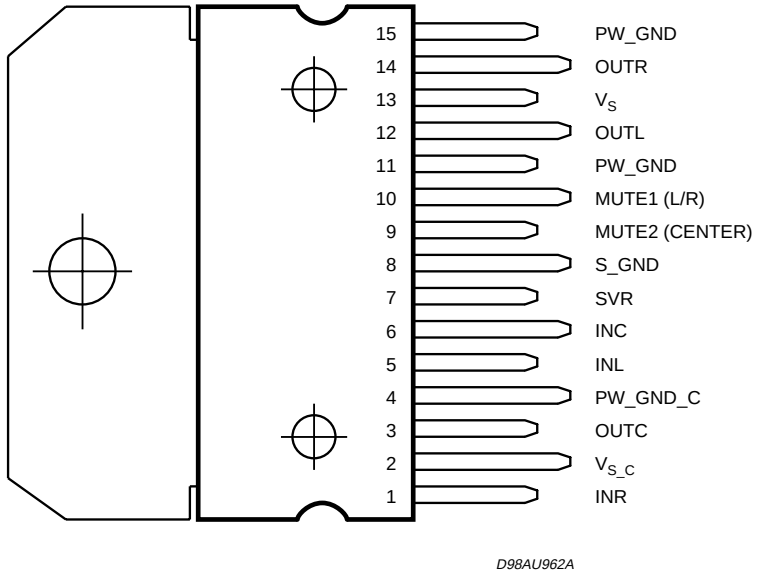


ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_S	DC Supply Voltage	35	V
P_{tot}	Total Power Dissipation (Tamb = 70°C)	20	W
T_{amb}	Ambient Operating Temperature (1)	0 to 70	°C
T_{stg}, T_j	Storage and Junction Temperature	-40 to 150	°C

(1) Operation between -20 to 85 °C guaranteed by correlation with 0 to 70°C.

PIN CONNECTION (Top view)



THERMAL DATA

Symbol	Parameter	Value	Unit
$R_{th\ j\text{-}case}$	Thermal Resistance Junction-case	Typ.=1.3 max = 1.9	°C/W
$R_{th\ j\text{-}amb}$	Thermal Resistance Junction-ambient	max = 35	°C/W

ELECTRICAL CHARACTERISTCS (Refer to the test circuit V_S = 25V; R_g = 50Ω, T_{amb} = 25°C)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_S	Supply Voltage Range		11		30	V
I_q	Total Quiescent Current			60	100	mA
V_O	Quiescent Output Voltage		11.5	12.5	13.5	V
$P_{O\_L/R}$	Output Power Left / RightChannels	THD = 10%; RL = 8Ω; THD = 1%; RL = 8Ω;	6 5	8 6		W W
P_{O_C}	Output Power Center Channel	THD = 10%; RL = 4Ω THD = 1%; RL = 4Ω	12 10	15 12		W W
THD	Total Harmonic Distortion	PO = 1W; f = 1KHz;			0.4	%
$I_{peak\ L/R}$	Output Peak Current	(internally limited)		2.0		A
$I_{peak\ C}$	Output Peak Current Central Channel	(internally limited)		2.5		A
GV	Closed Loop Gain		28.5	29.5	30.5	dB
ΔGV	L/R Voltage GainMatching		-1		1	dB
BW				0.6		MHz
e_N	Total Output Noise	f = 20Hz to 22KHz		60	150	µV
SR	Slew Rate		5	8		V/µs
R_i	Input Resistance		22.5	30		KΩ
SVR	Supply Voltage Rejection	f = 1kHz CSVR = 470µF; VRIP = 1Vrms	50	60		dB
T_M	Thermal Muting			150		°C
T_s	Thermal Shut-down			160		°C
MUTE & INPUT SELECTION FUNCTIONS						
V_{MUTE1}	Mute 1 ON threshold (L/R)		3.5			V
	Mute 1 OFF threshold (L/R)				1.5	V
V_{MUTE2}	Mute 2 ON Threshold (center)		3.5			V
	Mute 2 OFF Threshold (center)				1.5	V
A_{MUTE}	Mute Attenuation		50	65		dB
$I_{muteBIAS}$	Mute bias current Mute1/Mute2	Mute		1	5	µA
		Play		0.2	2	µA

Figure 1. PC Boar and Component Layout

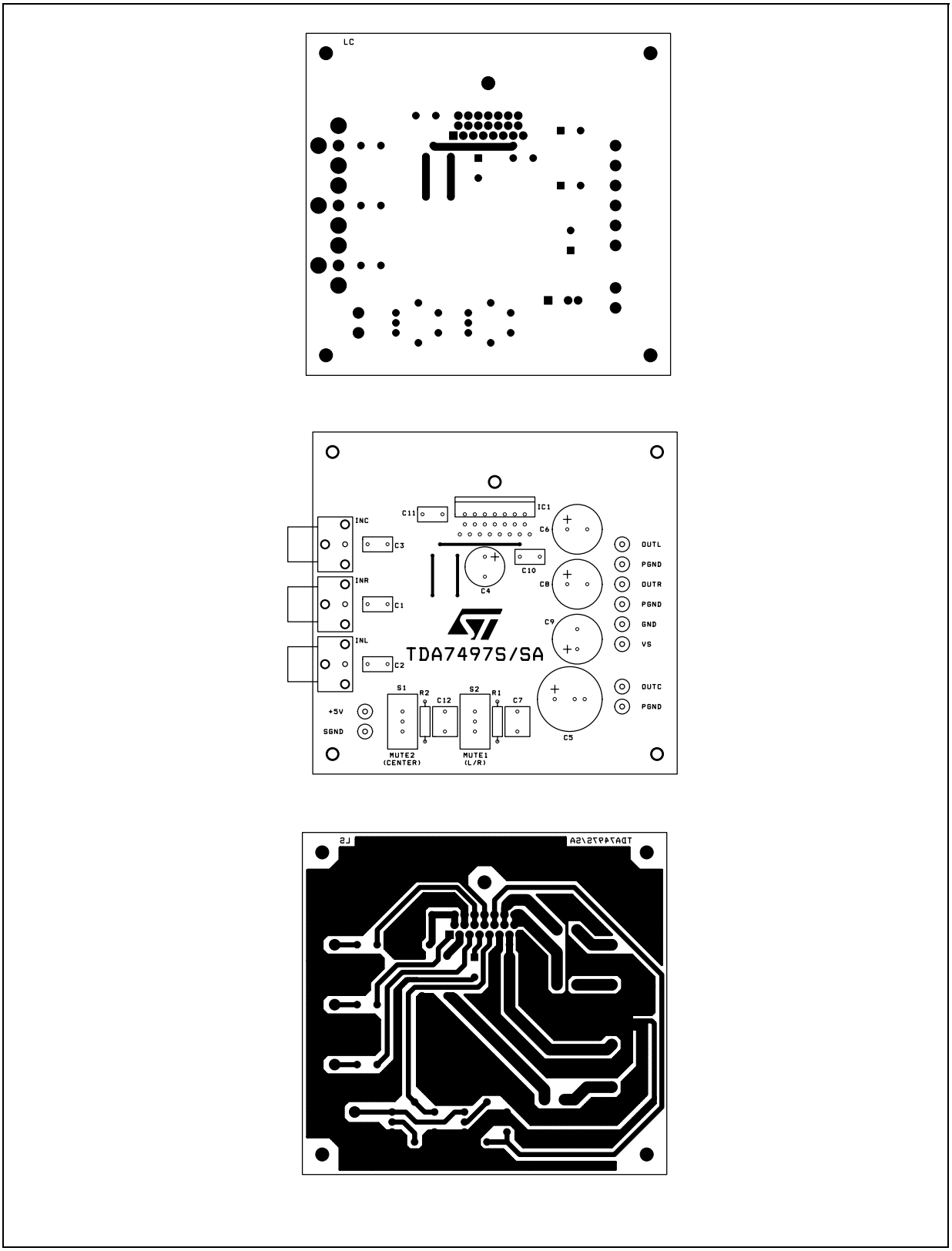


Figure 2. Output Power vs Supply Voltage

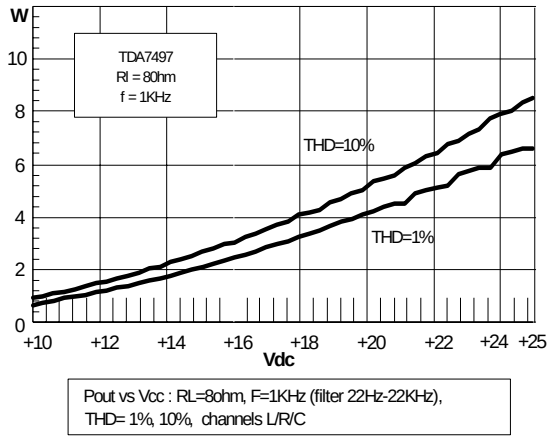


Figure 3. Frequency Response

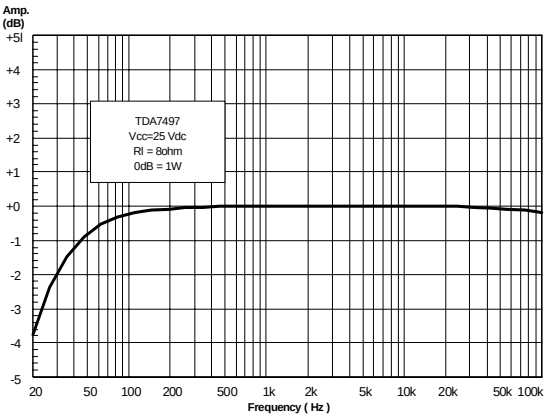


Figure 4. THD+N vs Output Power

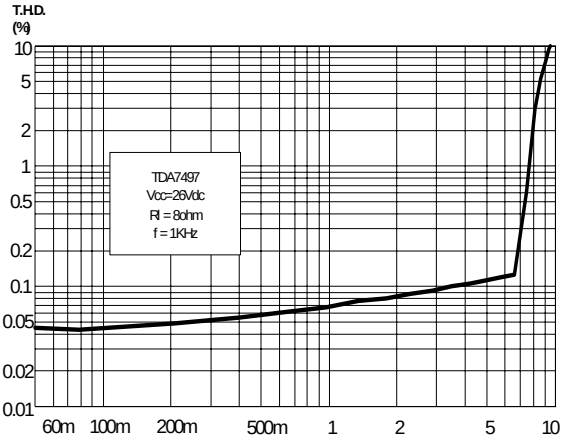
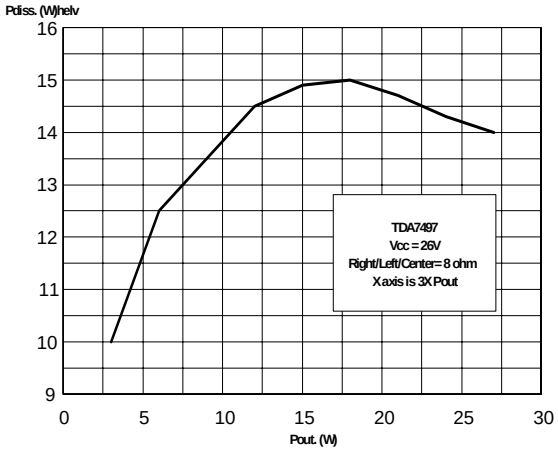
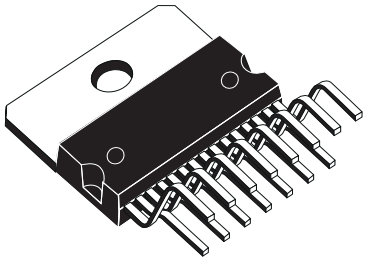


Figure 5. Pdiss vs Output Power



DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			5			0.197
B			2.65			0.104
C			1.6			0.063
D		1			0.039	
E	0.49		0.55	0.019		0.022
F	0.66		0.75	0.026		0.030
G	1.02	1.27	1.52	0.040	0.050	0.060
G1	17.53	17.78	18.03	0.690	0.700	0.710
H1	19.6			0.772		
H2			20.2			0.795
L	21.9	22.2	22.5	0.862	0.874	0.886
L1	21.7	22.1	22.5	0.854	0.870	0.886
L2	17.65		18.1	0.695		0.713
L3	17.25	17.5	17.75	0.679	0.689	0.699
L4	10.3	10.7	10.9	0.406	0.421	0.429
L7	2.65		2.9	0.104		0.114
M	4.25	4.55	4.85	0.167	0.179	0.191
M1	4.63	5.08	5.53	0.182	0.200	0.218
S	1.9		2.6	0.075		0.102
S1	1.9		2.6	0.075		0.102
Dia1	3.65		3.85	0.144		0.152

OUTLINE AND MECHANICAL DATA



Multiwatt15 V