TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SZ32AFS

#### 2-Input OR Gate

#### **Features**

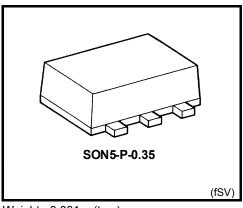
High output current : ±24mA (min) at V<sub>CC</sub> = 3.0V

• Super high speed operation : t<sub>pd</sub> = 2.4ns (typ.)

at  $V_{CC}$  = 5 V, 50pF

• Operating voltage range : V<sub>CC</sub> = 1.65 to 5.5V

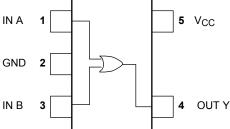
• 5.5-V tolerant inputs.



Weight: 0.001 g (typ.)

#### Marking





Pin Assignment (top view)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	–0.5 to 6	V
DC input voltage	V <sub>IN</sub>	–0.5 to 6	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> +0.5	٧
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	±20 (Note 1)	mA
DC output current	lout	±50	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	$P_{D}$	50	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V<sub>OUT</sub> < GND, V<sub>OUT</sub> > V<sub>CC</sub>

Start of commercial production 2008-02



# IEC Logic Symbol



## **Truth Table**

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

# **Operating Ranges**

Characteristics	Symbol	Rating			
Supply voltage	V <sub>CC</sub>	1.65 to 5.5	V		
Supply voltage		1.5 to 5.5 (Note 2)	V		
Input voltage	V <sub>IN</sub>	0 to 5.5	٧		
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	٧		
Operating temperature	T <sub>opr</sub>	-40 to 85	°C		
	dt/dv	0 to 20 (V <sub>CC</sub> = 1.80 V $\pm$ 0.15 V, 2.5 V $\pm$ 0.2 V)			
Input rise and fall time		0 to 10 (V <sub>CC</sub> = 3.3 V $\pm$ 0.3 V)	ns/V		
		0 to 5 ( $V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$ )			

2

Note 2: Data retention only

## **Electrical Characteristics**

### **DC Characteristics**

Characteristics Symbol T			act Candition		Ta = 25°C			Ta = -40 to 85°C		Unit
Gridiaciensiios Symbol		rest	Test Condition V <sub>CC</sub>		Min	Тур.	Max	Min	Max	- Unit
High-level input voltage			1.65 to 1.95	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	. v	
			_		V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7		_
Low-level input	V.,			1.65 to 1.95	l	ı	V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	V
voltage	_		2.3 to 5.5	l	ı	V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3	V	
				1.65	1.55	1.65	1	1.55	1	
			I <sub>OH</sub> = -100 μA	2.3	2.2	2.3		2.2		V
			ΙΟΗ = -100 μΑ	3.0	2.9	3.0		2.9		
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>		4.5	4.4	4.5	_	4.4	_	
High-level output voltage	Vон		I <sub>OH</sub> = -4 mA	1.65	1.29	1.52	_	1.29	_	
			I <sub>OH</sub> = -8 mA	2.3	1.9	2.15	_	1.9	_	
			I <sub>OH</sub> = -16 mA	3.0	2.4	2.8	_	2.4	_	
			I <sub>OH</sub> = -24 mA	3.0	2.3	2.68	_	2.3	_	
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8	_	
	VoL	$V_{IN} = V_{IL}$	I <sub>OL</sub> = 100 μA	1.65	_	0	0.1	_	0.1	-
				2.3	_	0	0.1	_	0.1	
				3.0	_	0	0.1	_	0.1	
				4.5	_	0	0.1	_	0.1	
Low-level output voltage			I <sub>OL</sub> = 4 mA	1.65	_	0.08	0.24	_	0.24	V
			I <sub>OL</sub> = 8 mA	2.3		0.1	0.3	_	0.3	
			I <sub>OL</sub> = 16 mA	3.0	_	0.15	0.4	_	0.4	
			I <sub>OL</sub> = 24 mA	3.0	_	0.22	0.55	_	0.55	
			I <sub>OL</sub> = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5 V or GND		0 to 5.5		_	±1	_	±10	μА
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	1	_	10	μΑ

## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics Symbol	Cumbal	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	Symbol	rest Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	<sup>t</sup> pLH <sup>t</sup> pHL	$C_L$ = 15 pF, $R_L$ = 1 $M\Omega$	$1.80 \pm 0.15$	2.0	5.3	9.6	2.0	9.8	- ns
			2.5 ± 0.2	0.8	3.2	5.3	0.8	5.7	
			$3.3\pm0.3$	0.5	2.4	3.7	0.5	4.0	
			5.0 ± 0.5	0.5	1.9	2.9	0.5	3.2	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	$3.3 \pm 0.3$	1.5	3.0	4.6	1.5	4.9	
			5.0 ± 0.5	8.0	2.4	3.6	0.8	3.9	
Input capacitance	C <sub>IN</sub>	_	0 to 5.5	_	4		_		pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 3)	3.3	_	19	_	_	_	- pF
			5.5	_	27		_		

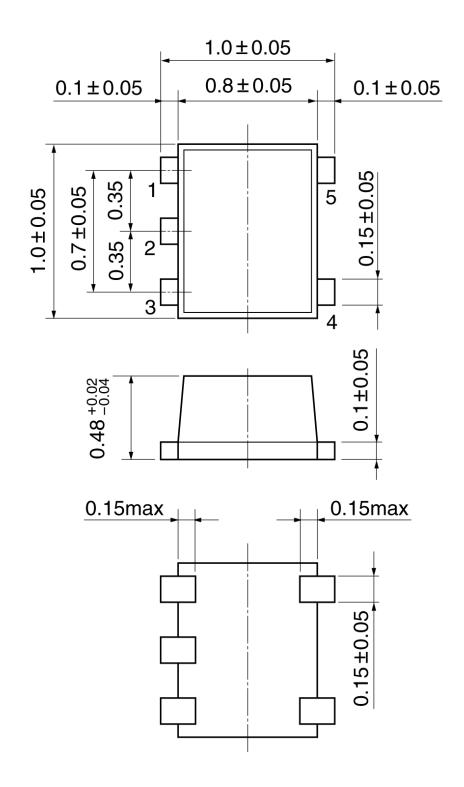
Note3: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

## **Package Dimensions**

SON5-P-0.35 Unit: mm



Weight: 0.001 g (typ.)

5 2014-03-01

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