

[Products](#) > [Clocks & Timing](#) > [Clock Distribution](#) > [Zero Delay Buffers \(PLL\)](#) > [2:1 PCIe Gen1/2/3 Clock Multiplexer](#)

Status: **Active New**

Product Quick Links

Specifications

Product Options

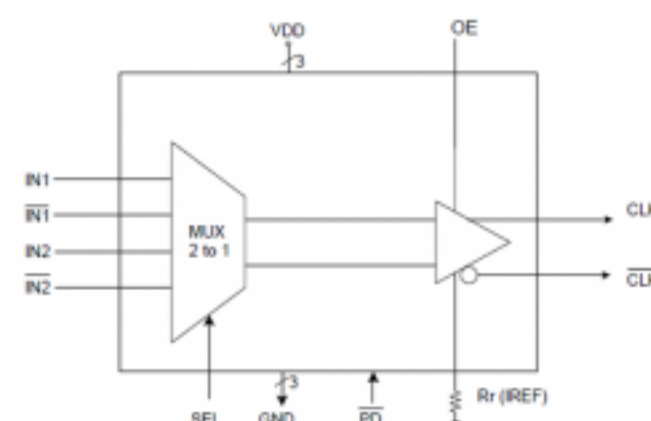
Technical Support

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6V31023

2:1 PCIe Gen1/2/3 Clock Multiplexer

The 6V31023 is a 2:1 differential clock mux for PCI Express applications. It has very low additive jitter making it suitable for use in PCIe Gen2 and Gen3 systems. The 6V31023 selects between 1 of 2 differential HCSL inputs to drive a single differential HCSL output pair. The output can also be terminated to LVDS.



Features

- Low additive jitter suitable for use in PCIe Gen2 and Gen3 systems
- 16-pin TSSOP package for small board footprint
- Outputs can be terminated to LVDS to drive a wider variety of devices
- OE control pin offers greater system power management
- Industrial temperature range supports demanding embedded applications
- Additive cycle-to-cycle jitter <5 ps
- Additive phase jitter (PCIe Gen3) <0.2ps
- Operating frequency up to 200MHz

Product Specification

APP JITTER COMPLIANCE	CORE VOLTAGE (V)	DIFF. OUTPUT SIGNALING	INPUT FREQ (MHZ)	OUTPUT FREQ RANGE (MHZ)	OUTPUT VOLTAGE (V)	POWER TYP (MW)	SUPPLY VOLTAGE (V)
PCIe Gen 3, PCIe Gen 2, PCIe Gen 1	3.3	HCSL	25.000000	0.000000 - 2000.000000	0.8		3.30

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
6V31023PGG	Active	PGG16	TSSOP	16	C	Y	Check Inventory
6V31023PGG8	Active	PGG16	TSSOP	16	C	Y	Check Inventory

Documents

There are no public documents available for this product. [Click here to request documentation.](#)

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