



### PNP SURFACE MOUNT TRANSIS

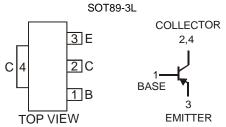
## **Features**

- **Epitaxial Planar Die Construction**
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
  - "Green" Device (Note 2)

## **Mechanical Data**

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)





Schematic and Pin Configuration

## **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-25	V
Collector-Emitter Voltage	$V_{CEO}$	-25	V
Emitter-Base Voltage	$V_{EBO}$	-5	V
Peak Pulse Current	I <sub>CM</sub>	-2	A
Continuous Collector Current	Ic	-1	А

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 3) @ T <sub>A</sub> = 25°C	P <sub>D</sub>	1	W
Thermal Resistance, Junction to Ambient Air (Note 3) @ T <sub>A</sub> = 25°C	$R_{ heta JA}$	125	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

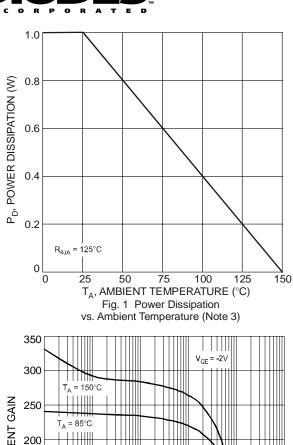
## Electrical Characteristics @TA = 25°C unless otherwise specified

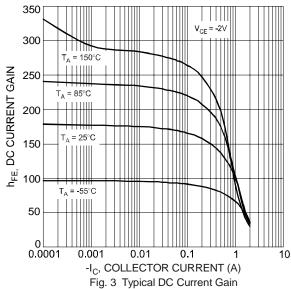
Characteristic	Symbol	Min	Тур	Max	Unit	Conditions
OFF CHARACTERISTICS (Note 4)						
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	-25	_	_	V	$I_C = -10\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	-25	_	_	V	$I_C = -1 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	-5	_	_	V	$I_E = -10\mu A, I_C = 0$
Collector Cut-Off Current	I <sub>CBO</sub>	_	_	-0.1	μΑ	$V_{CB} = -20V, I_{E} = 0$
Emitter Cut-Off Current	I <sub>EBO</sub>	_	_	-0.1	μΑ	$V_{EB} = -4V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)						
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	_	-0.15	-0.7	V	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Base-Emitter Saturation Voltage	V <sub>BE(SAT)</sub>	_	-0.85	-1.2	V	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
DC Current Gain	h	140	_	280		$V_{CE} = -2V, I_{C} = -50mA$
	h <sub>FE</sub>	40	_	_	_	$V_{CE} = -2V, I_{C} = -1A$
SMALL SIGNAL CHARACTERISTICS						
Transition Frequency	f <sub>T</sub>	_	200	_	MHz	$V_{CE} = -10V, I_{C} = -50mA$ f = 100MHz
Output Capacitance	C <sub>ob</sub>	_	12	_	pF	$V_{CB} = -10V, I_{E} = 0,$ f = 1MHz

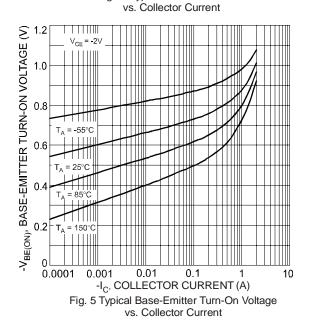
Notes:

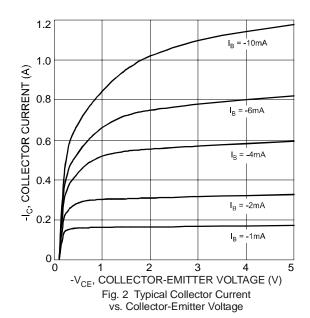
- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead\_free/index.php.
- Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 4. Measured under pulsed conditions. Pulse width =  $300\mu s$ . Duty cycle  $\leq 2\%$ .

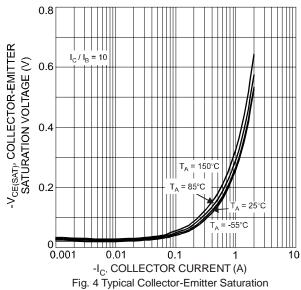


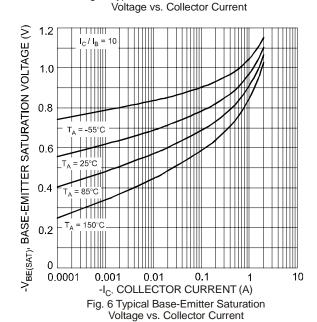




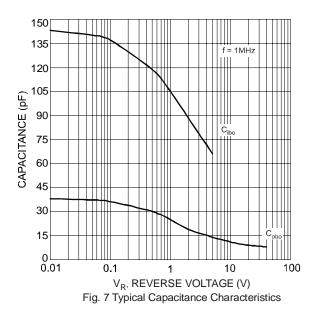












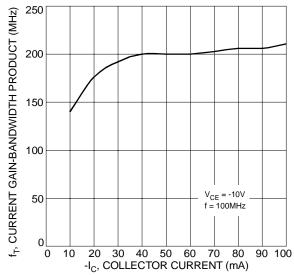


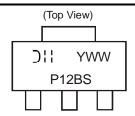
Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

## **Ordering Information** (Note 5)

Device	Packaging	Shipping
2DB1119S-13	SOT89-3L	2500/Tape & Reel

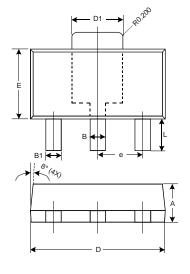
5. For packaging details, please see below or go to our website at http://www.diodes.com/ap02007.pdf.

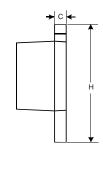
# **Marking Information**



P12BS = Product Type Marking Code YWW = Date Code Marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

# **Package Outline Dimensions**

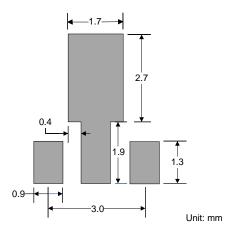




SOT89-3L					
Dim	Min	Max	Тур		
Α	1.40	1.60	1.50		
В	0.45	0.55	0.50		
B1	0.37	0.47	0.42		
С	0.35	0.43	0.38		
D	4.40	4.60	4.50		
D1	1.50	1.70	1.60		
Е	2.40	2.60	2.50		
е	_	_	1.50		
Н	3.95	4.25	4.10		
L	0.90	1.20	1.05		
All Dimensions in mm					



## **Suggested Pad Layout**



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