

# Power Rectifier MURHD560T4G, MURHD560W1T4G,

#### **Features and Benefits**

- Ultrafast 30 Nanosecond Recovery Times
- 175°C Operating Junction Temperature
- High Temperature Glass Passivated Junction
- High Voltage Capability to 600 Volts
- SURHD8 Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Applications**

- Power Supplies
- Inverters
- Free Wheeling Diodes

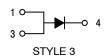
#### **Mechanical Characteristics**

- · Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 0.4 g (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- ESD Ratings:
  - ♦ Machine Model = C (> 400 V)
  - ♦ Human Body Model = 3B (> 8000 V)

## ULTRAFAST RECTIFIER 5.0 AMPERES 600 VOLTS

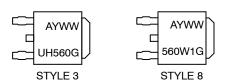


DPAK CASE 369C STYLES 3, 8





#### MARKING DIAGRAM



UH560 = MURHD560T4 560W1 = MURHD560W1T4 A = Assembly Location

Y = Year
WW = Work Week
G = Pb-Free Package

\* The Assembly Location code (A) is front side optional. In cases where the Assembly Location is stamped in the package bottom (molding ejecter pin), the front side assembly code may be blank.

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MURHD560T4G	DPAK (Pb-Free)	2500 / Tape & Reel
MURHD560W1T4G	DPAK (Pb-Free)	2500 / Tape & Reel
SSURHD8560W1T4G	DPAK (Pb-Free)	2500 / Tape & Reel

#### **DISCONTINUED** (Note 1)

1

SURHD8560T4G	DPAK (Pb-Free)	2500 / Tape & Reel
SURHD8560W1T4G	DPAK (Pb-Free)	2500 / Tape & Reel
SSURHD8560T4G-VF01	DPAK (Pb-Free)	2500 / Tape & Reel

- †For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
- DISCONTINUED: This device is not recommended for new design. Please contact your onsemi representative for information. The most current information on this device may be available on <a href="https://www.onsemi.com">www.onsemi.com</a>.

#### MURHD560T4G, MURHD560W1T4G,

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	600	V
Average Rectified Forward Current (Rated V <sub>R</sub> , T <sub>C</sub> = 159°C)	I <sub>F(AV)</sub>	5.0	А
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I <sub>FSM</sub>	50	Α
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Maximum Thermal Resistance, Junction to Case	$R_{ heta JC}$	2.5	°C/W
Maximum Thermal Resistance, Junction to Ambient (Note 1)	$R_{ heta JA}$	49.5	°C/W

<sup>1.</sup> Rating applies when surface mounted on a 1.5 mm FR4 PC board with a 1 oz. thick, 700 mm² Cu area.

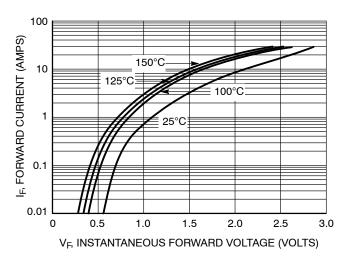
#### **ELECTRICAL CHARACTERISTICS**

Rating	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 2) ( $I_F = 5.0 \text{ Amps}, T_C = 25^{\circ}\text{C}$ ) ( $I_F = 5.0 \text{ Amps}, T_C = 125^{\circ}\text{C}$ )	V <sub>F</sub>	2.7 1.65	٧
Maximum Instantaneous Reverse Current (Note 2) (Rated dc Voltage, T <sub>C</sub> = 25°C) (Rated dc Voltage, T <sub>C</sub> = 125°C)	I <sub>R</sub>	10 70	μА
Maximum Reverse Recovery Time (I <sub>F</sub> = 1.0 Amp, di/dt = 50 Amps/μs, V <sub>R</sub> = 30 V, T <sub>J</sub> = 25°C)	t <sub>rr</sub>	30	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

<sup>2.</sup> Pulse Test: Pulse Width = 300 μs, Duty Cycle ≤ 2.0%.

#### MURHD560T4G, MURHD560W1T4G,



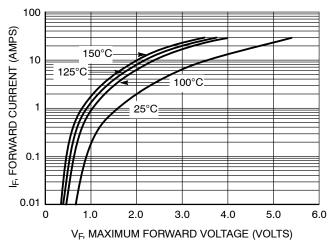


Figure 1. Typical Forward Voltage

Figure 2. Maximum Forward Voltage

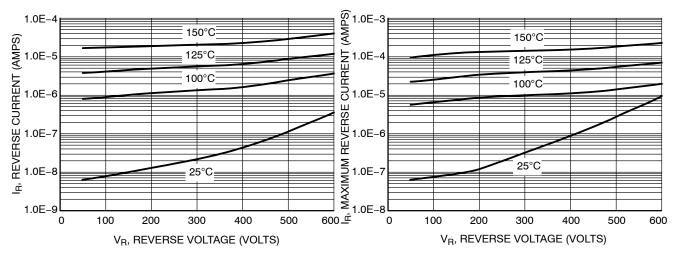


Figure 3. Typical Reverse Current

**Figure 4. Maximum Reverse Current** 

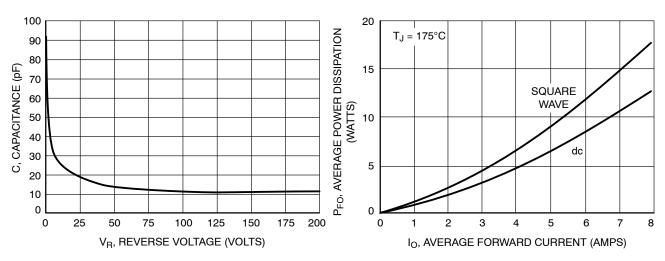


Figure 5. Typical Capacitance

Figure 6. Forward Power Dissipation

#### MURHD560T4G, MURHD560W1T4G,

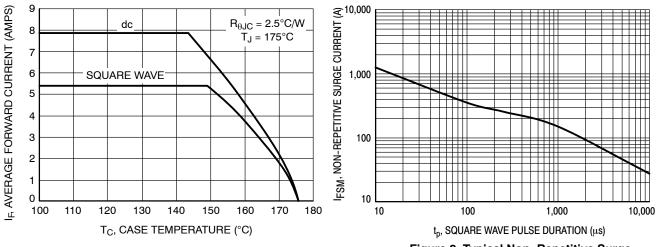


Figure 7. Current Derating

Figure 8. Typical Non-Repetitive Surge Current

<sup>\*</sup> Typical performance based on a limited sample size. ON Semiconductor does not guarantee ratings not listed in the Maximum Ratings table.

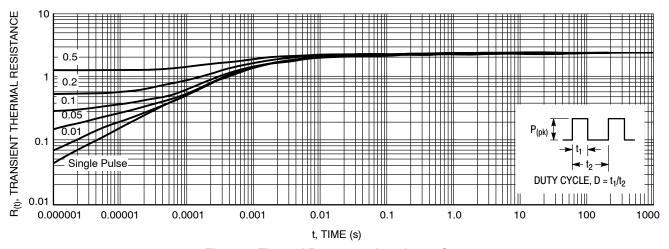


Figure 9. Thermal Response, Junction to Case

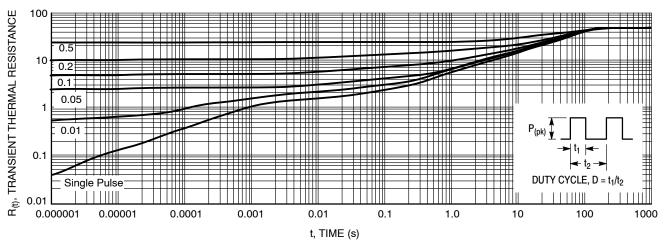


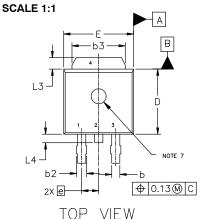
Figure 10. Thermal Response, Junction to Ambient

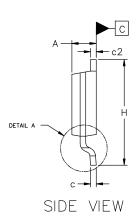




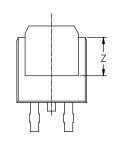
#### DPAK3 6.10x6.54x2.28, 2.29P CASE 369C **ISSUE J**

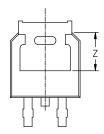
**DATE 12 AUG 2025** 

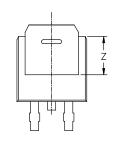


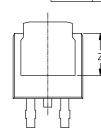


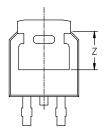
MILLIMETERS				
DIM	MIN	NOM	MAX	
А	2.18	2.28	2.38	
A1	0.00		0.13	
b	0.63	0.76	0.89	
b2	0.72	0.93	1.14	
b3	4.57	5.02	5.46	
С	0.46	0.54	0.61	
c2	0.46	0.54	0.61	
D	5.97	6.10	6.22	
E	6.35	6.54	6.73	
е	:	2.29 BSC		
Н	9.40	9.91	10.41	
L	1.40	1.59	1.78	
L1	2.90 REF			
L2	0.51 BSC			
L3	0.89		1.27	
L4			1.01	
Z	3.93			











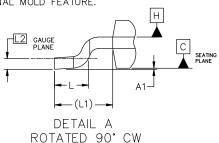
BOTTOM VIEW

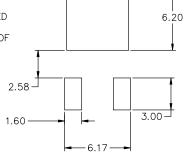
ALTERNATE CONSTRUCTIONS

#### NOTES:

- DIMENSIONING AND TOLERANCING ASME Y14.5M, 2018.

- CONTROLLING DIMENSION: MILLIMETERS.
  THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS b3, L3, AND Z.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR
  BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.15mm PER SIDE.
- DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- DATUMS A AND B ARE DETERMINED AT DATUM PLANE H. OPTIONAL MOLD FEATURE.





-5.80

RECOMMENDED MOUNTING FOOTPRINT\*

\*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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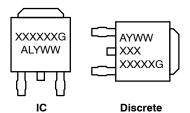
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#### DPAK3 6.10x6.54x2.28, 2.29P

CASE 369C ISSUE J

**DATE 12 AUG 2025** 

### GENERIC MARKING DIAGRAM\*



XXXXXX = Device Code

A = Assembly Location

L = Wafer Lot

Y = Year

WW = Work Week

G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 2: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN	STYLE 3: PIN 1. ANODE 2. CATHODE 3. ANODE 4. CATHODE	STYLE 4: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE	STYLE 5: PIN 1. GATE 2. ANODE 3. CATHODE 4. ANODE
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STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:
PIN 1. MT1	PIN 1. GATE	PIN 1. N/C	PIN 1. ANODE	PIN 1. CATHODE
2. MT2	<ol><li>COLLECTOR</li></ol>	<ol><li>CATHODE</li></ol>	2. CATHODE	2. ANODE
<ol><li>GATE</li></ol>	<ol><li>EMITTER</li></ol>	<ol><li>ANODE</li></ol>	<ol><li>RESISTOR ADJUST</li></ol>	<ol><li>CATHODE</li></ol>
4. MT2	<ol><li>COLLECTOR</li></ol>	<ol><li>CATHODE</li></ol>	4. CATHODE	<ol><li>ANODE</li></ol>

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