

NTE957 Integrated Circuit 3-Terminal Adjustable Negative Voltage Regulator

Description:

The NTE957 is an adjustable 3–terminal negative voltage regulator in a TO220 type package capable of supplying in excess of –1.5A over a –1.2V to –37V output range. The circuit design has been optimized for excellent regulation and low thermal transients. Further, the NTE957 features internal current limiting, thermal shutdown, and safe–area compensation, making this device virtually blowout–proof against overloads.

The NTE957 serves a wide variety of applications including local on–card regulation, programmable–output voltage regulation or precision current regulation. The NTE957 is the ideal complement to the NTE956 adjustable positive regulator.

Features:

- Output Voltage Adjustable from −1.2V to −37V
- Guaranteed 1.5A Output Current
- Line Regulation Typically 0.01%/V
- Load Regulation Typically 0.3%
- Excellent Thermal Regulation: 0.002%/W
- 77dB Ripple Rejection
- Temperature–Independent Current Limit
- Internal Thermal Overload Protection
- 100% Electrical Burn-In
- Eliminates the Need to Stock Many Voltages

Absolute Maximum Ratings:

Power Dissipation, P _D	Internally Limited
Input–Output Voltage Differential, V _I –V _O	40V
Operating Junction Temperature Range, T _J	0° to +125°C
Storage Temperature Range, T _{stq}	65° to +150°C
Typical Thermal Resistance, Junction-to-Case, R _{thJC}	4°C/W
Lead Temperature (During Soldering, 10sec), T _L	+300°C

<u>Electrical Characteristics:</u> $(0^{\circ} \le T_J \le +125^{\circ}C, \ V_{IN} - V_{OUT} = 5V, \ I_O = 500 mA, \ I_{MAX} = 1.5A, \ Note 1 unless otherwise specified)$

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
Line Regulation	Reg _{line}	$T_A = +25^{\circ}C$, $3V \le (V_{IN}-V_{OUT}) \le 40V$, Note 2		_	0.01	0.04	%/V
	$3V \le (V_{IN} - V_{OUT}) \le 40V$			_	0.02	0.07	%/V
Load Regulation	Reg _{load}	$T_{A} = +25^{\circ}C, \ \ 10mA \leq I_{O} \leq I_{MAX},$ Note 2	V _{OUT} ≤ 5V	_	15	50	mV
			V _{OUT} ≥ 5V	_	0.3	1.0	%
		$10\text{mA} \le I_O \le 1_{MAX}$, Note 2	V _{OUT} ≤ 5V	_	20	70	mV
			V _{OUT} ≥ 5V	_	0.3	1.5	%
Thermal Regulation		T _A = +25°C, 20ms Pulse		_	0.003	0.04	%/W
Adjustment Pin Current	I_{Adj}			_	65	100	μΑ
Adjustment Pin Current Change	ΔI_{Adj}	$\begin{array}{l} 10\text{mA} \leq I_L \leq I_{MAX}, \ 2.5\text{V} \leq (V_{IN}\!\!-\!\!V_{OUT}) \leq 40\text{V}, \\ T_A = +25^{\circ}\text{C} \end{array}$		_	2	5	μΑ
Reference Voltage $V_{ref} = V_{ref} = V_{ref$			-1.213	-1.250	-1.287	V	
		$I_O \le 1_{MAX}$	-1.200	-1.250	-1.300	V	
Temperature Stability	T _S	0° ≤ T _J ≤ +125°C		_	0.6	_	%
Minimum Load Current	I _{Lmin}	$(V_{IN}-V_{OUT}) \le 40V$		_	2.5	10	mΑ
		$(V_{IN}-V_{OUT}) \le 10V$		_	1.5	6.0	mΑ
Maximum Output Current	I _{max}	$V_{IN}-V_{OUT} \le 15V$		1.5	2.2	_	Α
Limit		$V_{IN}-V_{OUT} = 40V$		_	0.4	_	Α
RMS Output Noise, % of V _{OUT}	N	$T_A = +25^{\circ}C$, $10Hz \le f \le 10kHz$		_	0.003	_	%
Ripple Rejection Ratio	RR	V _{OUT} = 10V, f = 120Hz		_	60	_	dB
			$C_{ADJ} = 10\mu F$	66	77	_	dB
Long Term Stability	S	T _A = +125°C, 1000 Hours		_	0.3	1.0	%

- Note 1. Although power dissipation is internally limited, these specifications are applicable for power dissipations of 20W.
- Note 2. Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output voltage due to heating effects are covered under the specification for thermal regulation.

