

QUADRUPLE 2-INPUT POSITIVE NOR GATES

DESCRIPTION

The M74LS02P is a semiconductor integrated circuit containing 4 dual-input positive NOR and negative NAND gates.

FEATURES

- High breakdown input voltage ($V_i \geq 15V$)
- Low power dissipation ($P_d = 10mW$ typical)
- High speed ($t_{pd} = 6ns$ typical)
- Low output impedance
- Wide operating temperature range ($T_a = -20 \sim +75^\circ C$)

APPLICATION

General purpose, for use in industrial and consumer equipment.

FUNCTIONAL DESCRIPTION

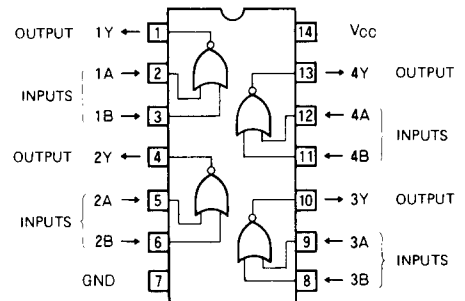
The use of Schottky TTL technology, enables the achievement of high input voltage, high speed, low power dissipation, and high fan-out.

When at least input A or input B is high, output Y is low, and when both A and B are low, Y is high.

FUNCTION TABLE

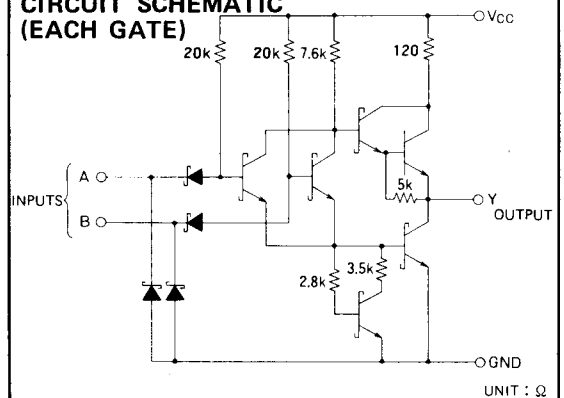
A	B	Y
L	L	H
H	L	L
L	H	L
H	H	L

PIN CONFIGURATION (TOP VIEW)



Outline 14P4

CIRCUIT SCHEMATIC (EACH GATE)



ABSOLUTE MAXIMUM RATINGS ($T_a = -20 \sim +75^\circ C$, unless otherwise noted)

Symbol	Parameter	Conditions	Limits	Unit
V_{CC}	Supply voltage		$-0.5 \sim +7$	V
V_i	Input voltage		$-0.5 \sim +15$	V
V_o	Output voltage	High-level state	$-0.5 \sim V_{CC}$	V
T_{opr}	Operating free-air ambient temperature range		$-20 \sim +75$	$^\circ C$
T_{stg}	Storage temperature range		$-65 \sim +150$	$^\circ C$

QUADRUPLE 2-INPUT POSITIVE NOR GATES

RECOMMENDED OPERATING CONDITIONS (Ta = -20 ~ +75°C, unless otherwise noted)

Symbol	Parameter		Limits			Unit
			Min	Typ	Max	
VCC	Supply voltage		4.75	5	5.25	V
IOH	High-level output current	VOH ≥ 2.7V	0		-400	μA
IOL	Low-level output current	VOL ≤ 0.4V	0		4	mA
		VOL ≤ 0.5V	0		8	mA

ELECTRICAL CHARACTERISTICS (Ta = -20 ~ +75°C, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ*	Max	
VIH	High-level input voltage		2			V
VIL	Low-level input voltage				0.8	V
VIC	Input clamp voltage	VCC = 4.75V, IIC = -18mA			-1.5	V
VOH	High-level output voltage	VCC = 4.75V, VI = 0.8V, IOH = -400μA	2.7	3.4		V
VOL	Low-level output voltage	VCC = 4.75V, VI = 2V, IOL = 4mA		0.25	0.4	V
		IOL = 8mA		0.35	0.5	V
IIH	High-level input current	VCC = 5.25V, VI = 2.7V			20	μA
		VCC = 5.25V, VI = 10V			0.1	mA
IIL	Low-level input current	VCC = 5.25V, VI = 0.4V			-0.4	mA
IOS	Short-circuit output current (Note 1)	VCC = 5.25V, VO = 0V	-20		-100	mA
IOCH	Supply current, all outputs high	VCC = 5.25V, VI = 0V		1.6	3.2	mA
IACL	Supply current, all outputs low	VCC = 5.25V, VI = 4.5V		2.8	5.4	mA

* : All typical values are at VCC = 5V, Ta = 25°C.

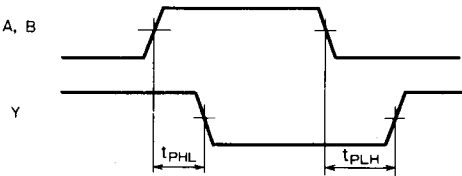
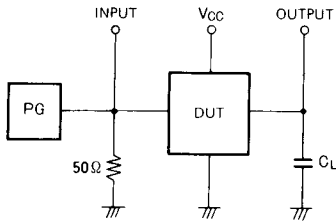
Note 1: All measurements should be done quickly, and not more than one output should be shorted at a time.

SWITCHING CHARACTERISTICS (VCC = 5V, Ta = 25°C, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
tPLH	Low-to-high-level output propagation time	CL = 15pF		6	15	ns
tPHL	High-to-low-level output propagation time	(Note 2)		6	15	ns

Note 2: Measurement circuit

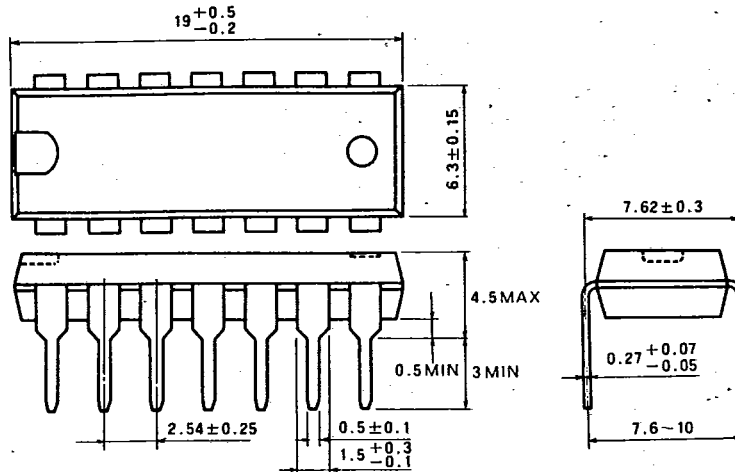
TIMING DIAGRAM (Reference level = 1.3V)



- (1) The pulse generator (PG) has the following characteristics:
PRR = 1MHz, tr = 6ns, tf = 6ns, tw = 500ns,
Vp = 3Vp.p., Zo = 50Ω
- (2) CL includes probe and jig capacitance.

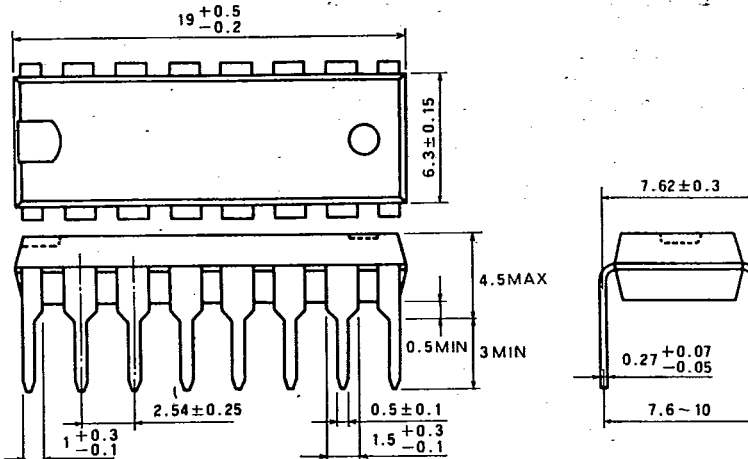
TYPE 14P4 14-PIN MOLDED PLASTIC DIL

Dimension in mm



TYPE 16P4 16-PIN MOLDED PLASTIC DIL

Dimension in mm



TYPE 20P4 20-PIN MOLDED PLASTIC DIL

Dimension in mm

