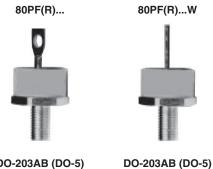


Vishay Semiconductors

Standard Recovery Diodes Generation 2 DO-5 (Stud Version), 80 A



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DO-203AB (DO-5)

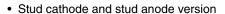
PRODUCT SUMMARY

I_{F(AV)}

80 A

FEATURES

- · High surge current capability
- Designed for a wide range of applications



- · Wire version available
- · Low thermal resistance
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for multiple level

TYPICAL APPLICATIONS

- Converters
- · Power supplies
- · Machine tool controls
- Welding
- · Any high voltage input rectification bridge

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
		80	А	
I _{F(AV)}	T _C	123	°C	
I _{F(RMS)}		126	А	
IFSM	50 Hz	1200	Α	
	60 Hz	1250	A	
I ² t	50 Hz	7100	A ² s	
	60 Hz	6450	A-S	
V _{RRM}	Range	1400 to 1600	V	
T _J		- 55 to 150	°C	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS						
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 150 °C mA		
80PF(R)(W)	140	1400	1650	4.5		
60FF(H)(VV)	160	1600	1900	4.5		

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80PF(R)...(W) High Voltage Series

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Standard Recovery Diodes Generation 2 DO-5 (Stud Version), 80 A



FORWARD CONDUCTION												
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS						
Maximum average forward current	I	180° conduction, half sine wave		80	Α							
at case temperature	I _{F(AV)}			123	°C							
Maximum RMS forward current	I _{F(RMS)}			126	Α							
		t = 10 ms	No voltage reapplied	Sinusoidal half wave,	1200	А						
Maximum peak, one cycle forward,		t = 8.3 ms			1250							
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM} reapplied		1000							
		t = 8.3 ms			1050							
		t = 10 ms	No voltage	initial T _J = 150 °C	7100							
Maximum 12t fax fusing	l ² t	l ² t	l ² t	12+	12+	12+	124	t = 8.3 ms	reapplied		6450	A ² s
Maximum I ² t for fusing				t = 10 ms	100 % V _{RRM}		5000	A-S				
		t = 8.3 ms	reapplied		4550							
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied			71 000	A²√s						
Low level value of threshold voltage	V _{F(TO)}	(16.7 % x π x I _{F(AV)} < I < π x I _{F(AV)}), T _J = T _J maximum 0.73 V			V							
Low level value of forward slope resistance	r _f	$(16.7 \% \text{ x } \pi \text{ x } _{F(AV)} < I < \pi \text{ x } _{F(AV)}), T_J = T_J \text{ maximum} \qquad 3.0 \qquad \text{m}\Omega$			mΩ							
Maximum forward voltage drop	V_{FM}	I_{pk} = 220 A, T_J = 25 °C, t_p = 400 μ s rectangular wave 1.46 V			٧							

THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.30	K/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, flat and greased	0.25	IV/VV
Maximum allowable mounting torque (+ 0 %, - 10 %)		Not lubricated thread, tighting on nut (1)	3.4 (30)	
		Lubricated thread, tighting on nut (1)	2.3 (20)	
		Not lubricated thread, tighting on hexagon (2)	4.2 (37)	(lbf · in)
		Lubricated thread, tighting on hexagon (2)	3.2 (28)	
Approximate weight			15.8	g
Approximate weight			0.56	OZ.
Case style		See dimensions - link at the end of datasheet	DO-203AB (DO-5)	

Notes

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⁽¹⁾ Recommended for pass-through holes

⁽²⁾ Torque must be appliable only to hexagon and not to plastic structure, recommended for holed heatsink

80PF(R)...(W) High Voltage Series

Standard Recovery Diodes Generation 2 DO-5 (Stud Version), 80 A

Vishay Semiconductors

△R _{thJC} CONDUCTION						
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS		
180°	0.14	0.10				
120°	0.16	0.17				
90°	0.21	0.22	$T_J = T_J \text{ maximum}$	K/W		
60°	0.30	0.31				
30°	0.50	0.50				

Note

The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC

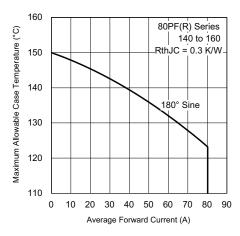


Fig. 1 - Current Ratings Characteristics

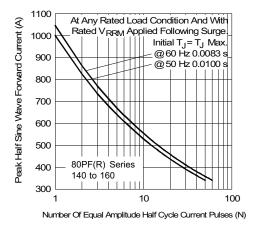


Fig. 2 - Maximum Non-Repetitive Surge Current

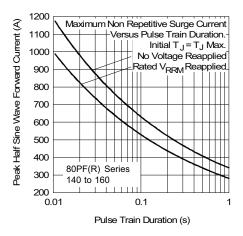


Fig. 3 - Maximum Non-Repetitive Surge Current

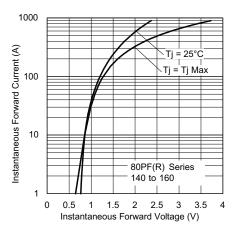


Fig. 4 - Forward Voltage Drop Characteristics

80PF(R)...(W) High Voltage Series

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Standard Recovery Diodes Generation 2 DO-5 (Stud Version), 80 A



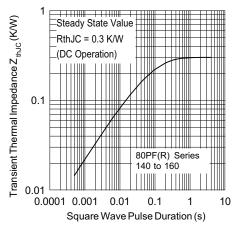
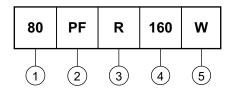


Fig. 5 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

Device code



- 1 80 = Standard device
- 2 PF = Plastic package
- None = Stud normal polarity (cathode to stud)
 - R = Stud reverse polarity (anode to stud)
- Voltage code x 10 = V_{RRM} (see Voltage Ratings table)
- None = Standard terminal
 (see dimensions for 80PF(R)... link at the end of datasheet)
 - W = Wire terminal (see dimensions for 80PF(R)...W - link at the end of datasheet)

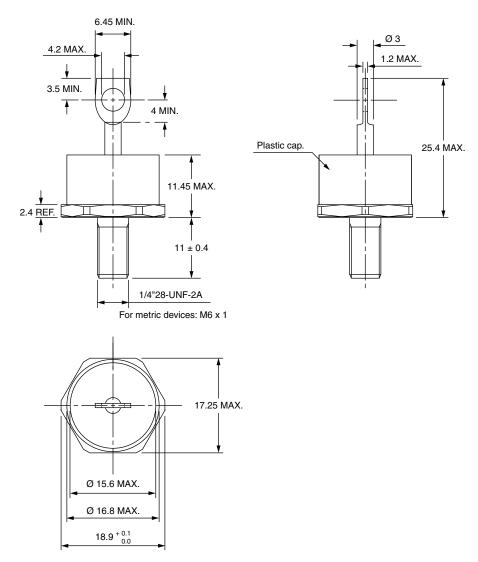
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95345			



Vishay Semiconductors

DO-203AB (DO-5) for 50PF(R)...(W), 80PF(R)...(W) and 95PF(R)...(W) Series

DIMENSIONS FOR 80PF(R), 50PF(R) AND 95PF(R) SERIES in millimeters



Note

• For metric device please contact factory

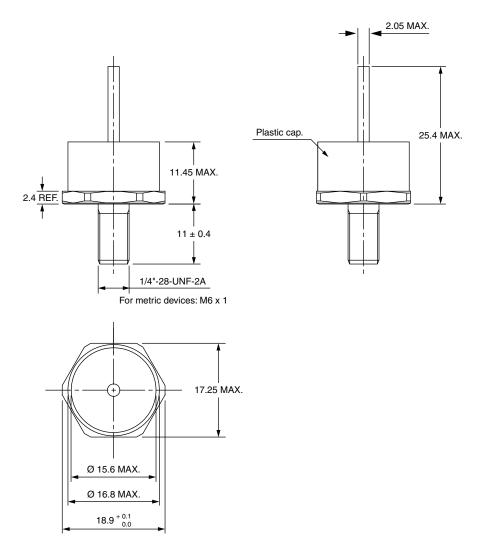
Outline Dimensions

Vishay Semiconductors

DO-203AB (DO-5) for 50PF(R)...(W), 80PF(R)...(W) and 95PF(R)...(W) Series



DIMENSIONS FOR 80PF(R)...(W), 50PF(R)...(W) AND 95PF(R)...(W) SERIES in millimeters



Note

• For metric device please contact factory

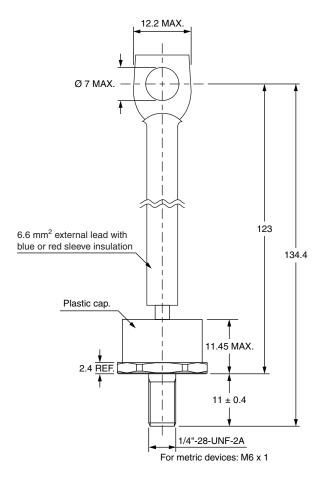
Document Number: 95345 Revision: 26-Aug-08



DO-203AB (DO-5) for 50PF(R)...(W), 80PF(R)...(W) and 95PF(R)...(W) Series

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DIMENSIONS FOR 52PF(R), 82PF(R) AND 97PF(R) SERIES in millimeters



Note

• For metric device please contact factory



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Revision: 02-Oct-12 Document Number: 91000

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