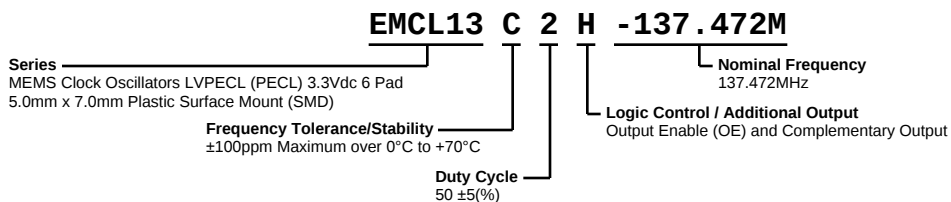


EMCL13C2H-137.472M



ELECTRICAL SPECIFICATIONS

Nominal Frequency	137.472MHz
Frequency Tolerance/Stability	$\pm 100\text{ppm}$ Maximum over 0°C to $+70^{\circ}\text{C}$ (Inclusive of all conditions: Calibration Tolerance at 25°C , Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C , Reflow, Shock, and Vibration)
Aging at 25°C	$\pm 1\text{ppm}$ First Year Maximum
Supply Voltage	$+3.3\text{Vdc} \pm 0.3\text{Vdc}$
Input Current	80mA Maximum (Excluding Load Termination Current)
Output Voltage Logic High (Voh)	Vdd-1.10Vdc Minimum, 2.40Vdc Typical, Vdd-0.70Vdc Maximum
Output Voltage Logic Low (Vol)	Vdd-2.0Vdc Minimum, 1.60Vdc Typical, Vdd-1.40Vdc Maximum
Rise/Fall Time	150pSec Typical, 300pSec Maximum (Measured over 20% to 80% of waveform)
Duty Cycle	$50 \pm 5(\%)$ (Measured at 50% of waveform)
Output Swing (VOpp)	600mVdc Minimum, 800mVdc Typical, 1000mVdc Maximum
Load Drive Capability	50 Ohms into Vdd-2.0Vdc
Output Logic Type	LVPECL
Logic Control / Additional Output	Output Enable (OE) and Complementary Output
Output Control Input Voltage	Vih of 70% of Vdd Minimum or No Connect to Enable Output and Complementary Output, Vil of 30% of Vdd Maximum to Disable Output and Complementary Output (High Impedance)
Output Enable Current	75mA Maximum (Without Load)
Period Jitter (Deterministic)	0.2pSec Typical
Period Jitter (Random)	2.0pSec Typical
Period Jitter (RMS)	1.5pSec Typical, 3.0pSec Maximum
Period Jitter (pk-pk)	20pSec Typical, 25pSec Maximum
RMS Phase Jitter (Fj = 637kHz to 10MHz; Random)	1.6pSec Typical
RMS Phase Jitter (Fj = 1MHz to 20MHz; Random)	1.0pSec Typical
RMS Phase Jitter (Fj = 1.875MHz to 20MHz; Random)	0.5pSec Typical
Start Up Time	10mSec Maximum
Storage Temperature Range	-55°C to $+125^{\circ}\text{C}$

ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

ESD Susceptibility	MIL-STD-883, Method 3015, Class 2, HBM 2000V
Flammability	UL94-V0
Mechanical Shock	MIL-STD-883, Method 2002, Condition G, 30,000G
Moisture Resistance	MIL-STD-883, Method 1004
Moisture Sensitivity Level	J-STD-020, MSL 1
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition K
Resistance to Solvents	MIL-STD-202, Method 215
Solderability	MIL-STD-883, Method 2003 (Six I/O Pads on bottom of package only)
Temperature Cycling	MIL-STD-883, Method 1010, Condition B
Thermal Shock	MIL-STD-883, Method 1011, Condition B
Vibration	MIL-STD-883, Method 2007, Condition A, 20G

The drawing consists of three views of a rectangular component:

- Front View (Left):** A rectangle with a width of 5.00 ± 0.15 and a height of 7.00 ± 0.15 . It features a circular marking in the top-left corner and the text "MARKING ORIENTATION" printed vertically in the center.
- Side View (Middle):** A narrow rectangle with a thickness of 0.85 ± 0.15 . It shows a central slot with a maximum depth of 0.08 MAX .
- Top View (Right):** A rectangle with overall dimensions of 5.08 ± 0.10 by 2.60 ± 0.15 . It contains a central rectangular feature labeled "A" with a width of 1.20 ± 0.10 (multiplied by 6) and a height of 2.30 . Six circular features are numbered 1 through 6. The distance from the top edge to the center of feature 6 is 5.08 ± 0.10 . The distance from the bottom edge to the center of feature 4 is 2.54 TYP . The distance from the right edge to the center of feature 1 is 1.47 . The distance from the right edge to the center of feature 3 is 1.40 ± 0.10 (multiplied by 6).

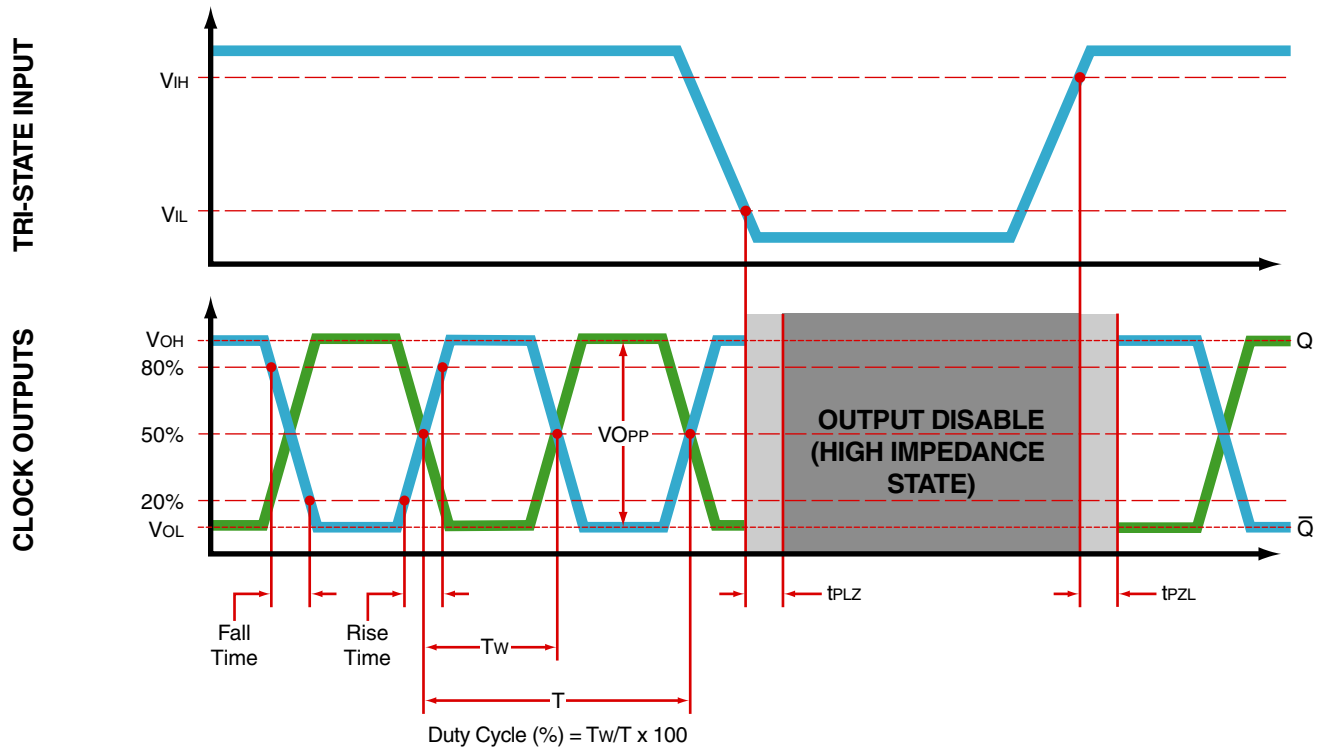
LINE	MARKING
1	XXXX or XXXXX XXXX or XXXXX=Ecliptek Manufacturing Identifier

Figure 1: Dimensions of the test board. The diagram shows a 3x2 grid of blue square components. Dimensions are given in inches and multiplied by a factor in parentheses. Horizontal dimensions: 1.80 (X6) between columns, 2.39 (X6) from the left edge to the first column. Vertical dimensions: 0.54 (X4) between rows, 2.00 (X6) from the top edge to the first row. A label 'Solder Land (X6)' points to the bottom-right component.

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OUTPUT WAVEFORM & TIMING DIAGRAM



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OUTPUT WAVEFORM & TIMING DIAGRAM



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Test Circuit for Output Enable and Complementary Output



Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>500MHz) passive probe is recommended.

Note 3: Test circuit PCB traces need to be designed for a characteristic line impedance of 50 ohms.

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Test Circuit for Standby and Complementary Output



Note 1: An external 0.01 μF ceramic bypass capacitor in parallel with a 0.1 μF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>500MHz) passive probe is recommended.

Note 3: Test circuit PCB traces need to be designed for a characteristic line impedance of 50 ohms.

Recommended Solder Reflow Methods



High Temperature Infrared/Convection

T_S MAX to T_L (Ramp-up Rate)	3°C/second Maximum
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Preheat

- Temperature Minimum (T _S MIN)	150°C
- Temperature Typical (T _S TYP)	175°C
- Temperature Maximum (T _S MAX)	200°C
- Time (t _s MIN)	60 - 180 Seconds

Ramp-up Rate (T_L to T_P)	3°C/second Maximum
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Time Maintained Above:

- Temperature (T _L)	217°C
- Time (t _L)	60 - 150 Seconds

Peak Temperature (T_P)	260°C Maximum for 10 Seconds Maximum
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Target Peak Temperature (T_P Target)	250°C +0/-5°C
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Time within 5°C of actual peak (t_p)	20 - 40 seconds
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Ramp-down Rate	6°C/second Maximum
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Time 25°C to Peak Temperature (t)	8 minutes Maximum
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Moisture Sensitivity Level	Level 1
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Recommended Solder Reflow Methods



Low Temperature Infrared/Convection 240°C

Ts MAX to Tl (Ramp-up Rate) 5°C/second Maximum

Preheat

- Temperature Minimum (Ts MIN) N/A
- Temperature Typical (Ts TYP) 150°C
- Temperature Maximum (Ts MAX) N/A
- Time (ts MIN) 60 - 120 Seconds

Ramp-up Rate (Tl to Tp) 5°C/second Maximum

Time Maintained Above:

- Temperature (Tl) 150°C
- Time (tL) 200 Seconds Maximum

Peak Temperature (Tp) 240°C Maximum

Target Peak Temperature (Tp Target) 240°C Maximum 2 Times / 230°C Maximum 1 Time

Time within 5°C of actual peak (tp) 10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time

Ramp-down Rate 5°C/second Maximum

Time 25°C to Peak Temperature (t) N/A

Moisture Sensitivity Level Level 1

Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.