



AK1220

900MHz High Linearity Mixer

1. Overview

The AK1220 is high linearity mixer. RF and Lo frequency range coverage from 100 to 900MHz and IF coverage is from 20 to 100MHz. The RF input provides single-ended 50Ω interface. Lo ports are 50Ω matched and complementary inputs should be decoupled to the ground. IF output ports are differential open drain outputs.

The linearity and Power consumption performances can optimize with the resistance connected to the BIAS Pin.

2. Features

- ☐ Operating Frequency: 100 to 900MHz
- ☐ Linearity vs Power Selectable architecture
 - Power Consumption: 17mA, IIP3:+22dBm, Gain:1dB, NF:10.5dB
 - Power Consumption: 9mA, IIP3:+11dBm, Gain:0.5dB, NF:10dB
- ☐ Lo input level: 0dBm±5dB
- ☐ Operating Supply Voltage: 4.75 to 5.25 V
- ☐ Package: 16pin UQFN(0.5mm pitch, 3mm x 3mm x 0.60mm)
- ☐ Operating Temperature Range: -40 to 85°C

3. Applications

- ☐ Two-way Radios (PMR/LMR)
- ☐ Radio Communications for disaster prevention
- ☐ Marine Radios
- ☐ Amateur Radios

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5. Block Diagram

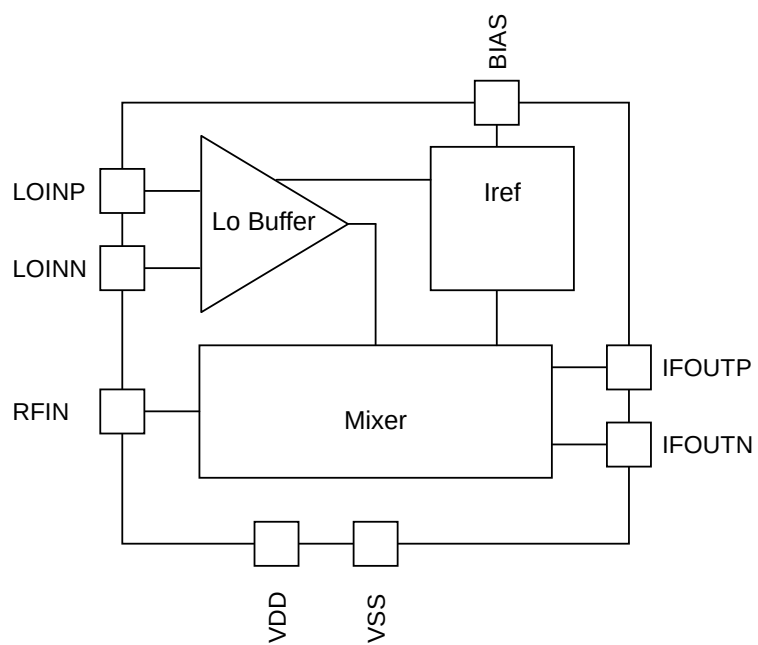


Figure 1. Block Diagram

6. System Diagram

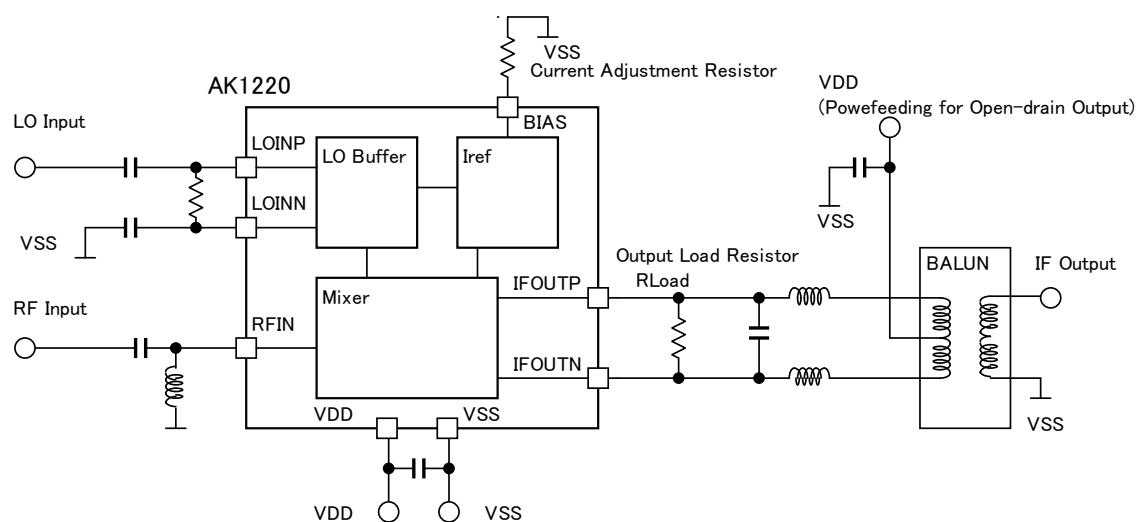


Figure 2. System Diagram

7. Pin Functional Description

Table 1 Pin Function

No.	Name	I/O	Pin Functions	Remarks
1	RFIN	AI	RF Input	Connecting a inductor between this pin and ground.
2	VSS	G	Ground pin	
3	LOINN	AI	Lo Input Negative	
4	LOINP	AI	Lo Input Positive	
5	NC	-	Non Connect	
6	NC	-	Non Connect	
7	NC	-	Non Connect	
8	NC	-	Non Connect	
9	BIAS	AIO	Resistance pin for current adjustment	Connecting a resistor between this pin and ground.
10	VDD	P	Power Supply	
11	IFOUTN	AO	IF Output Negative	This pin is open drain output. It needs power feeding via a inductor.
12	IFOUTP	AO	IF Output Positive	This pin is open drain output. It needs power feeding via a inductor.
13	NC	-	Non Connect	
14	NC	-	Non Connect	
15	NC	-	Non Connect	
16	NC	-	Non Connect	

Note) It is recommended to connect NC pins to ground, although it will not make any impact on the electrical characteristics if the pin is open.

AI: Analog input pin	AO: Analog output pin	AIO: Analog I/O pin
P: Power supply pin	G: Ground pin	

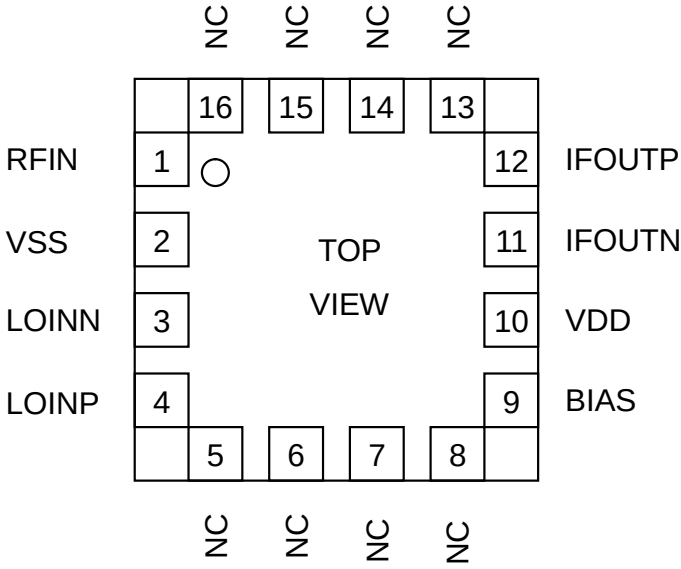


Figure 3. Package Pin Layout

8. Absolute Maximum Ratings

Table 2 Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	VDD	-0.3	5.5	V	
RF Input Power	RFPOW		12	dBm	
LO Input Power	LOPOW		12	dBm	
Storage Temperature	Tstg	-55	125	°C	

Exceeding these maximum ratings may result in damage to the AK1220. Normal operation is not guaranteed at these extremes.

9. Recommended Operating Range

Table 3 Recommended Operating Range

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remarks
Operating Temperature	Ta	-40		85	°C	
Supply Voltage	VDD	4.75	5	5.25	V	

The specifications are applicable within the recommended operating range (supply voltage/operating temperature).

10. Electrical Characteristics

1. Analog Circuit Characteristics

Unless otherwise noted IF output=50MHz, Lo Input Level=-5dBm to +5dBm,

Output Load Resistor (R_{Load})=2.2k Ω , VDD=4.75 to 5.25V, Ta=-40 to 85°C

Parameter		Min.	Typ.	Max.	Unit	Remarks
RF Input Frequency		100		900	MHz	
Lo Input Frequency		100		900	MHz	
IF output Frequency		20		100	MHz	
Lo Input Power		-5	0	+5	dBm	
Current Adjustment Resistor(BIAS)		22		56	kΩ	
IDD	BIAS=22kΩ		17	24	mA	The total current of VDD pin, IFOUTP pin and IFOUTN pin.
	BIAS=56kΩ		9	13	mA	
RFIN=600MHz, Output Load Resistor = 22kΩ						
Conversion Gain		-1	1	3	dB	
SSB Noise Figure			10.5	13	dB	
IP1dB		+4	+7		dBm	
IIP3		+19	+22		dBm	
RFIN=600MHz, Output Load Resistor = 56kΩ						
Conversion Gain		-1.5	0.5	2.5	dB	
SSB Noise Figure			10	12.5	dB	
IP1dB		-2	+1		dBm	
IIP3		+8	+11		dBm	

Note 1) In the shipment test, NC pins and the exposed pad on the center of the back of the package is connected to ground.

11. Typical Performance

Unless otherwise noted, RF input =600MHz, Lo input =550MHz,IF output =50MHz,

Output Load Resistor (R_{Load})=2.2k Ω

1. Current Adjustment Resistor vs. IIP, NF, P1dB, Gain, IDD

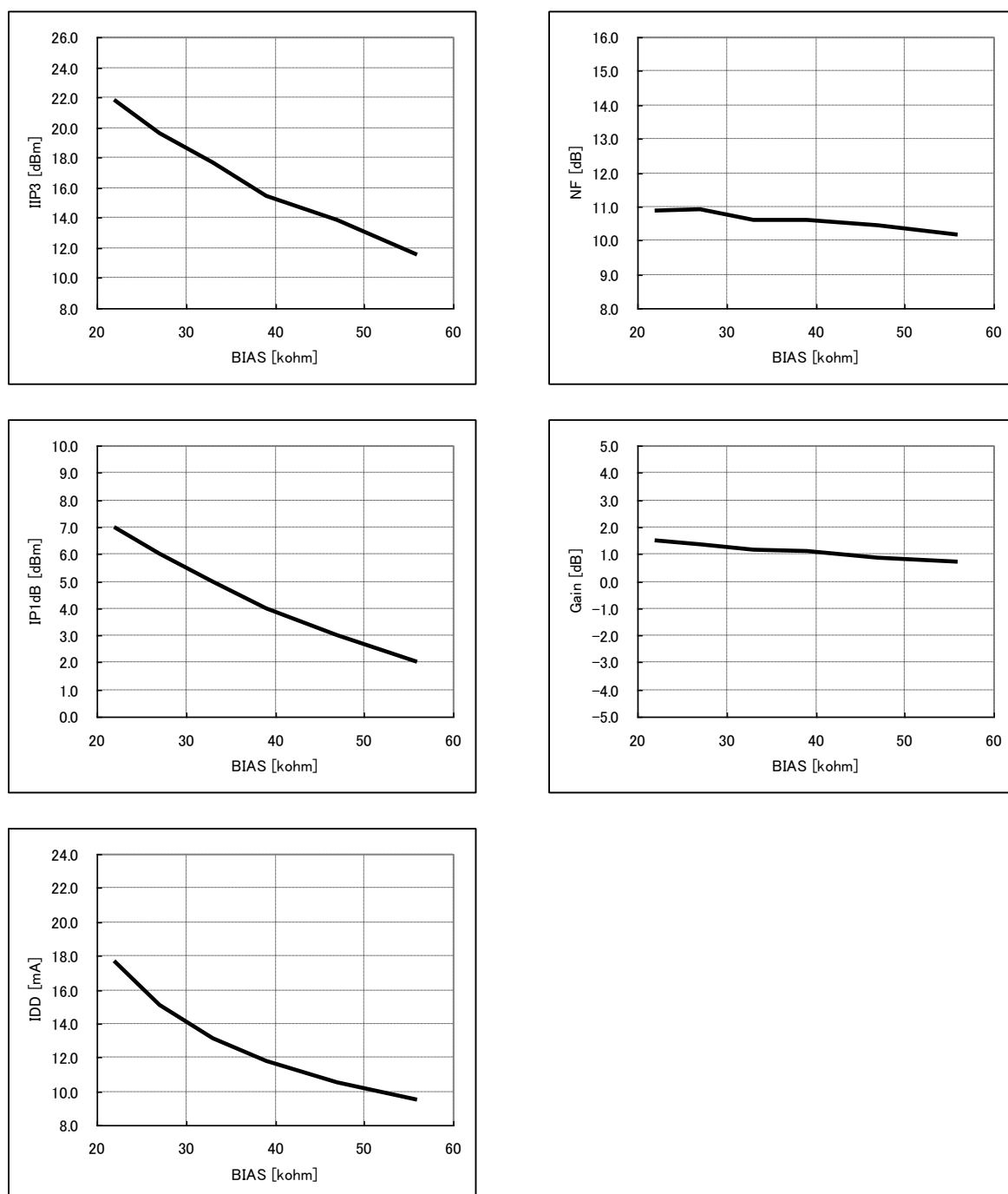


Figure 4. Current Adjustment Resistor vs. IIP3, NF, P1dB, Gain, IDD

Note 1) A resistor with 5% tolerance are used.

2. Over temperature vs. IIP3, NF, P1dB, Gain, IDD

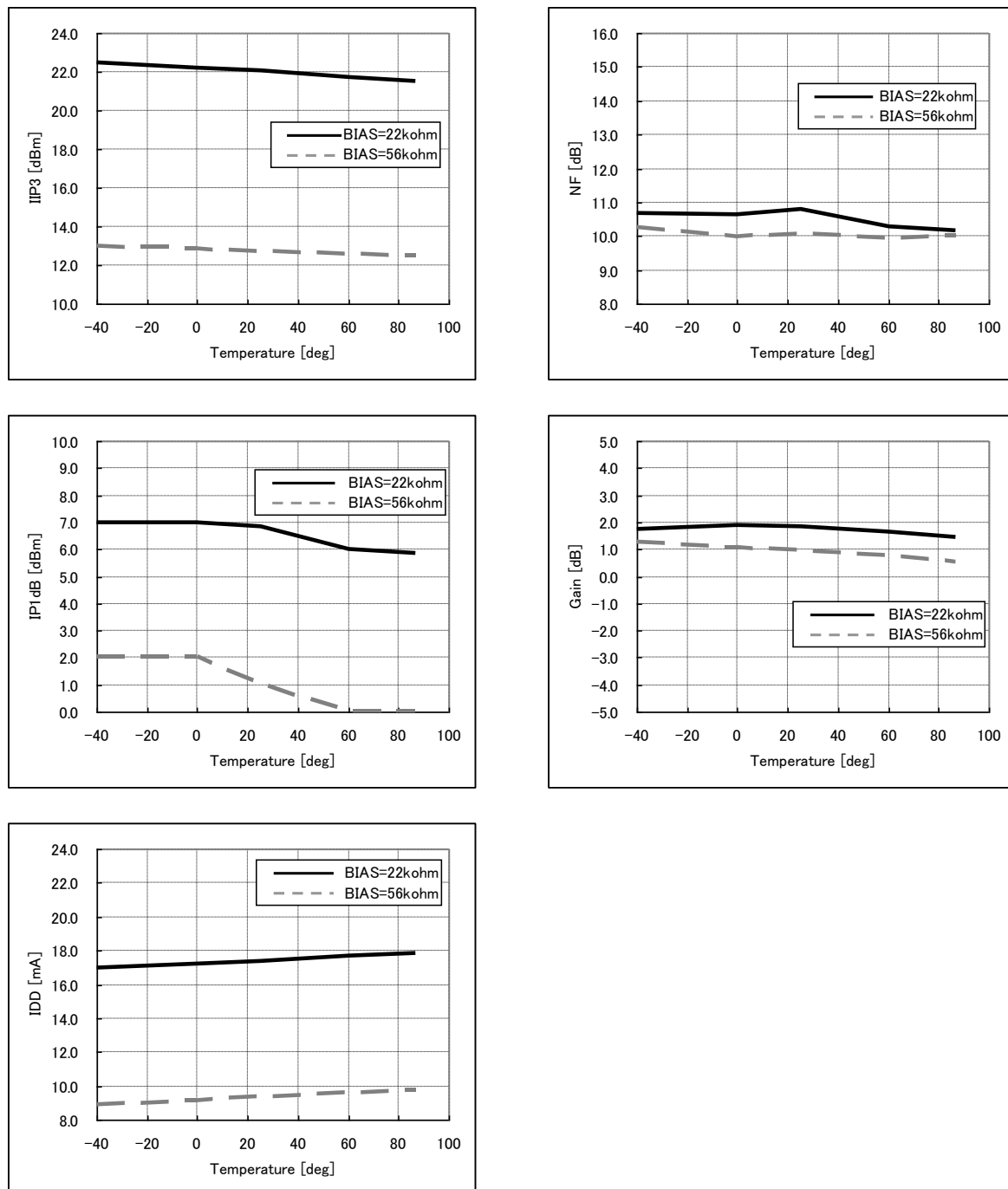


Figure 5. Over temperature vs. IIP3, NF, P1dB, Gain, IDD

3. Supply voltage vs. IIP3, NF, P1dB, Gain, IDD

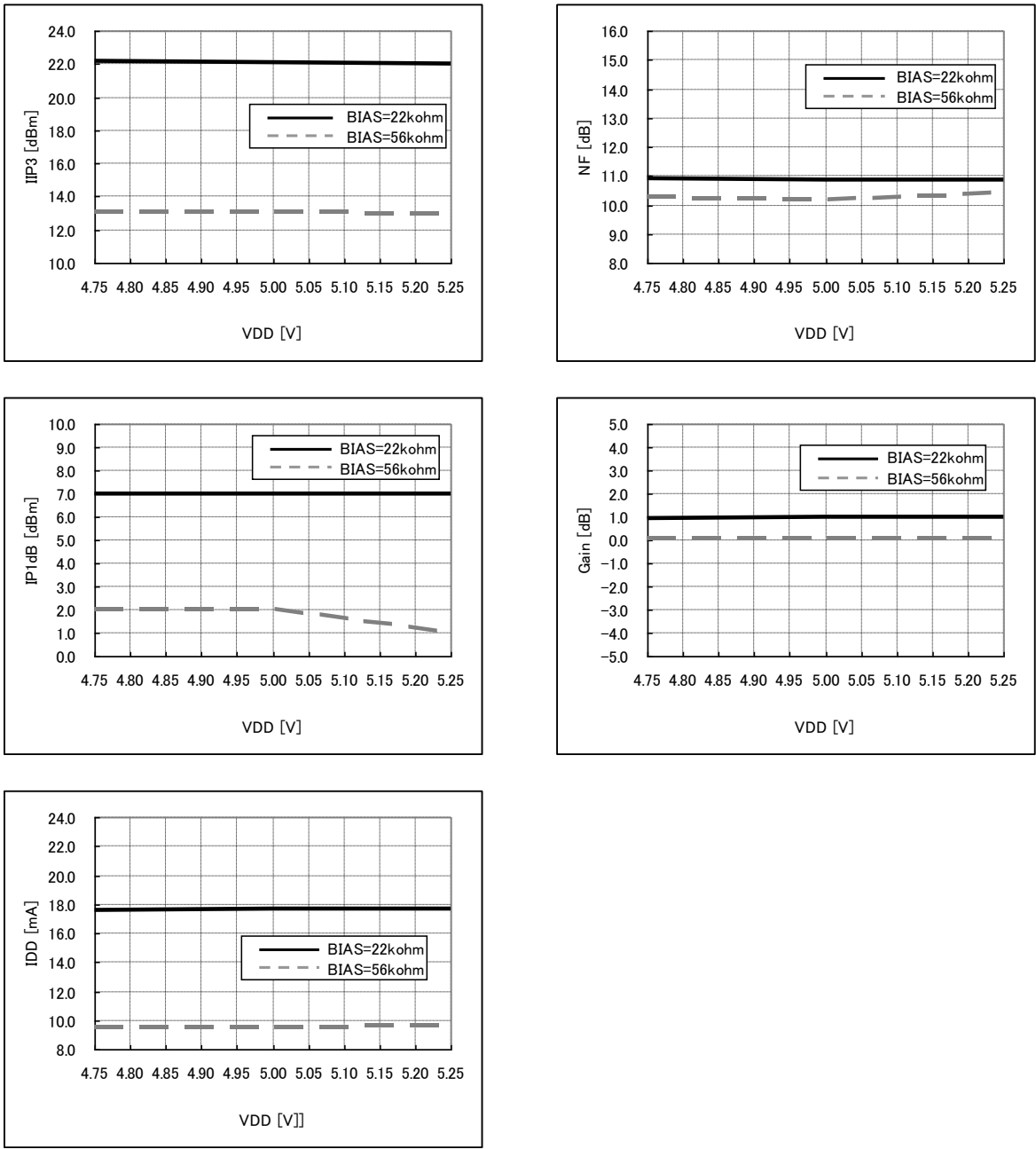


Figure 6. Supply voltage vs. IIP3, NF, P1dB, Gain, IDD

4. RF input frequency vs. IIP3, NF, P1dB, Gain

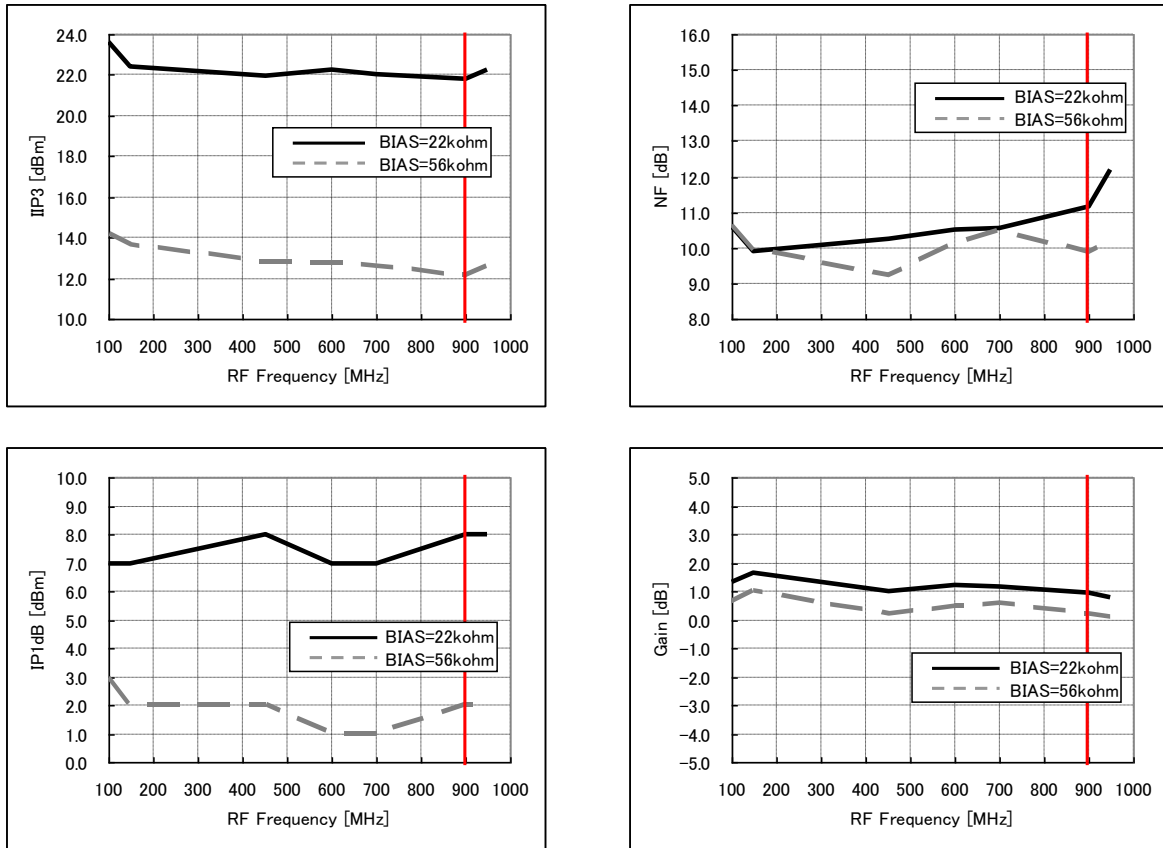


Figure 7. RF input frequency vs. IIP3, NF, P1dB, Gain

Note 1) AK1220 supports 100MHz to 900MHz RF Input.

5. IF output frequency vs. IIP3, NF, P1dB, Gain

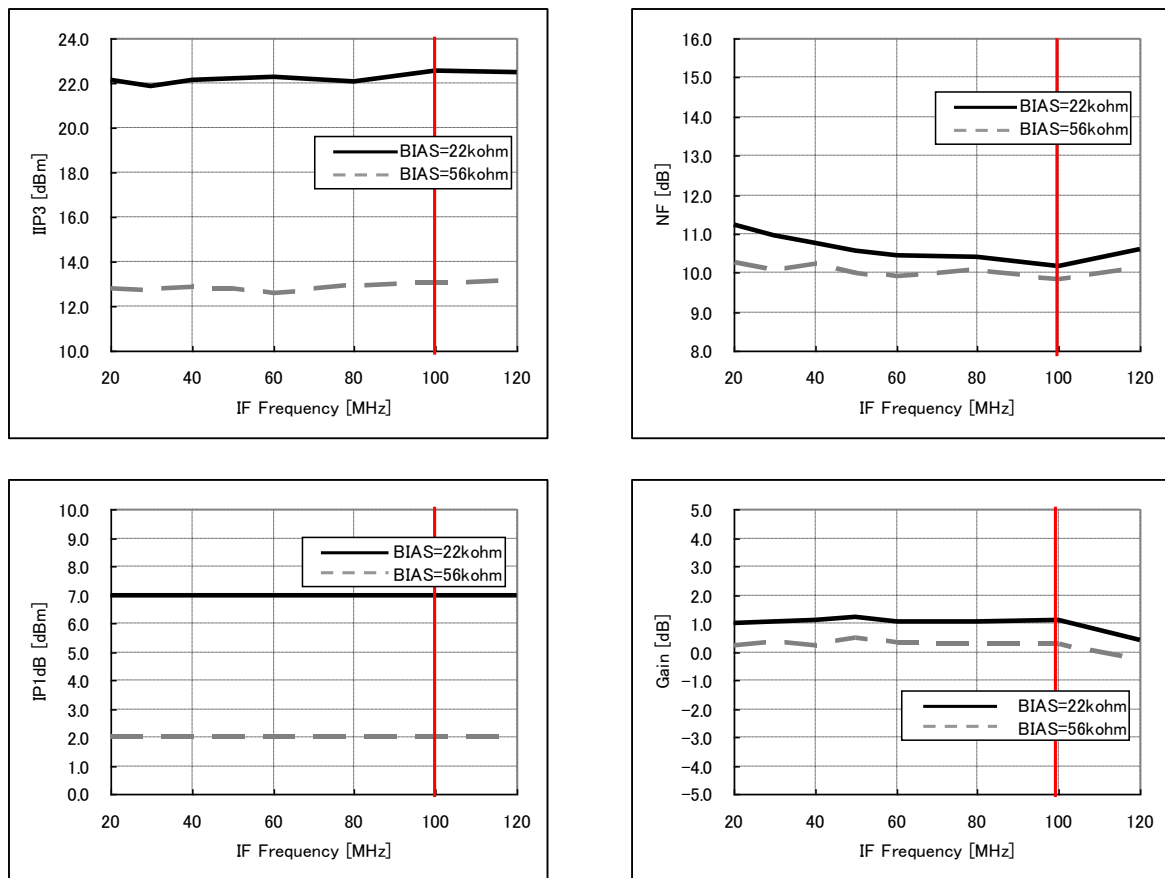


Figure 8. IF output frequency vs. IIP3, NF, P1dB, Gain

Note 1) AK1220 supports 20MHz to 100MHz IF Output.

6. Lo input power vs. IIP3, NF, P1dB, Gain

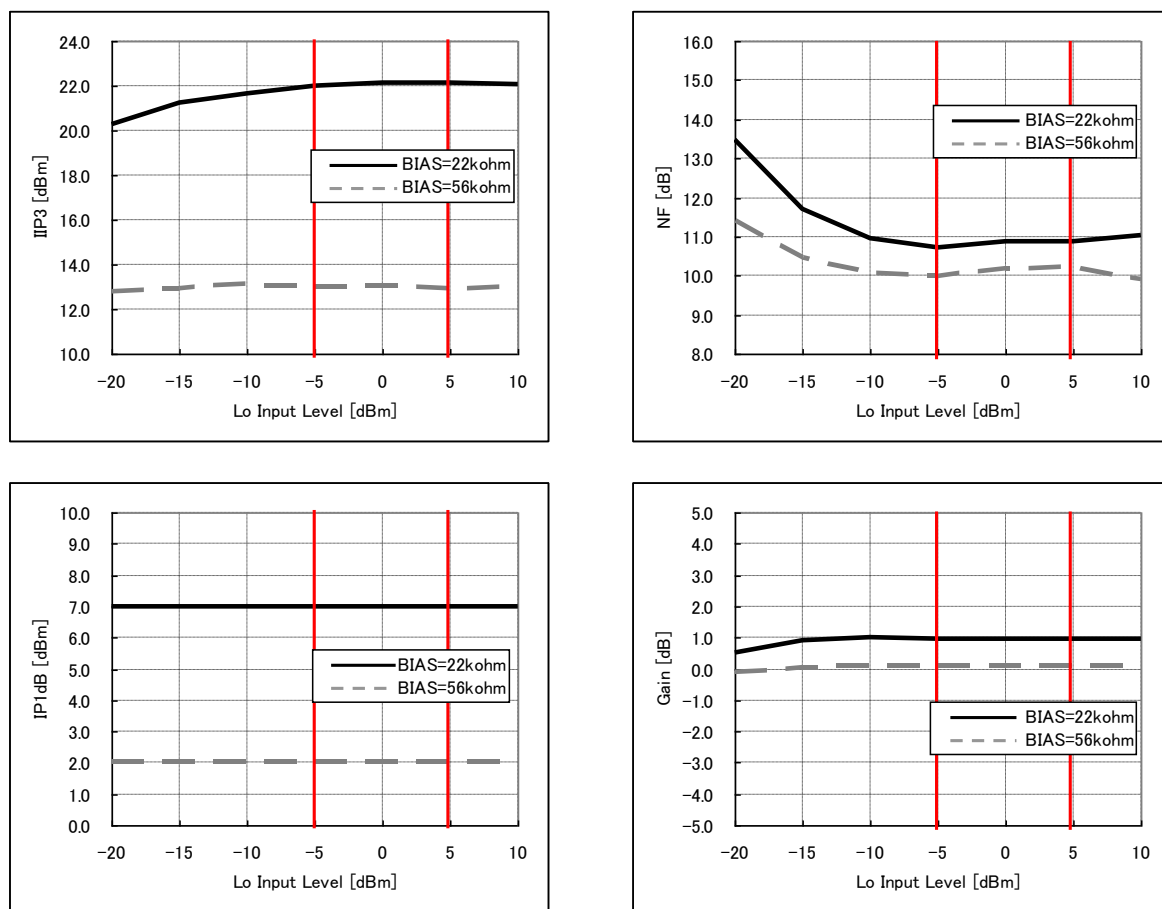


Figure 9. Lo input power vs. IIP3, NF, P1dB, Gain

Note 1) AK1220 supports -5dBm to 5dBm Lo input power.

7. Output Load Resistor (R_{Load}) vs. IIP3, NF, P1dB, Gain

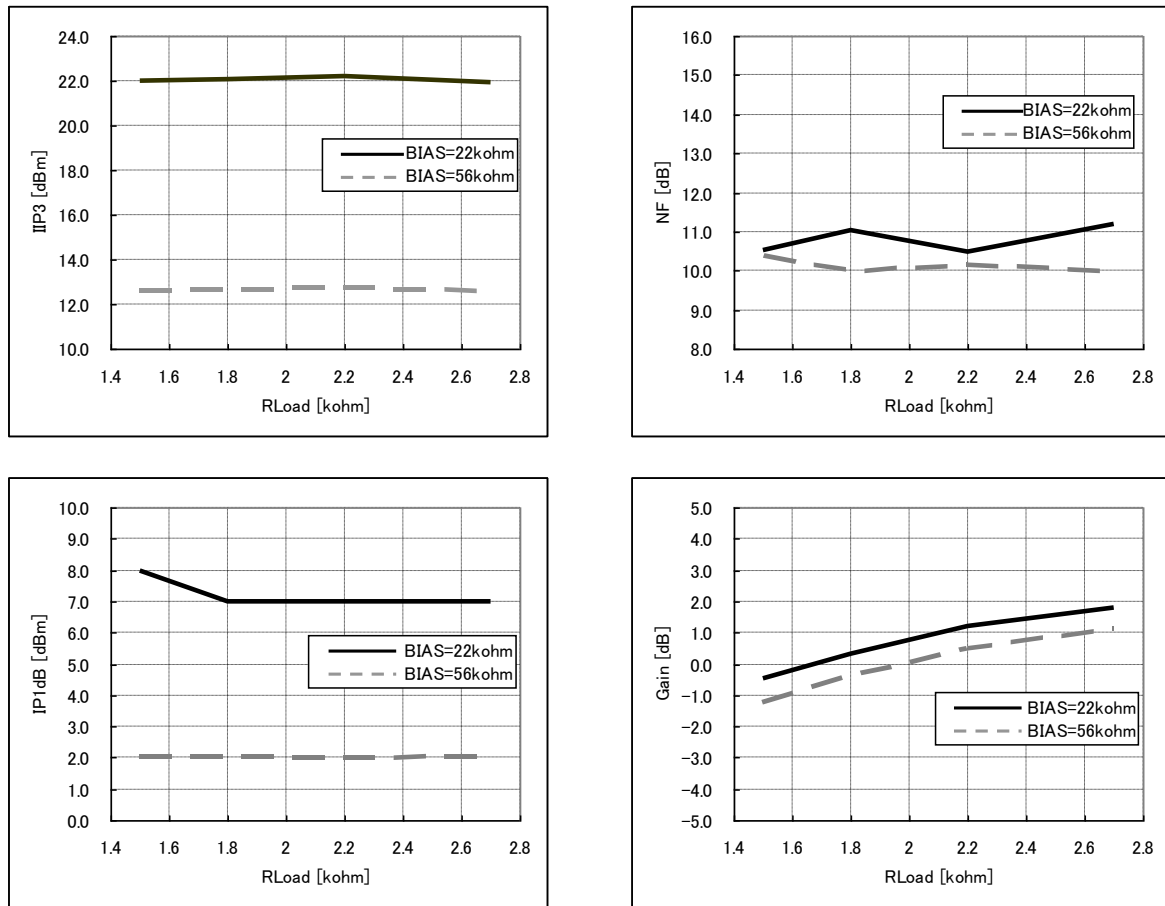


Figure 10. Output Load Resistor (R_{Load}) vs. IIP3, NF, P1dB, Gain

8. Leakage

RF input=600MHz, -20dBm, Lo input =600MHz, 0dBm, Ta=25°C VDD=5V

Parameter	BIAS	Typ.	Unit
RF – Lo Leakage	22kΩ	-50	dBc
	56kΩ	-50	dBc
RF – IF Leakage	22kΩ	-90	dBc
	56kΩ	-90	dBc
Lo - RF Leakage	22kΩ	-50	dBc
	56kΩ	-50	dBc
Lo - IF Leakage	22kΩ	-80	dBc
	56kΩ	-80	dBc

12. Typical Evaluation Board Schematic

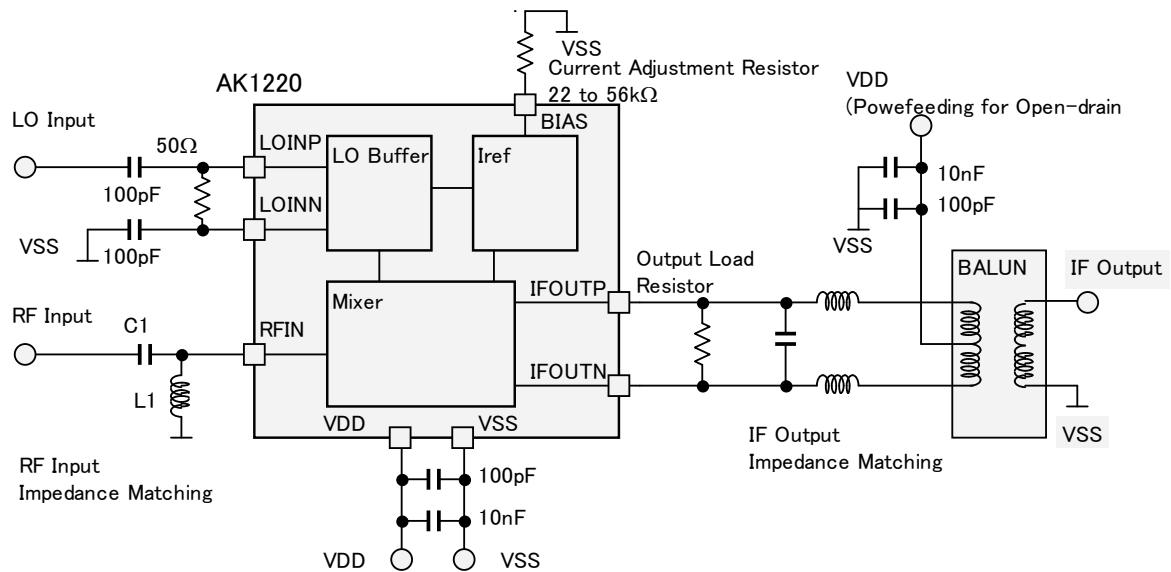


Figure 11. Typical Evaluation Board Schematic

Note 1) The open drain output needs power feeding via an inductor. (IFOUTP pin and IFOUTN)

Note 2) It is necessary to adjust impedance matching as to its setting frequency. (RF input and IF output)

2. Example of impedance matching

•RFIN

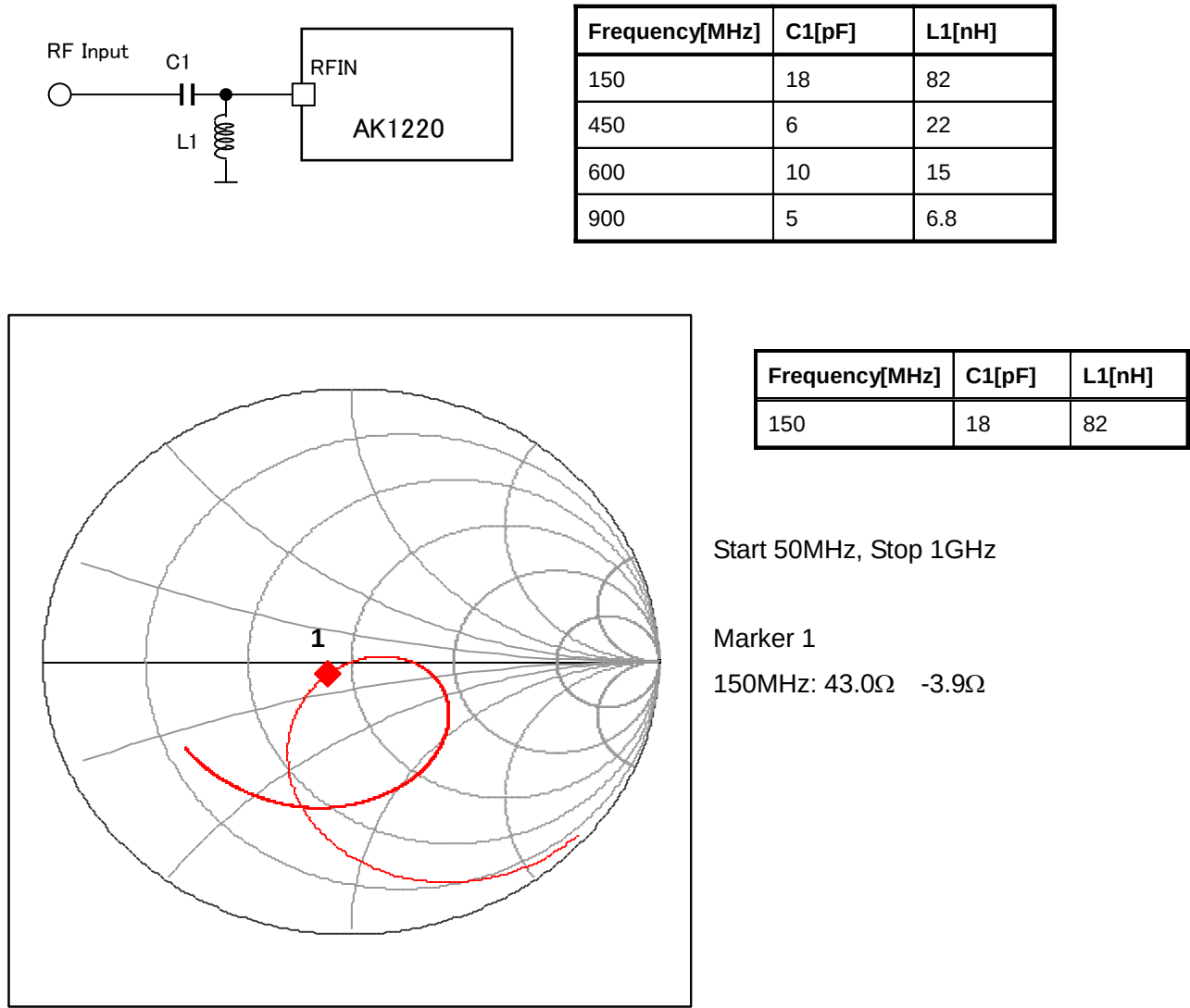
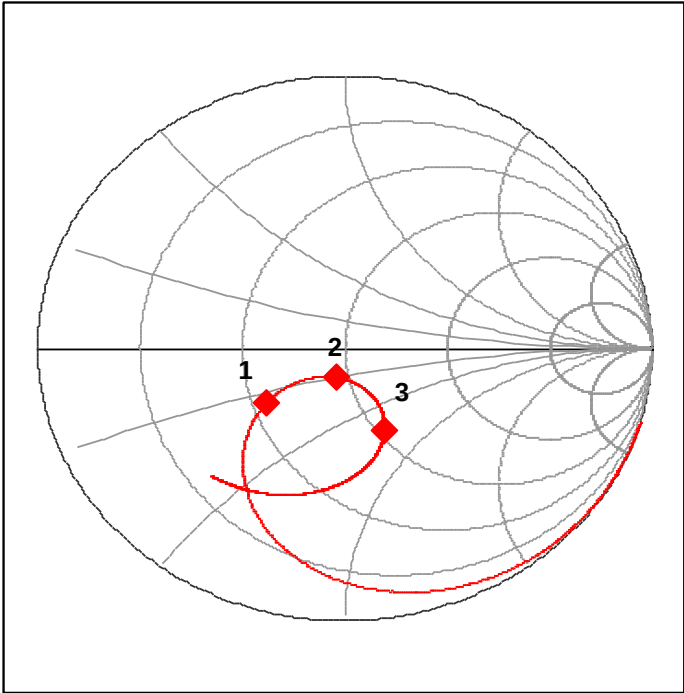


Figure 12. RFIN 150MHz example of impedance matching

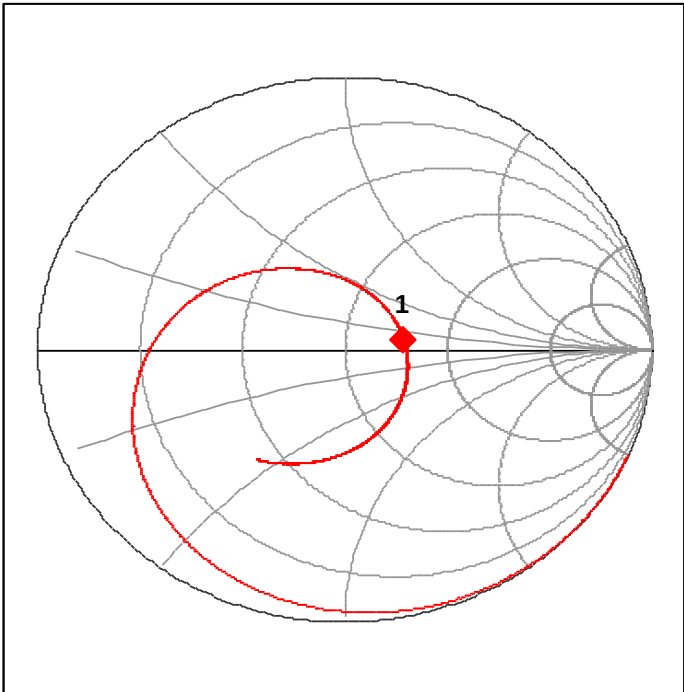


Frequency[MHz]	C1[pF]	L1[nH]
450	6	22

Start 50MHz, Stop 1GHz

Marker 1
380MHz: 27.5Ω -12.2Ω
Marker 2
450MHz: 46.5Ω -10.2Ω
Marker 3
570MHz: 52.5Ω -35.4Ω

Figure 13. RFIN 450MHz example of impedance matching

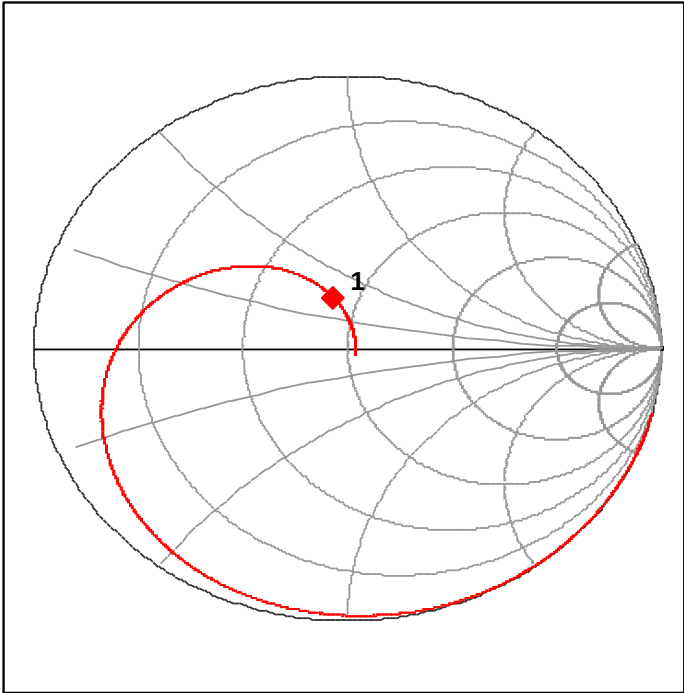


Frequency[MHz]	C1[pF]	L1[nH]
600	10	15

Start 50MHz, Stop 1GHz

Marker 1
600MHz: 72.3Ω 5.9Ω

Figure 14. RFIN 600MHz example of impedance matching



Frequency[MHz]	C1[pF]	L1[nH]
900	5	6.8

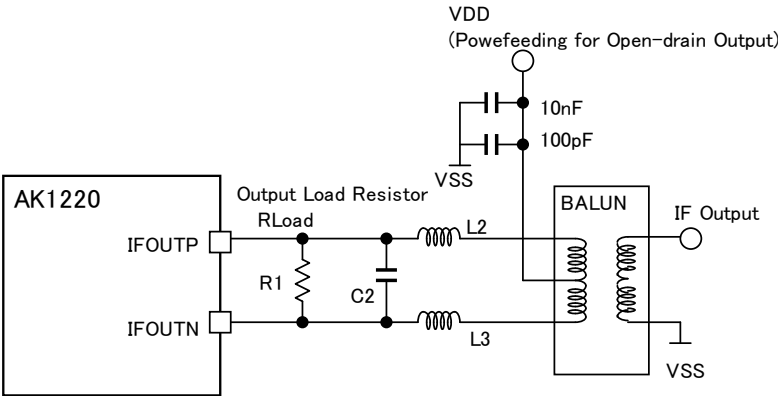
Start 50MHz, Stop 1GHz

Marker 1

900MHz: 42.9Ω 16.3Ω

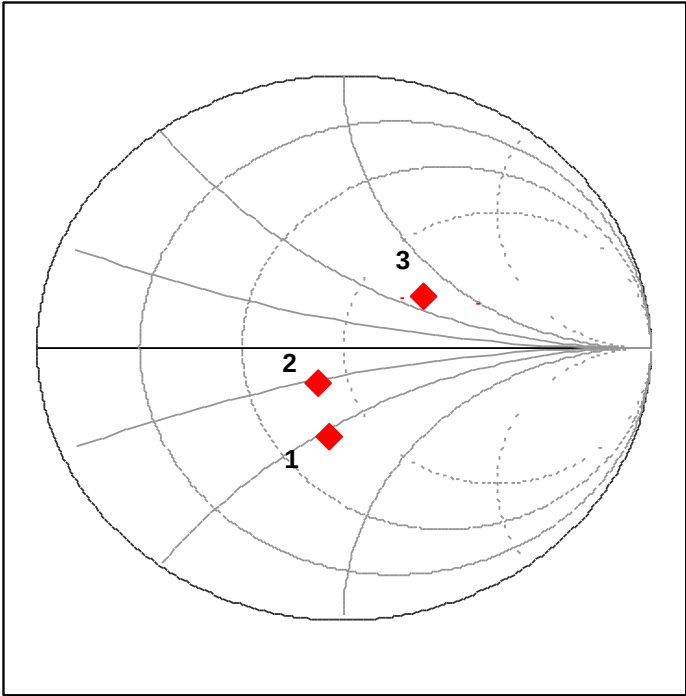
Figure 15. RFIN 900MHz example of impedance matching

•IFOUT



Frequency [MHz]	R1 [kΩ]	C2 [pF]	L2 [nH]	L3 [nH]
30	2.2	3.9	1800	1800
50	2.2	2.0	1000	1000
60	2.2	0.5	1000	1000
80	2.2	N/A	680	680
100	2.2	N/A	470	470

1800nH : Murata LQW21HN1R8J00L
1000nH : Murata LQW21HN1R0J00L
680nH : Murata LQW21HNR68J00L
470nH : Murata LQW21HNR47J00L
BALUN:Mini-Circuits ADT4-6T+

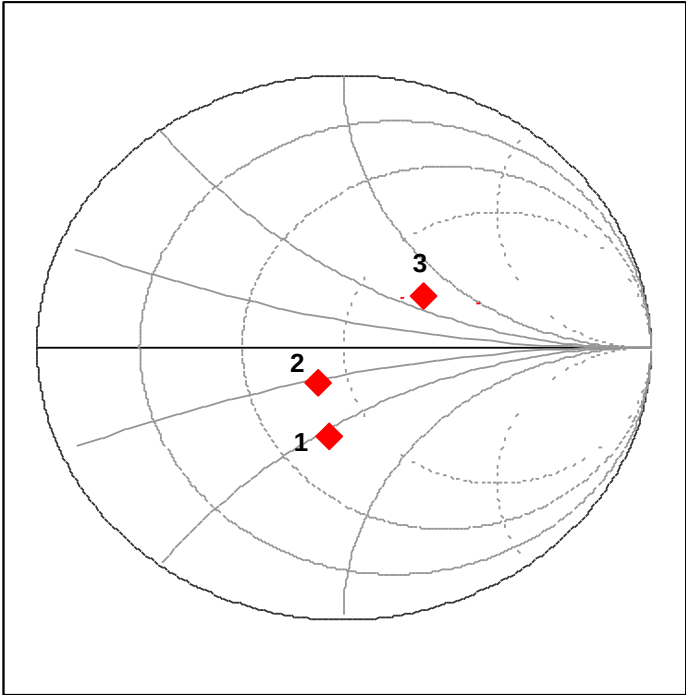


Frequency [MHz]	R1 [kΩ]	C2 [pF]	L2 [nH]	L3 [nH]
30	2.2	3.9	1800	1800

Start 10MHz, Stop 140MHz

Marker 1
28MHz: 52.5Ω -35.7Ω
Marker 2
30MHz: 53.0Ω -24.1Ω
Marker 3
36MHz: 75.6Ω 34.1Ω

Figure 16. IFOUT 30MHz example of impedance matching

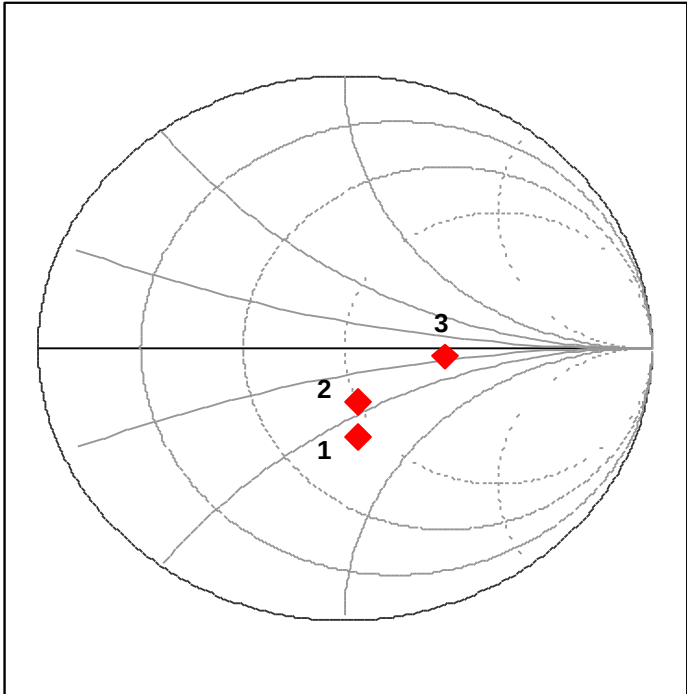


Frequency [MHz]	R1 [kΩ]	C2 [pF]	L2 [nH]	L3 [nH]
50	2.2	2	1000	1000

Start 10MHz, Stop 140MHz

- Marker 1
45MHz: 36.7Ω -26.9Ω
- Marker 2
50MHz: 40.9Ω -11.0Ω
- Marker 3
60MHz: 77.2Ω 33.9Ω

Figure 17. IFOUT 50MHz example of impedance matching

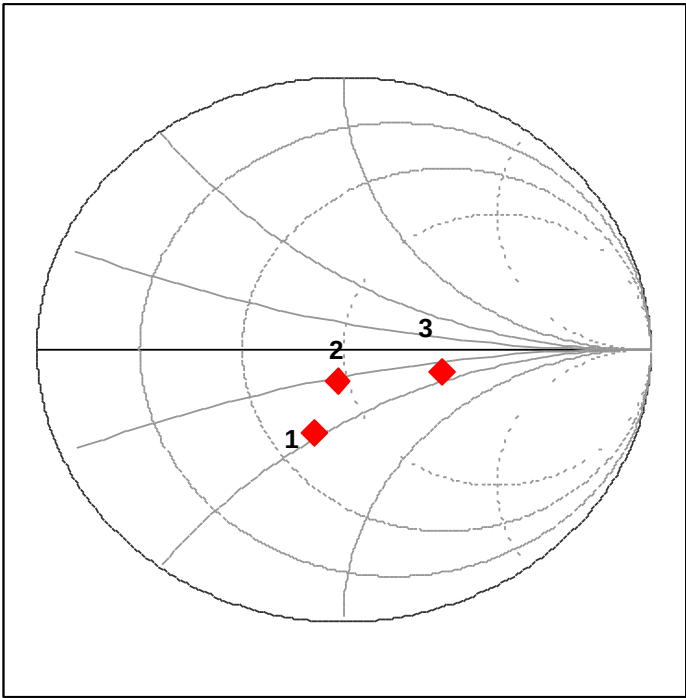


Frequency [MHz]	R1 [kΩ]	C2 [pF]	L2 [nH]	L3 [nH]
60	2.2	0.5	1000	1000

Start 10MHz, Stop 140MHz

- Marker 1
55MHz: 43.5Ω -32.0Ω
- Marker 2
60MHz: 50.5Ω -20.6Ω
- Marker 3
70MHz: 98.9Ω -6.1Ω

Figure 18. IFOUT 60MHz example of impedance matching

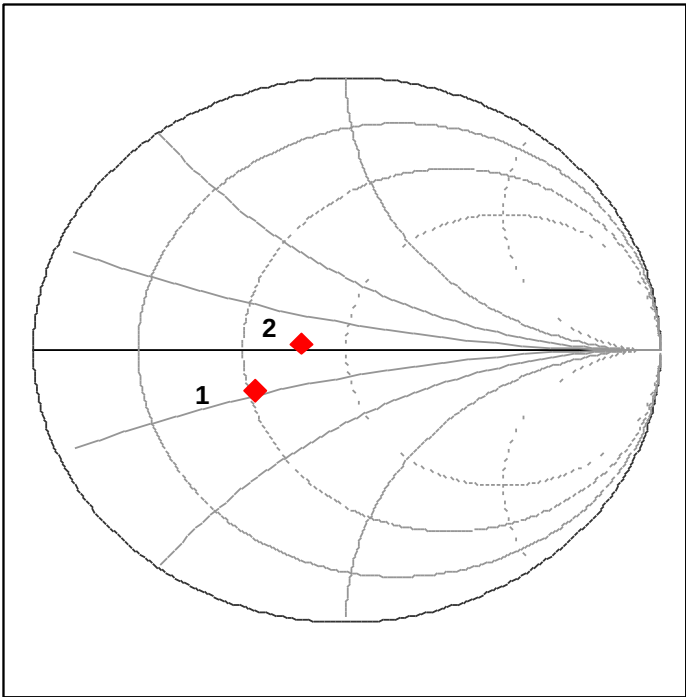


Frequency [MHz]	R1 [kΩ]	C2 [pF]	L2 [nH]	L3 [nH]
80	2.2	N/A	680	680

Start 10MHz, Stop 140MHz

- Marker 1
72MHz: 32.1Ω -21.9Ω
- Marker 2
80MHz: 45.5Ω -9.3Ω
- Marker 3
92MHz: 98.3Ω -10.2Ω

Figure 19. IFOUT 80MHz example of impedance matching



Frequency [MHz]	R1 [kΩ]	C2 [pF]	L2 [nH]	L3 [nH]
100	2.2	N/A	470	470

Start 10MHz, Stop 140MHz

- Marker 1
92MHz: 26.3Ω -8.9Ω
- Marker 2
100MHz: 37.5Ω -1.9Ω

Figure 20. IFOUT 100MHz example of impedance matching

• LOINP/LOINN

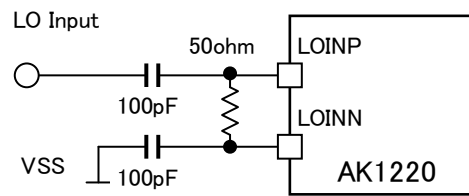
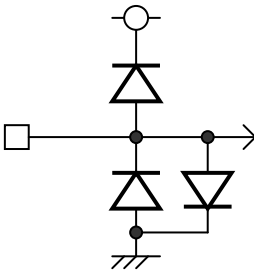
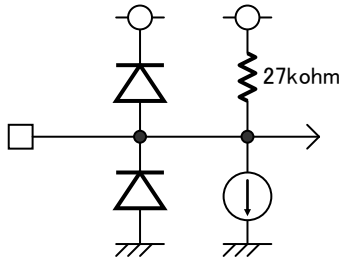
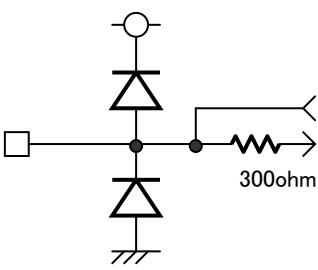
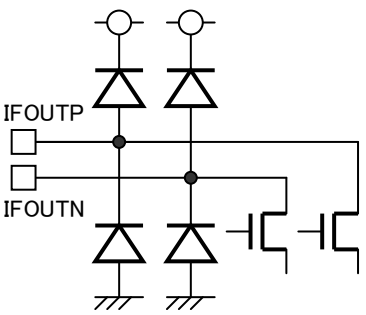


Figure 21. LOINP/LOINN example of impedance matching

13. IC Interface Schematic

No.	Name	I/O	Function
1	RFIN	I	RF input pin 
3	LOINN	I	
4	LOINP	I	Lo input pins 
8	BIAS	I/O	
			Analog I/O pin 
11	IFOUTN	O	IF output pins 
12	IFOUTP	O	

14. Application Information

•Impedance matching network with LC

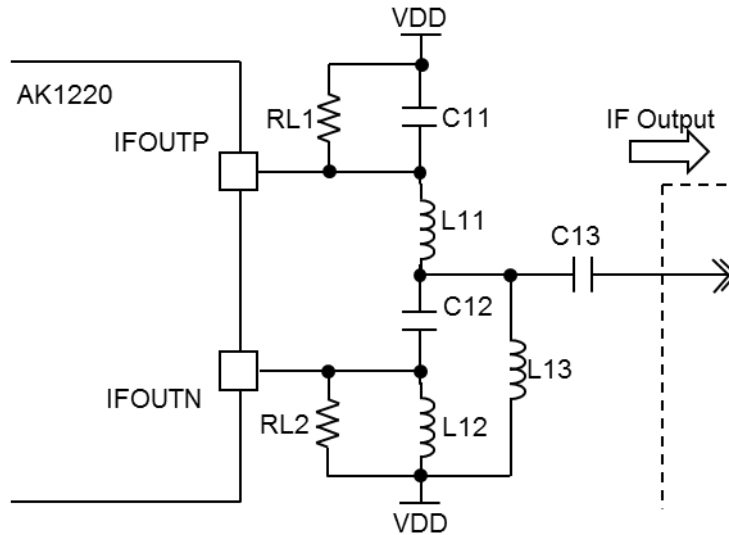


Figure 22. Impedance matching network with LC

Impedance matching network with LC is shown in Figure 22. AK1220 has open drain outputs, so $RL1 + RL2$ is output load resistance. $C11$ and $L11$ compose lowpass filter. $C12$ and $L12$ are for highpass filter. $C13$ is DC blocking capacitor and $L13$ is RF choke. IFOUTP and IFOUTN pins need power feeding via $L11$, $L12$ and $L13$.

The differential voltage from IFOUTP/N can be converted to a single-ended by $L11$, $L12$, $C11$ and $C12$ properly. The differential impedance ($RL1 + RL2$) is converted to single-ended output terminating impedance R_o .

$L11$, $C11$, $L12$ and $C12$ are calculated as below. f_{out} is IF output frequency.

$$C_{11} = C_{12} = \frac{1}{2\pi * f_{OUT} * \sqrt{(R_{L1} + R_{L2}) * R_o}}$$

$$L_{11} = L_{12} = \frac{\sqrt{(R_{L1} + R_{L2}) * R_o}}{2\pi * f_{OUT}}$$

For example, in the case of IF Output = 50MHz, Output Load Resistor (R_{load}) = 2.2k Ω in 50 Ω interface, $L11$, $C11$, $L12$ and $C12$ are calculated as below.

$$C_{11} = C_{12} = \frac{1}{2\pi * (50 * 10^6) * \sqrt{(2.2 * 10^3) * 50}} = 9.6\text{pF}$$

$$L_{11} = L_{12} = \frac{\sqrt{(2.2 * 10^3) * 50}}{2\pi * (50 * 10^6)} = 1056\text{nH}$$

L13 and C13 should be large enough not to affect the impedance at IF output frequency. In some cases the impedance matching can be optimized by L13 and C13.

For example, in the case of IF Output = 50MHz, Output Load Resistor (Rload) = 2.2kΩ in 50Ω interface, it is recommended to choose 2200nH and 1000pF as L13 and C13. If any correction is needed, it can be adjusted by reducing the value of L13 and C13.

These calculated values are approximation. In some cases, some correction is needed due to the effect of parasitic capacitance of external parts or/and PCBs. The impedance matching network components should be decided through enough evaluation on AK1220.

Typical Performance using impedance matching network with LC is below. RF Input = 600MHz, IF Output = 50MHz, LO Input = 550MHz, Output Load Resistor (Rload) = 2.2kΩ, Vdd = 5V, Ta = 25°C, LO Input Level = 0dBm,

Ref.	Value	Size	Part Number
RL1, RL2	1.1kΩ	1005	KOA RK73K1ETP112
L11, L12	1000nH	2012	Murata LQW21HN1R0J00
C11, C12	10pF	1005	Murata GRM1552C1H100JA01
L13	2200nH	2012	Murata LQW21HN2R2J00
C13	120pF	1005	Murata GRM1552C1H121JA01

Parameter	Rbias	Min.	Typ.	Max.	Unit
Conversion Gain	Rbias = 22k Ω ($\cong$ 17mA)		0.7		dB
	Rbias = 56k Ω ($\cong$ 9mA)		0.0		
SSB Noise Figure (NF)	Rbias = 22k Ω ($\cong$ 17mA)		11.3		dB
	Rbias = 56k Ω ($\cong$ 9mA)		10.1		
IP1dB	Rbias = 22k Ω ($\cong$ 17mA)		6.0		dBm
	Rbias = 56k Ω ($\cong$ 9mA)		1.5		
IIP3	Rbias = 22k Ω ($\cong$ 17mA)		22.1		dBm
	Rbias = 56k Ω ($\cong$ 9mA)		13.8		

The phase and amplitude balance is achieved at IF Output frequency by using impedance matching network with LC. The port-to-port leakage is improved with the phase and amplitude balance is achieved at RF, LO, and IF frequency with wide band balun.

•Evaluation Board

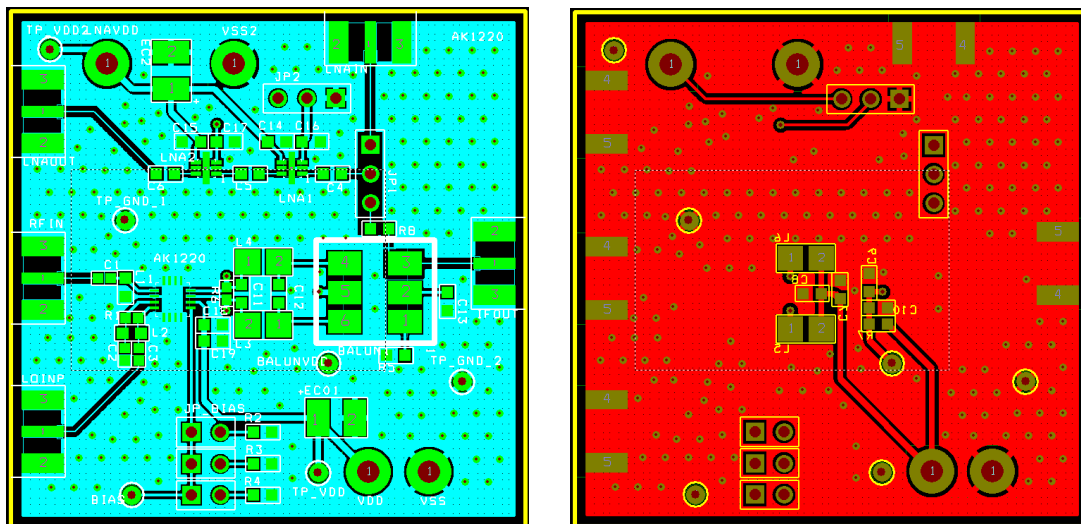


Figure 23. AK1220/AK1222 Evaluation Board (Balun)

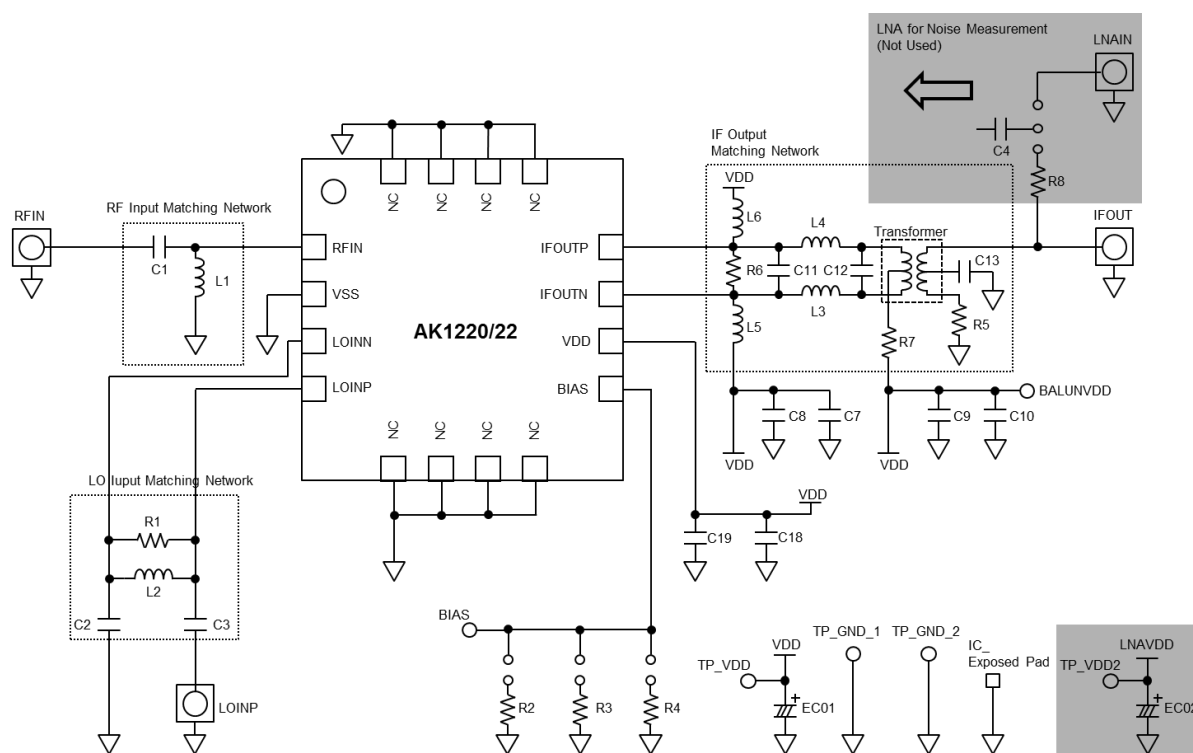


Figure 24. AK1220/AK1222 Evaluation Board Schematic (Balun)

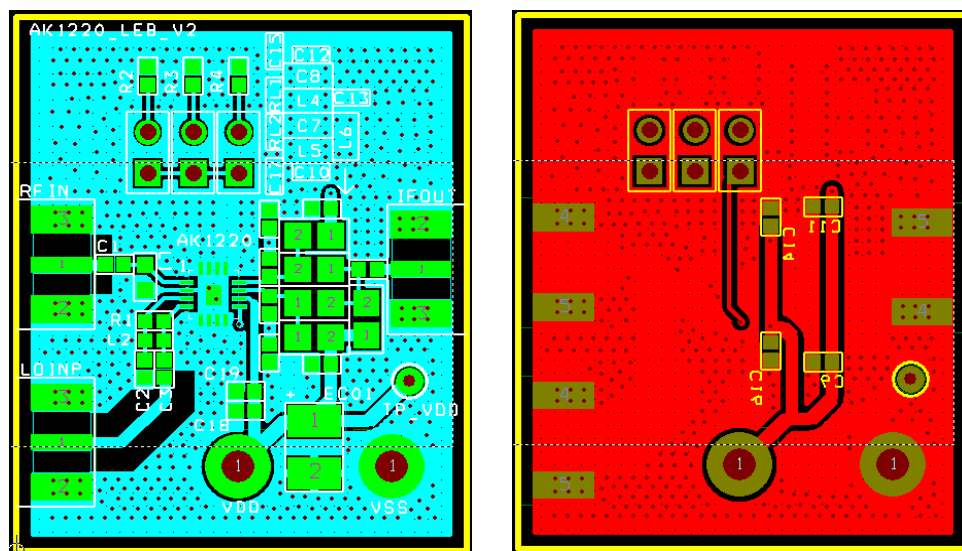


Figure 25. AK1220/AK1222 Evaluation Board (matching network with LC)

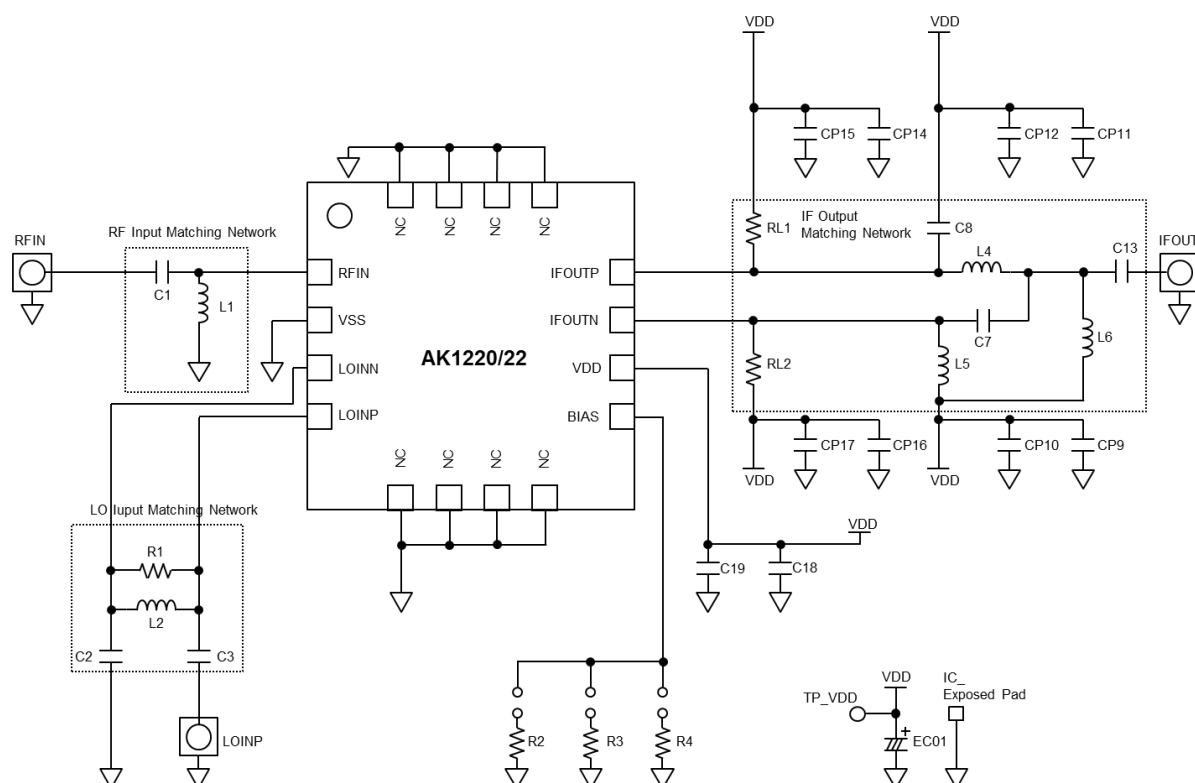


Figure 26. AK1220/AK1222 Evaluation Board Schematic (matching network with LC)

15. Outer Dimensions

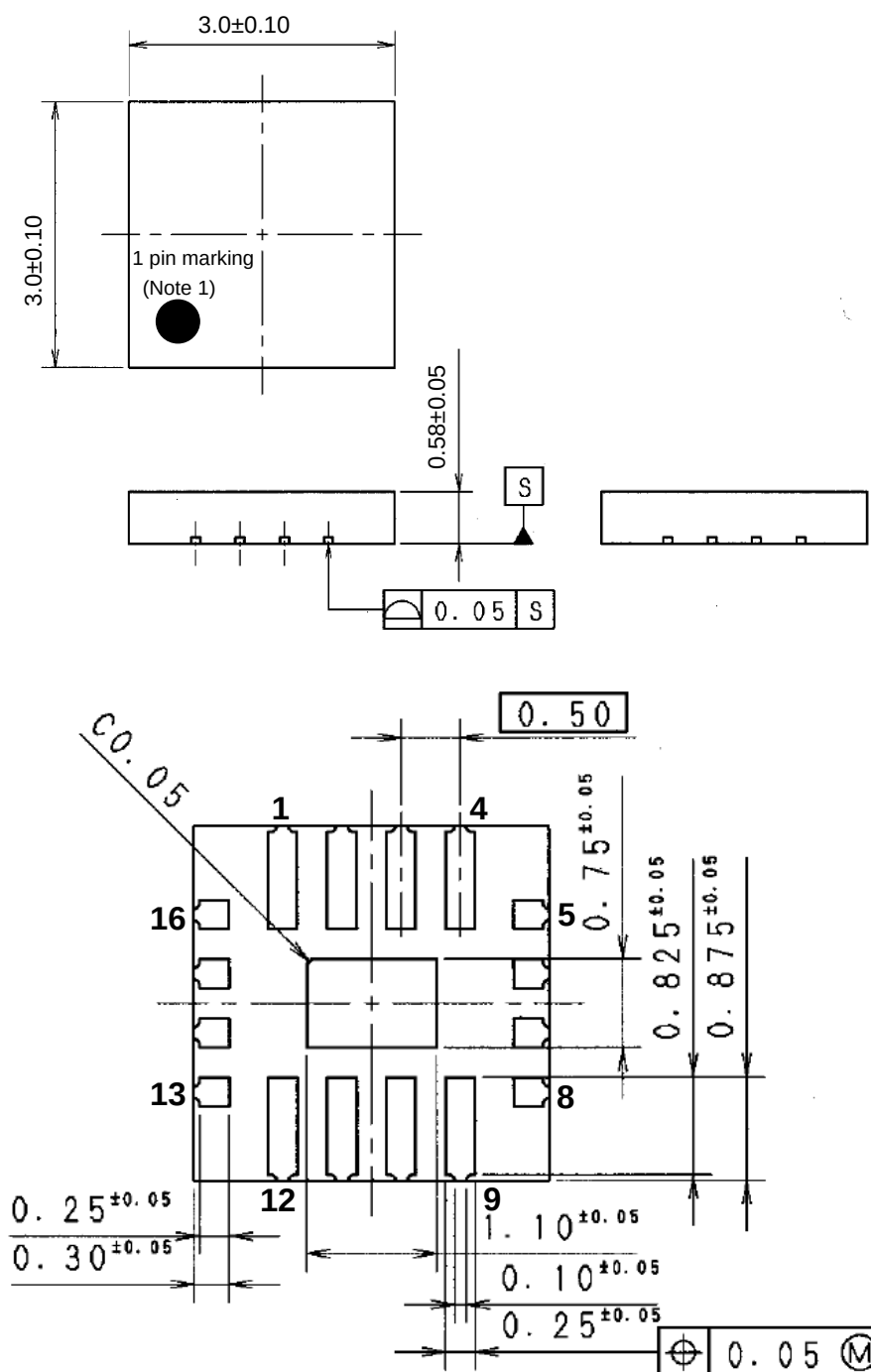
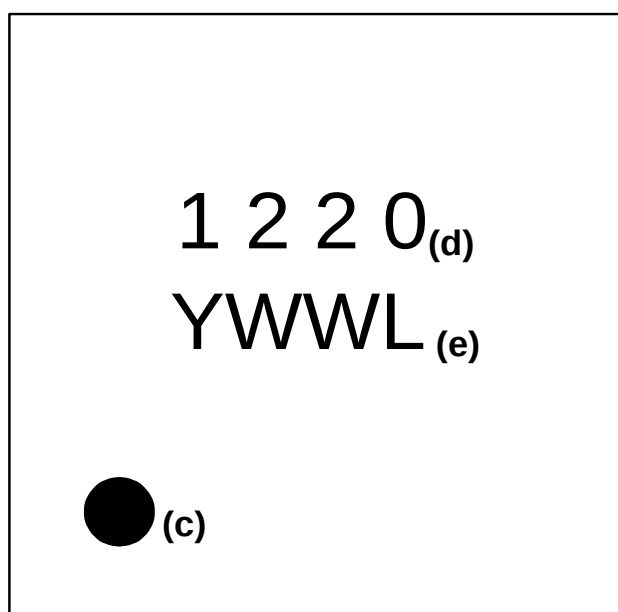


Figure 27. Outer Dimensions

Note 1. 1 pin marking is only a reference for the 1 pin location on the top of package.

16. Marking

- (a) Style : QFN
(b) Number of pins : 16
(c) 1 pin marking: : ○
(d) Product number : 1220
(e) Date code : YWWL (4 digits)
Y: Lower 1 digit of calendar year (Year 2010 → 0, 2011 → 1 ...)
WW: Week
Lot identification, given to each product lot which is made in a week
→ LOT ID is given in alphabetical order (A, B, C...).

**Figure 28. Marking**

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•Related Parts

Part#	Discription	Comments
Mixer		
AK1220	100MHz~900MHz High Linearity Down Conversion Mixer	IIP3:+22dBm
AK1222	100MHz~900MHz Low Power Down Conversion Mixer	IDD:2.9mA
AK1224	100MHz~900MHz Low Noise, High Liniarity Down Conversion Mixer	NF:8.5dB, IIP3:+18dBm
AK1228	10MHz~2GHz Up/Down Conversion Mixer	3V Supply, NF:8.5dB
AK1221	0.7GHz~3.5GHz High Linearity Down Conversion Mixer	IIP3:+25dBm
AK1223	3GHz~8.5GHz High Linearity Down Conversion Mixer	IIP3:+13dB, NF:15dB
PLL Synthesizer		
AK1541	20MHz~600MHz Low Power Fractional-N Synthesizer	IDD:4.6mA
AK1542A	20MHz~600MHz Low Power Integer-N Synthesizer	IDD:2.2mA
AK1543	400MHz~1.3GHz Low Power Fractional-N Synthesizer	IDD:5.1mA
AK1544	400MHz~1.3GHz Low Power Integer-N Synthesizer	IDD:2.8mA
AK1590	60MHz~1GHz Fractional-N Synthesizer	IDD:2.5mA
AK1545	0.5GHz~3.5GHz Integer-N Synthesizer	16-TSSOP
AK1546	0.5GHz~3GHz Low Phase Noise Integer-N Synthesizer	Normalized C/N:-226dBc/Hz
AK1547	0.5GHz~4GHz Integer-N Synthesizer	5V Supply
AK1548	1GHz~8GHz Low Phase Noise Integer-N Synthesizer	Normalized C/N:-226dBc/Hz
IFVGA		
AK1291	100~300MHz Analog Signal Control IF VGA w/ RSSI	Dynamic Range:30dB
integrated VCO		
AK1572	690MHz~4GHz Down Conversion Mixer with Frac.-N PLL and VCO	IIP3:24dBm, -111dBc/Hz@100kHz
AK1575	690MHz~4GHz Up Conversion Mixer with Frac.-N PLL and VCO	IIP3:24dBm, -111dBc/Hz@100kHz
IF Reciever (2nd Mixer + IF BPF + FM Detector)		
AK2364	Built-in programmable AGC+BPF, FM detector IC	IFBPF:±10kHz ~ ±4.5kHz
AK2365A	Built-in programmable AGC+BPF, IFIC	IFBPF:±7.5kHz ~ ±2kHz
Analog BB for PMR/LMR		
AK2345C	CTCSS Filter, Encoder, Decoder	24-VSOP
AK2360/ AK2360A	Inverted frequency(3.376kHz/3.020kHz) scrambler	8-SON
AK2363	MSK Modem/DTMF Receiver	24-QFN
AK2346B	0.3-2.55/3.0kHz Analog audio filter,	24-VSOP
AK2346A	Emphasis, Compandor, scrambler, MSK Modem	24-QFN
AK2347B	0.3-2.55/3.0kHz Analog audio filter	24-VSOP
AK2347A	Emphasis, Compandor, scrambler, CTCSS filter	24-QFN
Function IC		
AK2330	8-bit 8ch Electronic Volume	VREF can be selected for each channel
AK2331	8-bit 4ch Electronic Volume	VREF can be selected for each channel

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