FAIRCHILD

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MMBTA92 / PZTA92
PNP High-Voltage Amplifier

Description

This device is designed for high-voltage driver applications. Sourced from process 76.

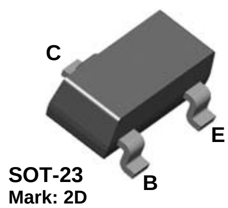

Figure 1. MMBTA92 Device Package

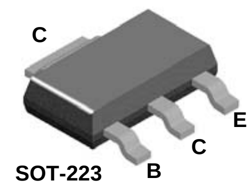

Figure 2. PZTA92 Device Package

Ordering Information

Part Number	Top Mark	Package	Packing Method
MMBTA92	2D	SOT-23 3L	Tape and Reel
PZTA92	A92	SOT-223 4L	Tape and Reel

Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Value	Unit
V_{CEO}	Collector-Emitter Voltage	-300	V
V_{CBO}	Collector-Base Voltage	-300	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current - Continuous	-500	mA
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

1. These ratings are based on a maximum junction temperature of 150°C.
2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

Thermal Characteristics

Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Max.		Unit
		MMBTA92[3]	PZTA92[4]	
P_D	Total Device Dissipation	350	1000	mW
	Derate Above 25°C	2.8	8.0	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	125	°C/W

Notes:

3. Device is mounted on FR-4 PCB 1.6 inch x 1.6 inch x 0.06 inch.
4. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics

Values are at T_A = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage[5]	I_C = -1.0 mA, I_B = 0	-300		V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	I_C = -100 µA, I_E = 0	-300		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	I_E = -100 µA, I_C = 0	-5.0		V
I_{CBO}	Collector Cut-Off Current	V_{CB} = -200 V, I_E = 0		-0.25	µA
I_{EBO}	Emitter Cut-Off Current	V_{EB} = -3.0 V, I_C = 0		-0.1	µA
h_{FE}	DC Current Gain[5]	I_C = -1.0 mA, V_{CE} = -10 V	25		
		I_C = -10 mA, V_{CE} = -10 V	40	250	
		I_C = -30 mA, V_{CE} = -10 V	25		
$V_{CE}(sat)$	Collector-Emitter Saturation Voltage[5]	I_C = -20 mA, I_B = -2.0 mA		-0.5	V
$V_{BE}(sat)$	Base-Emitter Saturation Voltage[5]	I_C = -20 mA, I_B = -2.0 mA		-0.9	V
f_T	Current Gain - Bandwidth Product	I_C = -10 mA, V_{CE} = -20 V, f = 100 MHz	50		MHz
C_{cb}	Collector-Base Capacitance	V_{CB} = -20 V, I_E = 0, f = 1.0 MHz		6.0	pF

Note:

5. Pulse test: pulse width ≤ 300 µs, duty cycle ≤ 2.0%.

Typical Performance Characteristics

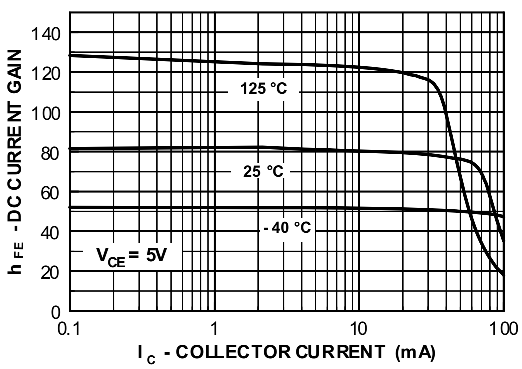

Figure 3. Typical Pulsed Current Gain vs. Collector Current

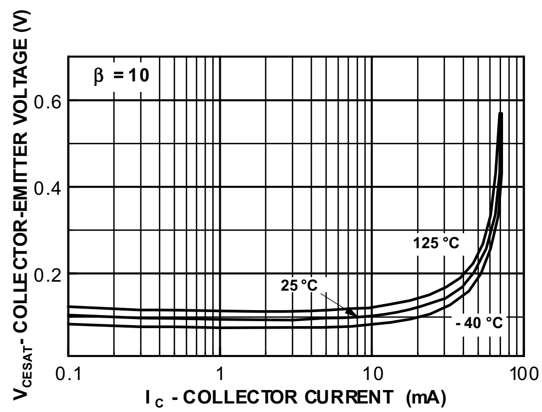

Figure 4. Collector-Emitter Saturation Voltage vs. Collector Current

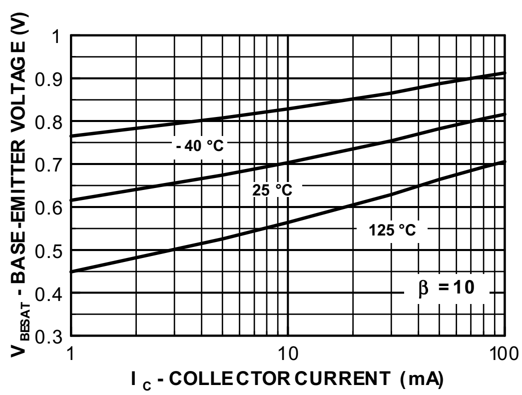

Figure 5. Base-Emitter Saturation Voltage vs. Collector Current

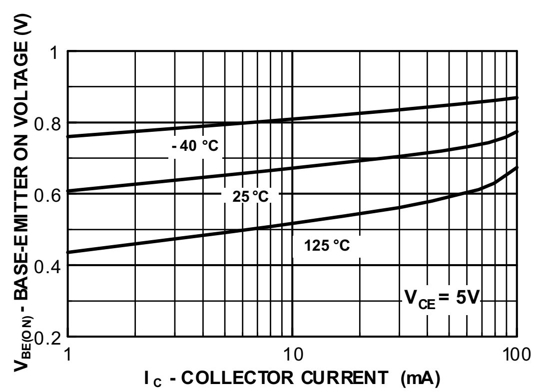

Figure 6. Base-Emitter On Voltage vs. Collector Current

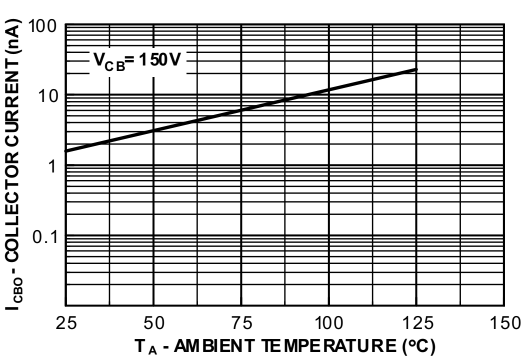

Figure 7. Collector Cut-Off Current vs. Ambient Temperature

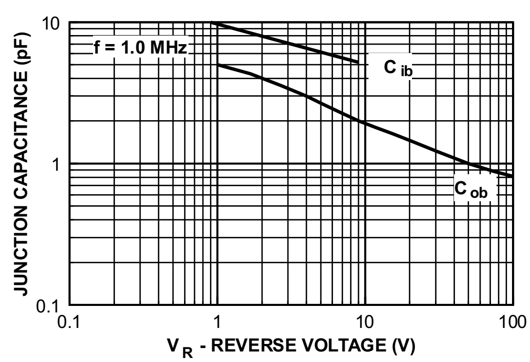

Figure 8. Junction Capacitance vs. Reverse-Bias Voltage

Typical Performance Characteristics (Continued)

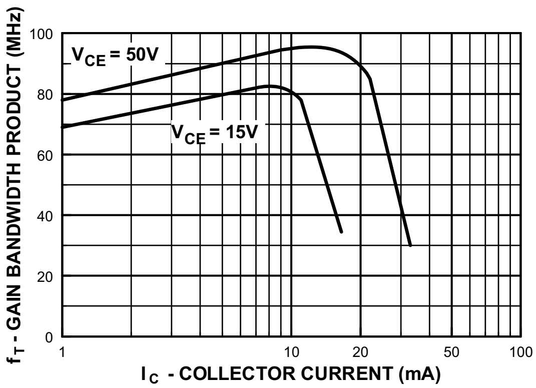

Figure 9. Gain Bandwidth Product vs. Collector Current

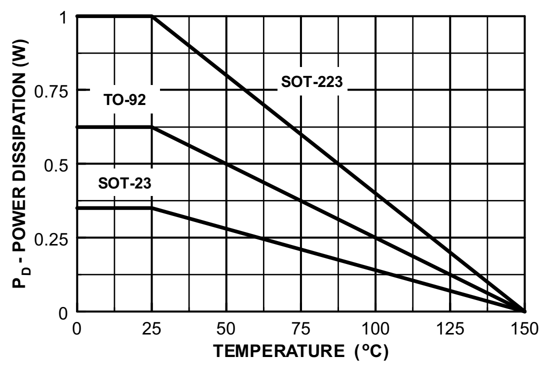

Figure 10. Power Dissipation vs. Ambient Temperature

Physical Dimensions

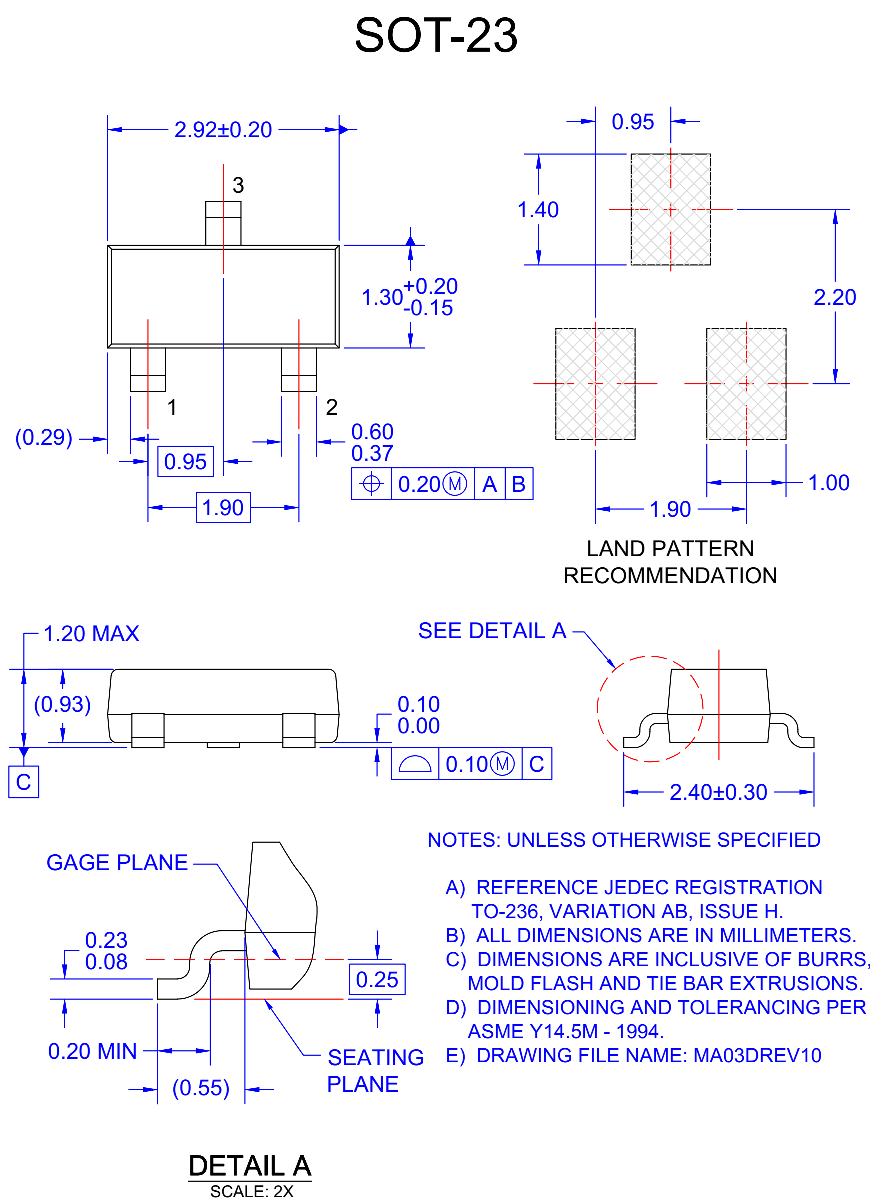

Figure 11. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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Physical Dimensions (Continued)

SOT-223

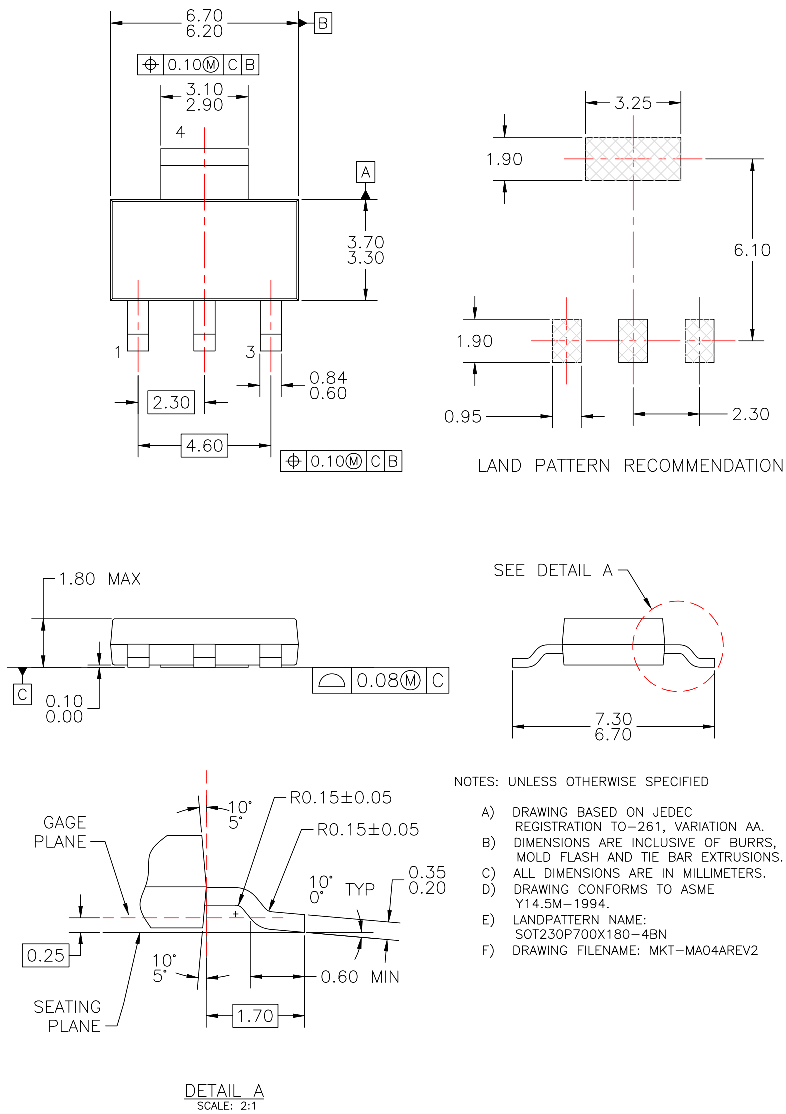

Figure 12. MOLDED PACKAGING, SOT-223, 4-LEAD (ACTIVE)

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