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ADC1412D105F1 Obsolete

ADC1412D105F1 demo board; CMOS version; SPI, Regulators and CMOS buffer on board

IDT's dual channel ADC demonstration board is suitable for dynamic performances evaluation from low to high IF configuration with LVCMOS outputs. A Data acquisition board can be used to easily analyze the ADC performances in the design and prototyping phase

Features

- SMA connector for clock and analog inputs signal
- CMOS (buffered outputs)

Product Options

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Demo Board Software ADC_1_r04	ZIP	2.33 MB	Jul 18, 2012

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ADC1412D105F1 - Evaluation Board

