



SOP-8

Pin Definition:



1. Output 8. Input 2. Ground 7. Ground 3. Ground 6. Ground 4. N/C 5. Inhibit

General Description

The TS2938 series of fixed-voltage monolithic micro-power voltage regulators is designed for a wide range of applications. This device excellent choice of use in battery-power application. Furthermore, the quiescent current increases on slightly at dropout, which prolongs battery life.

This series of fixed-voltage regulators features very low ground current (200uA Typ.), very low drop output voltage (Typ. 60mV at light load and 600mV at 500mA) and output inhibit control. This includes a tight initial tolerance of 2%, extremely good line regulation of 0.05% typ., and very low output temperature coefficient.

Features

- Dropout voltage typically 0.5V @lo=500mA
- Output current up to 500mA
- Logic Control Electronic Shutdown
- Output voltage trimmed before assembly
- -18V Reverse peak voltage
- +30V Input over voltage protection
- +60V Transient peak voltage
- Low Quiescent Current 100uA (Typ.) (On Mode)
- Internal current limit
- Thermal shutdown protection

Ordering Information

Part No.	Package	Packing
TS2938CS <u>xx</u> RL	SOP-8	2.5Kpcs / 13" Reel

Note: Where xx denotes voltage option, available are

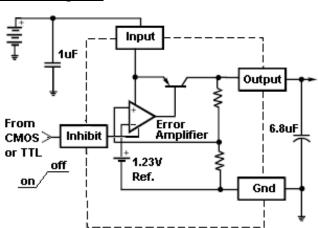
12= 12V

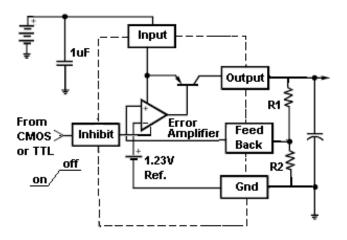
50= 5.0V

33= 3.3V

Contact factory for additional voltage options.

Block Diagram









Absolute Maximum Rating (Note 1)

Parameter	Symbol	Limit	Unit
Input Supply Voltage (Note 2)	V _{IN}	-18 ~ +60	V
Operation Input Supply Voltage	V_{OPR}	26	V
Power Dissipation (Note 3)	P _D	Internally Limited	W
Operating Junction Temperature Range	T _J	-40 ~ + 125	°C
Thermal Resistance Junction to Case	T_{JC}	20	°C/W
Storage Temperature Range	T _{STG}	-65 ~ +150	°C

Electrical Characteristics (V_{IN}=V_{OUT}+1V, I_L=5mA, C_O=10uF, T_A=25°C, unless otherwise noted)

Parameter	Conditions	Min	Тур	Max	Unit
Output Voltage	T _A =25°C	0.980 Vo	V _{OUT}	1.020 Vo	V
	T _A =25~85°C	0.960 Vo		1.040 Vo	V
Output Voltage Temperature Coefficient			50	150	ppm/°C
Line Regulation	$V_0+1V \le V_{IN} \le 26V$		0.05	0.5	%
Load Regulation	5mA ≤ I _L ≤ 500mA		0.2	1.0	%
	I _L =100mA		100	200	
Dropout Voltage (Note 4)	I _L =300mA		300	400	mV
	I _L =500mA		500	600	
	I _L =100uA		200	300	uA
Quiescent Current (Note 5)	I _L =300mA		12	20	mA
	I _L =500mA		25	50	mA
Short Circuit Current (Note 6)	V _{OUT} =0		0.9	1.3	Α
Outrat Naine	C _L =2.2uF		500		
Output Noise, 10Hz to 100KHz, I _L =10mA	C _L =3.3uF		350		uVrms
TOHZ to TOOKHZ, IL-TOTIA	C _L =33uF		120		
Ripple Rejection	Ratio	60			dB
Shutdown Input					
Input Logic Voltage	Low (Regulator ON)		1.3	0.7	\/
	High (Regulator OFF)	2.0			V
Chutdaum Din Innut Current	Vinhibit = 2.4V		30	50	
Shutdown Pin Input Current	Vinhibit = 26V		450	600	
	Vinhibit≤2V, Vin≤26V			000	uA
Regulator Output Current in Shutdown	Vout=0V			200	

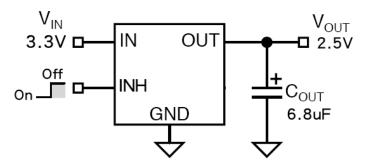




Electrical Characteristics (Continue)

- Note 1: Absolute Maximum Rating is limits beyond which damage to the device may occur. For guaranteed Specifications and test conditions see the Electrical Characteristics.
- Note 2: Maximum positive supply voltage of 60V must be limited duration (<100mS) and duty cycle (<1%).
- Note 3: The maximum allowable power dissipation is a function of the maximum junction temperature, Tj, the junction to ambient thermal resistance, θ ja, and the ambient temperature, Ta. Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. The effective value of θ ja can be reduced by using a heatsink.
- Note 4: Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential.
- Note 5: Ground pin current is the regulator quiescent current. The total current drawn from the source is the sum of the ground pin current and output load current.
- Note 6: Output current will decrease with increasing temperature, but it will be not dropped below 500mA at the maximum specified temperature.

Application Circuit



Fixed Output Voltage Type

Application Information

Application

The TS2938 series is a high performance with low dropout voltage regulator suitable for moderate to high current and voltage regulator application. Its 500Mv (typ) dropout voltage at full load and over temperature makes it especially valuable in battery power systems and as high efficiency noise filters in post regulator applications. Unlike normal NPN transistor design, where the base to emitter voltage drop and collector to emitter saturation voltage limit the minimum dropout voltage, dropout performance of the PNP output of these devices is limited only by low Vce saturation voltage.

The TS2938 series is fully protected from damage due to fault conditions. Linear current limiting is provided. Output current during overload conditions is constant. Thermal shutdown the device when the die temperature exceeds the maximum safe operating temperature. Transient protection allows device survival even when the input voltage spikes above and below nominal. The output structure of these regulators allows voltages in excess of the desired output voltage to be applied without reverse current flow.





Application Information (Continue)

Output Capacitor

The TS2938 series requires an output capacitor to maintain stability and improve transient response. Proper capacitor selection is important to ensure proper operation. The output capacitor selection is dependent upon the ESR of the output capacitor the maintain stability. When the output capacitor is 6.8uF or greater, the output capacitor should have an ESR less than 2 ohm. This will improve transient response as well as promoted stability. Ultra low ESR capacitors (<100mohm), such as ceramic chip capacitors may promote instability. These very low ESR levels may cause an oscillation and/or under damped transient response. A low ESR solid tantalum capacitor works extremely well and provide good transient response and stability over temperature. Aluminum electrolytic can also be used, as long as the ESR of the capacitor is <20hm. The value of the output capacitor can be increased without limit. Higher capacitance values help to improved transient response and ripple rejection and reduce output noise.

Minimum Load Current

The TS2938 series is specified between finite loads. If the output current is too small leakage currents dominate and the output voltage rises. A 5mA minimum load current is necessary for proper regulation.

Input Capacitor

An input capacitor of 1uF or greater is recommended when the device is more that 4 inches away from the bulk AC supply capacitance or when the supply is a battery. Small and surface mount ceramic chip capacitors can be used for bypassing. Larger values will help to improve ripple rejection by bypassing the input to the regulator, further improving the integrity of the output voltage.

Thermal Characteristics

A heatsink may be required depending on the maximum power dissipation and maximum ambient temperature of the application. Under all possible operating conditions, the junction temperature must be within the range specified under absolute maximum ratings. To determine if the heatsink is required, the power dissipated by the regulator, P_D must be calculated.

The below formula shows the voltages and currents for calculating the P_D in the regulator:

```
I_{IN} = I_L / I_G

P_D = (V_{IN} - V_{OUT}) * I_L + (V_{IN}) * I_G

Ex. P_D = (3.3V - 2.5V) * 0.5A + 3.3V * 11mA

= 400mW + 36mW

= 436mW
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The next parameter which must be calculated the maximum allowable temperature rise.

 $T_R(max)$ is calculated by the using to formula:

```
T_R(max) = T_J(max) - T_A(max)
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Where: $T_J(max)$ is the maximum allowable junction temperature, which is $125^{\circ}C$ for commercial grade parts. $T_A(max)$ is the maximum ambient temperature which will be encountered in the application.

Using the calculated values for $T_A(max)$ and P_D , the maximum allowable value for the junction to ambient thermal resistance, θ_{JA} , can now be found:

 $\theta_{JA} = T_R(max) / P_D$

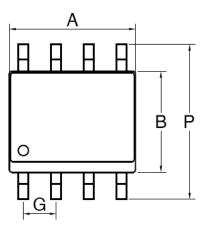
Inhibit Input

TS2938 feature an active-low (Inhibit) input that allows ON/OFF control of the regulator. The Inhibit input has TTL/CMOS compatible thresholds for simple interfacing with logic interfacing. Inhibit should be directly tied to Ground or keep open (not connected).

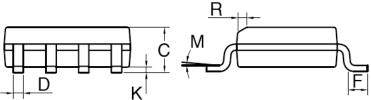




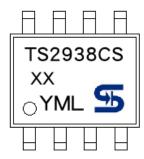
SOP-8 Mechanical Drawing



SOP-8 DIMENSION					
DIM	MILLIMETERS		INCHES		
DIIVI	MIN	MAX	MIN	MAX.	
Α	4.80	5.00	0.189	0.196	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.054	0.068	
D	0.35	0.49	0.014	0.019	
F	0.40	1.25	0.016	0.049	
G	1.27BSC		0.05BSC		
K	0.10	0.25	0.004	0.009	
М	0°	7°	0°	7°	
Р	5.80	6.20	0.229	0.244	
R	0.25	0.50	0.010	0.019	



Marking Diagram



Υ = Year Code

= Month Code

(A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)

= Lot Code

XX = Voltage Code

(3.3=3.3V, 5.0=5V, 12=12V)

= Leave the Blank for Adjustable type

TS2938

Pb Rohs COMPLIANCE

500mA Ultra Low Dropout Voltage Regulator with Inhibit Function

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