

## **PRELIMINARY**

- Designed for CDMA IS-95 Receiver IF Applications
- Simple External Impedance Matching
- Hermetic Metal DIP
- Unbalanced Input and Output



### $\int_{-\infty}^{\infty}$

Characterist	ic	Sym	Min	Тур	Max	Units	Notes
Nominal Cent	ter Frequency	fc	fc 120.000 MHz		1		
Passband	Insertion Loss at fc	IL		15	20.0	dB	
	1.5 dB Passband	BW <sub>1.5</sub>	±450	±480		kHz	1, 2
	Phase Linearity over fc ±450 kHz			8	20	°P-P	
	Triple Transit Rejection	TTR	40	42		dB	
Rejection	fc-900 to fc-875 and fc+875 to fc+900 kHz		35	45		dB	1, 2, 3
	At fc ±900 kHz		50	54			
10 MHz to fc-900 kHz and fc+900 kHz to 500 MHz			50	51			
Operating Temperature Range		T <sub>A</sub>	-20		+70	°C	1

Impedance Matching to 50 $\Omega$ unbalanced	External L-C			
Case Style	DIP18-8 27.2 x 12.6 mm Nominal Footprint			
Lid Symbolization (YY = year, WW = week)	RFM SF1108 YYWW			

# **Absolute Maximum Ratings**

Rating	Value	Units
Maximum Incident Power in Passband	+10	dBm
Max. DC voltage between any 2 terminals	30	VDC
Storage Temperature Range	-40 to +85	°C
Max Soldering Profile	265°C for	10 s

#### Electrical Connections (See note 3)

Connection	Terminals			
Port 1 Hot	18			
Port 1 Gnd Return	1			
Port 2 Hot	9			
Port 2 Gnd Return	10			
Case Ground	All others			

#### Notes:

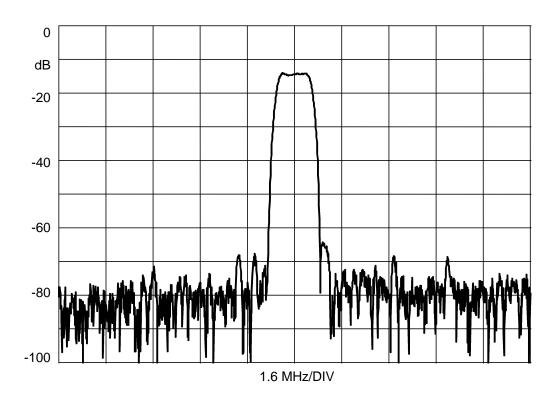
- 1. Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with appropriate external impedance matching to 50  $\Omega$  and measured with 50  $\Omega$  network analyzer.
- 2. Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, fc.
- 3. Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details. All "NC" or "no connection pins should be grounded.
- 4. "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."
- 5. The design, manufacturing process, and specifications of this filter are subject to change.
- 6. Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
- 7. US and international patents may apply.
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- 10. Electrostatic Sensitive Device. Observe precautions for handling.

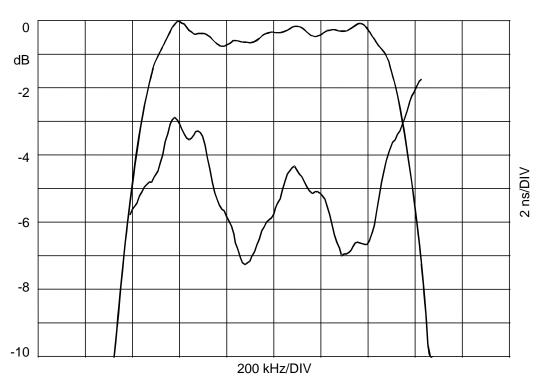


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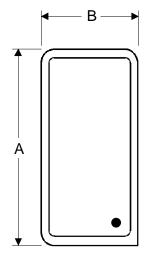


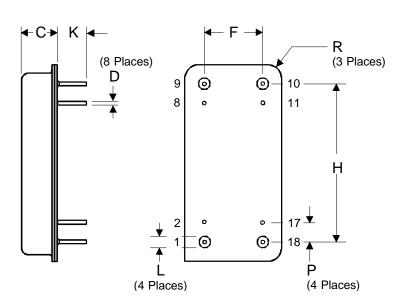


# Metal 8-Pin DIP in 18-Pin Configuration 27.2 x 12.6 mm Nominal Footprint



Dimension	mm		Inches			
Dimension	Min	Nom	Max	Min	Nom	Max
Α		27.18	27.56		1.070	1.085
В		12.55	12.95		0.494	0.510
С		3.56	5.08		0.140	0.200
D	0.41	0.48	0.51	0.016	0.019	0.020
Е		20.32			0.800	
F		7.62			0.300	
K	3.30	3.81	6.73	0.130	0.150	0.265
L	1.37	1.45	1.52	0.054	0.057	0.060
Р		2.54			0.100	
R		2.03			0.080	





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