

NPN SiGe RF TRANSISTOR FOR MEDIUM OUTPUT POWER AMPLIFICATION (800 mW) 3-PIN POWER MINIMOLD (34 PACKAGE)

FEATURES

- This product is suitable for medium output power (800 mW) amplification
 - $P_o = 29 \text{ dBm TYP. } @ V_{CE} = 3.6 \text{ V, } P_{in} = 15 \text{ dBm, } f = 460 \text{ MHz}$
 - $P_o = 29 \text{ dBm TYP. } @ V_{CE} = 3.6 \text{ V, } P_{in} = 20 \text{ dBm, } f = 900 \text{ MHz}$
- MSG (Maximum Stable Gain) = 23 dB TYP., @ $V_{CE} = 3.6 \text{ V, } I_c = 100 \text{ mA, } f = 460 \text{ MHz}$
- Using UHS2-HV process (SiGe technology), V_{CBO} (ABSOLUTE MAXIMUM RATINGS) = 20 V
- 3-pin power minimold (34 package)

ORDERING INFORMATION

Part Number	Quantity	Supplying Form
NESG250134-A	25 pcs (Non reel)	<ul style="list-style-type: none"> 12 mm wide embossed taping
NESG250134-T1-A	1 kpcs/reel	<ul style="list-style-type: none"> Pin 2 (Emitter) face the perforation side of the tape

Remark To order evaluation samples, contact your nearby sales office.

Unit sample quantity is 25 pcs.

ABSOLUTE MAXIMUM RATINGS ($T_A = +25^\circ\text{C}$)

Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	V_{CBO}	20	V
Collector to Emitter Voltage	V_{CEO}	9.2	V
Emitter to Base Voltage	V_{EBO}	2.8	V
Collector Current	I_c	500	mA
Total Power Dissipation	P_{tot}^{Note}	1.5	W
Junction Temperature	T_j	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-65 to +150	$^\circ\text{C}$

Note Mounted on $34.2 \text{ cm}^2 \times 0.8 \text{ mm (t)}$ glass epoxy PWB

Caution: Observe precautions when handling because these devices are sensitive to electrostatic discharge

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THERMAL RESISTANCE ($T_A = +25^\circ\text{C}$)

Parameter	Symbol	Ratings	Unit
Termal Resistance from Junction to Ambient ^{Note}	$R_{\text{thj-a}}$	80	$^\circ\text{C/W}$

Note Mounted on $34.2 \text{ cm}^2 \times 0.8 \text{ mm (t)}$ glass epoxy PWB

RECOMMENDED OPERATING RANGE ($T_A = +25^\circ\text{C}$)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Collector to Emitter Voltage	V_{CE}	–	3.6	4.5	V
Collector Current	I_C	–	400	500	mA
Input Power ^{Note}	P_{in}	–	12	17	dBm

Note Input power under conditions of $V_{\text{CE}} \leq 4.5 \text{ V}$, $f = 460 \text{ MHz}$

ELECTRICAL CHARACTERISTICS (T_A = +25°C)

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Collector Cut-off Current	I _{CB0}	V _{CB} = 5 V, I _E = 0 mA	–	–	1	μA
Emitter Cut-off Current	I _{EB0}	V _{EB} = 0.5 V, I _C = 0 mA	–	–	1	μA
DC Current Gain	h _{FE} ^{Note 1}	V _{CE} = 3 V, I _C = 100 mA	80	120	180	–
RF Characteristics						
Gain Bandwidth Product	f _T	V _{CE} = 3.6 V, I _C = 100 mA, f = 460 MHz	–	10	–	GHz
Insertion Power Gain	S _{21e} ²	V _{CE} = 3.6 V, I _C = 100 mA, f = 460 MHz	–	19	–	dB
Maximum Satble Gain	MSG ^{Note 2}	V _{CE} = 3.6 V, I _C = 100 mA, f = 460 MHz	–	23	–	dB
Linner gain (1)	G _L	V _{CE} = 3.6 V, I _{C (set)} = 30 mA (RF OFF), f = 460 MHz, P _{in} = 0 dBm	16	19	–	dB
Linner gain (2)	G _L	V _{CE} = 3.6 V, I _{C (set)} = 30 mA (RF OFF), f = 900 MHz, P _{in} = 0 dBm	–	16	–	dB
Output Power (1)	P _O	V _{CE} = 3.6 V, I _{C (set)} = 30 mA (RF OFF), f = 460 MHz, P _{in} = 15 dBm	27	29	–	dBm
Output Power (2)	P _O	V _{CE} = 3.6 V, I _{C (set)} = 30 mA (RF OFF), f = 900 MHz, P _{in} = 20 dBm	–	29	–	dBm
Collector Efficiency (1)	η _C	V _{CE} = 3.6 V, I _{C (set)} = 30 mA (RF OFF), f = 460 MHz, P _{in} = 15 dBm	–	60	–	%
Collector Efficiency (2)	η _C	V _{CE} = 3.6 V, I _{C (set)} = 30 mA (RF OFF), f = 900 MHz, P _{in} = 20 dBm	–	60	–	%

Notes 1. Pulse measurement: PW ≤ 350 μs, Duty Cycle ≤ 2%

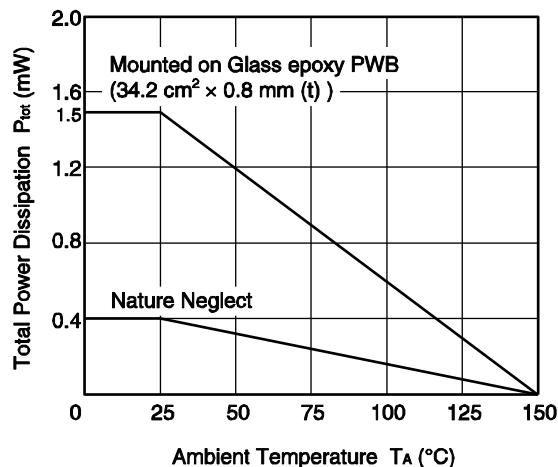
$$2. \text{ MSG} = \left| \frac{S_{21}}{S_{12}} \right|$$

h_{FE} CLASSIFICATION

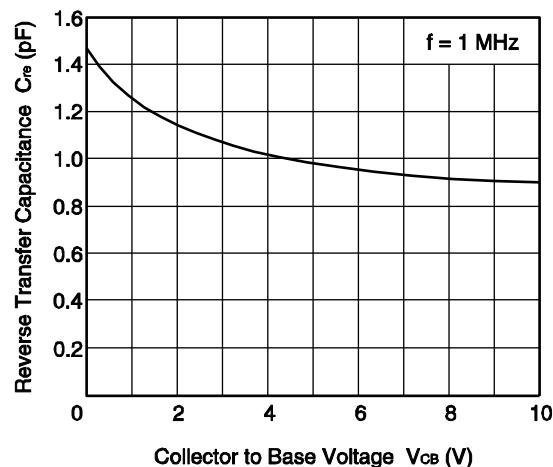
Rank	FB
Marking	SN
h _{FE} Value	80 to 180

• TYPICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$, unless otherwise specified)

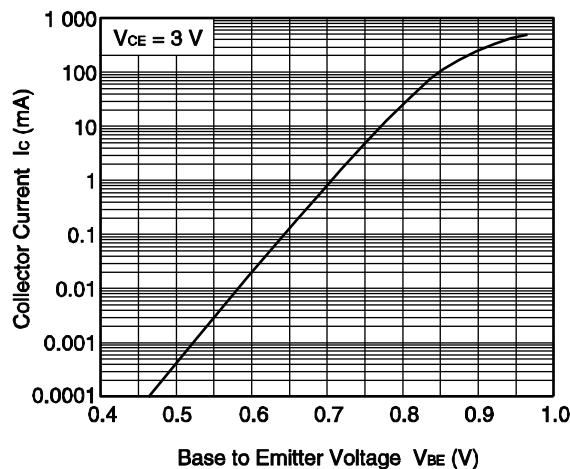
TOTAL POWER DISSIPATION
vs. AMBIENT TEMPERATURE



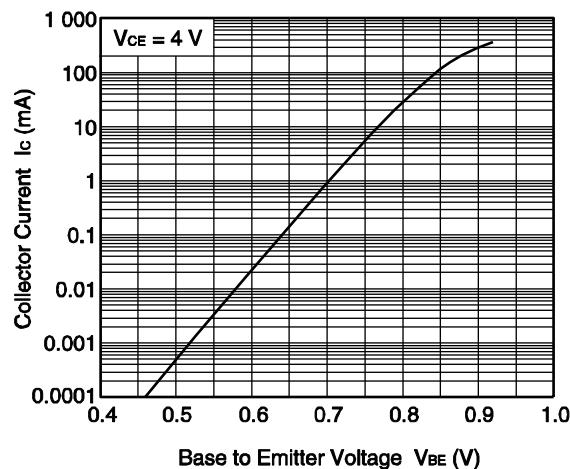
REVERSE TRANSFER CAPACITANCE
vs. COLLECTOR TO BASE VOLTAGE



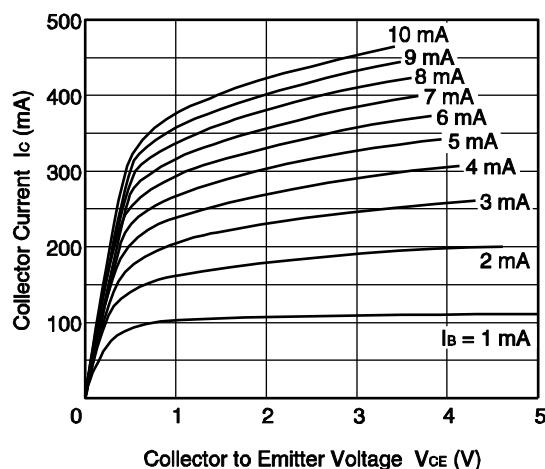
COLLECTOR CURRENT vs.
BASE TO Emitter VOLTAGE



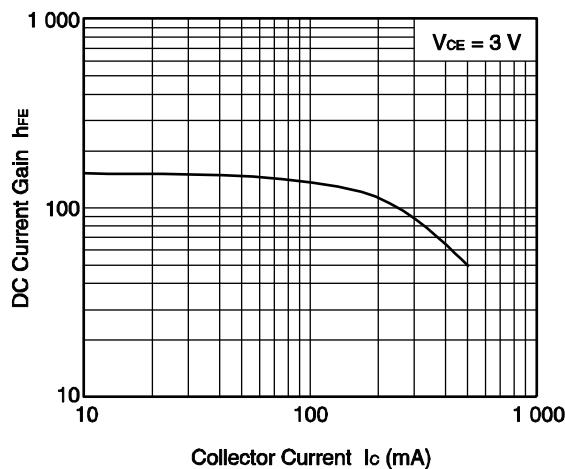
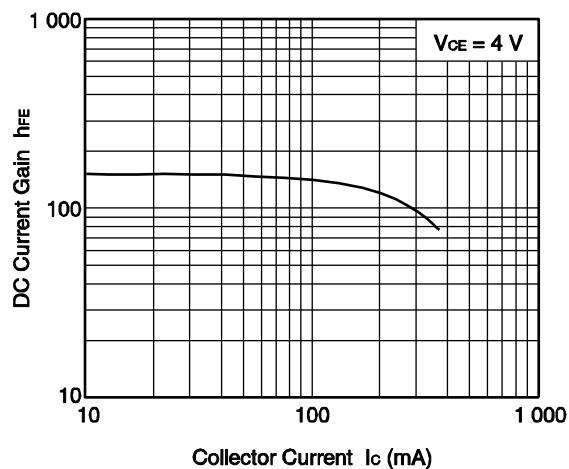
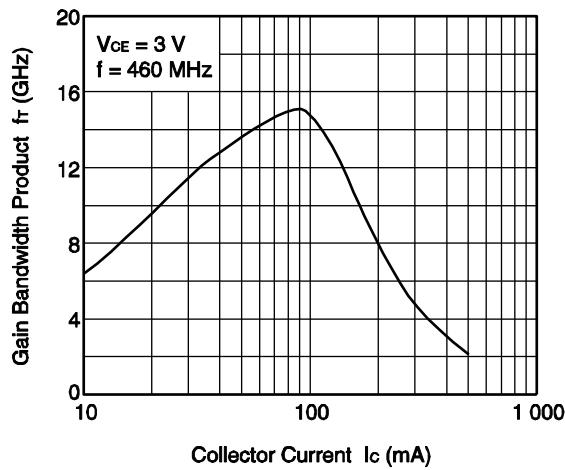
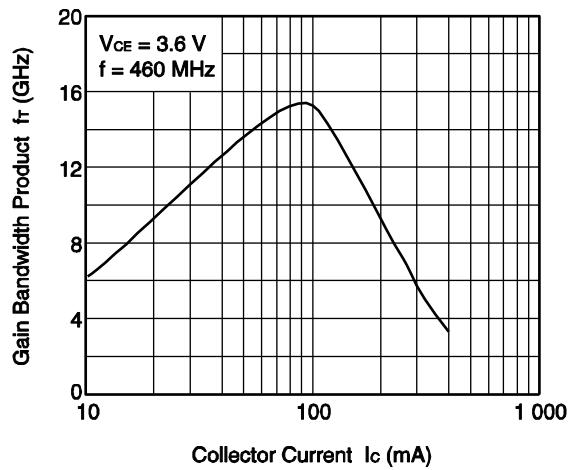
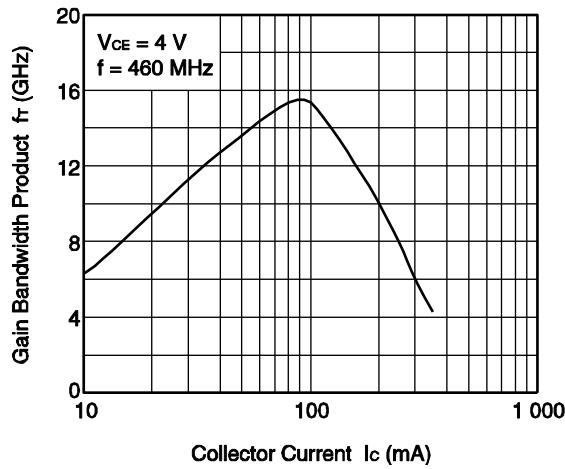
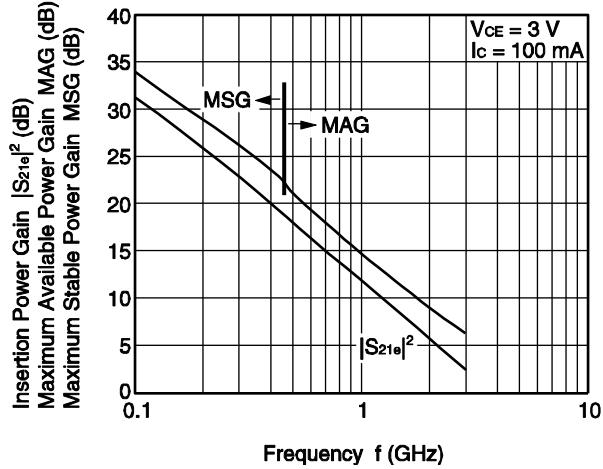
COLLECTOR CURRENT vs.
BASE TO Emitter VOLTAGE



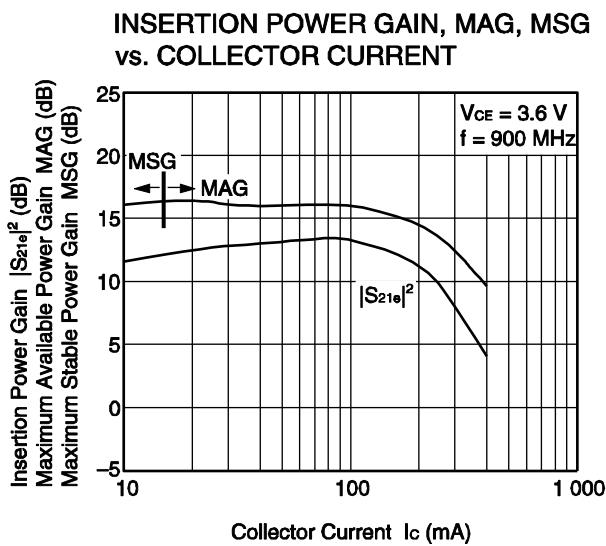
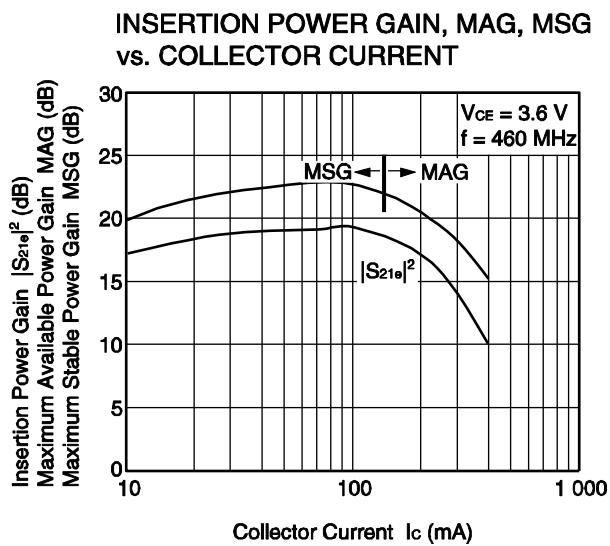
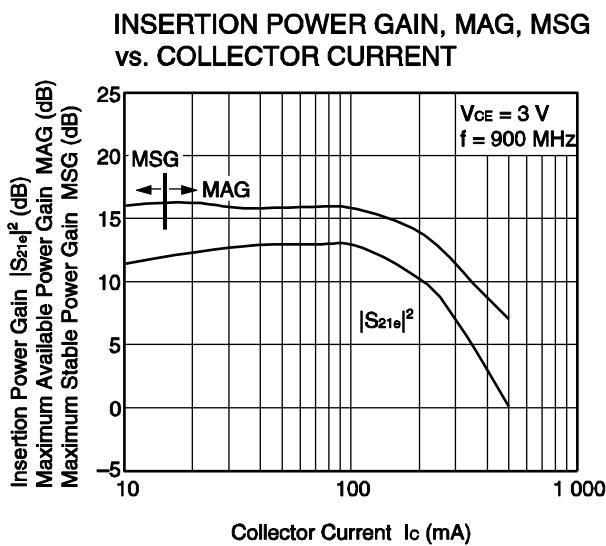
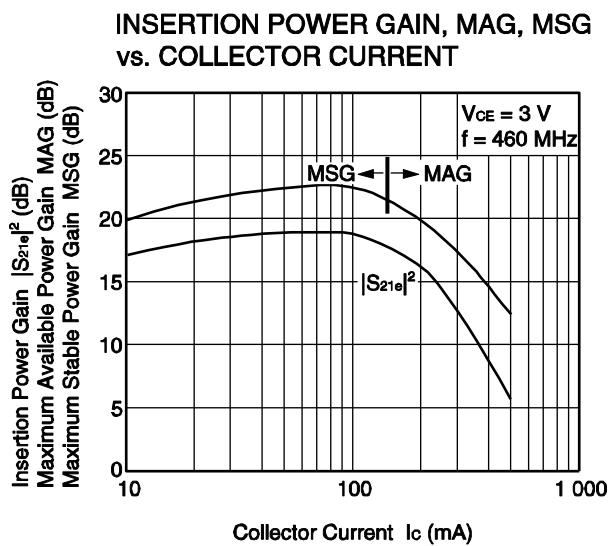
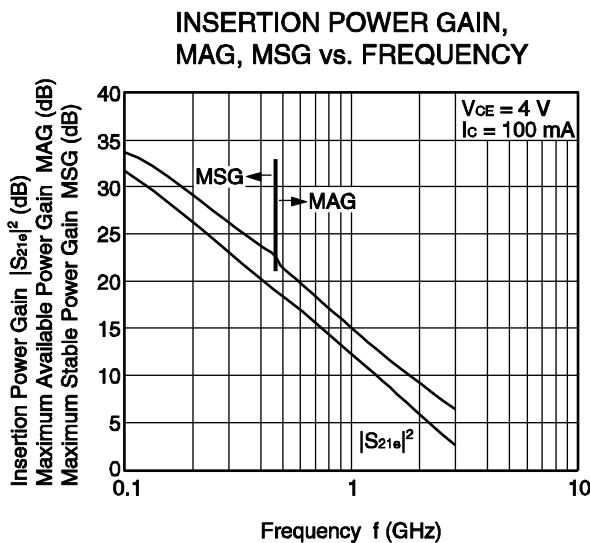
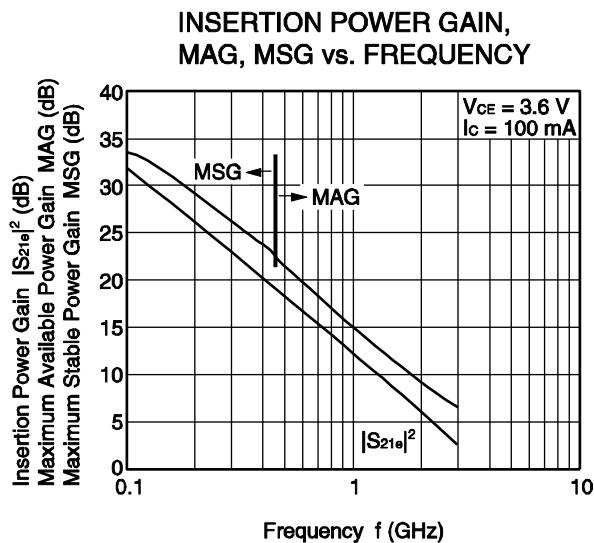
COLLECTOR CURRENT vs.
COLLECTOR TO Emitter VOLTAGE



Remark The graphs indicate nominal characteristics.

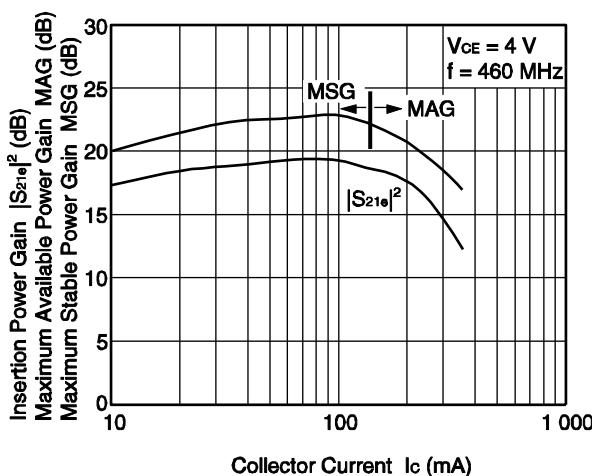
DC CURRENT GAIN vs.
COLLECTOR CURRENTDC CURRENT GAIN vs.
COLLECTOR CURRENTGAIN BANDWIDTH PRODUCT
vs. COLLECTOR CURRENTGAIN BANDWIDTH PRODUCT
vs. COLLECTOR CURRENTGAIN BANDWIDTH PRODUCT
vs. COLLECTOR CURRENTINSERTION POWER GAIN,
MAG, MSG vs. FREQUENCY

Remark The graphs indicate nominal characteristics.

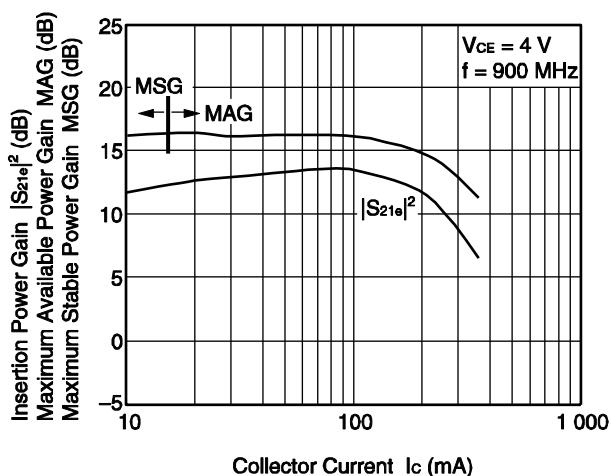


Remark The graphs indicate nominal characteristics.

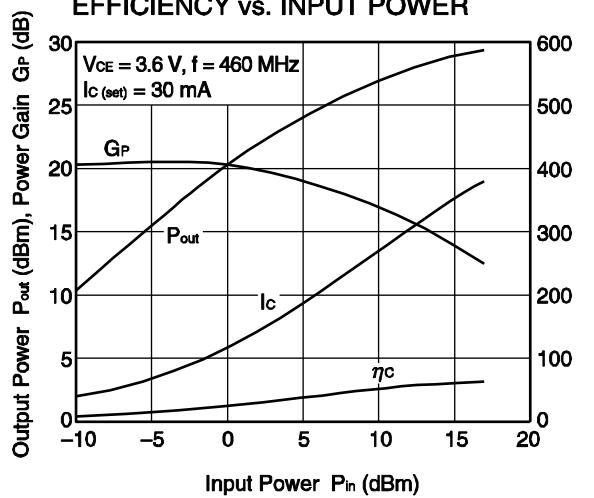
INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT



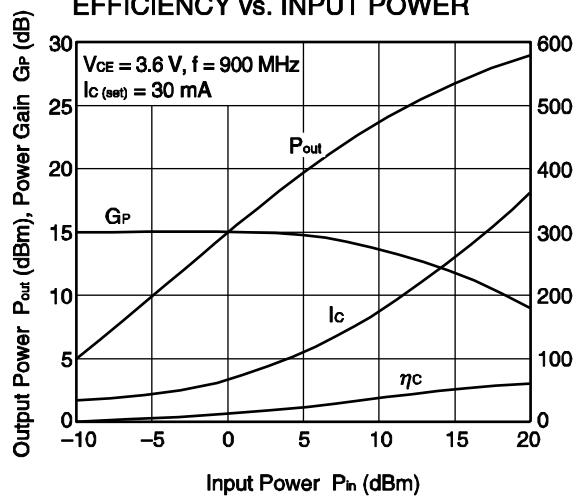
INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT



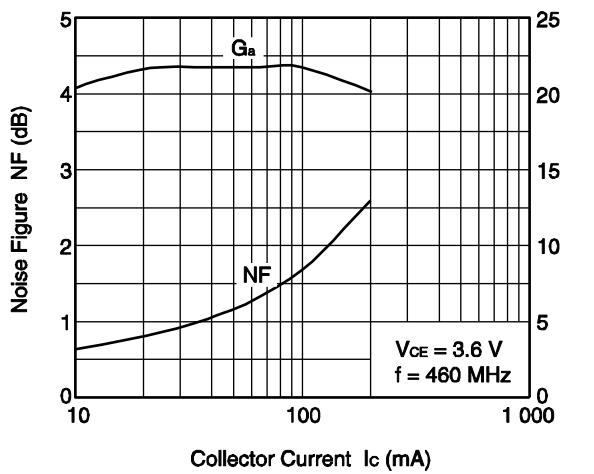
OUTPUT POWER, POWER GAIN, COLLECTOR CURRENT, COLLECTOR EFFICIENCY vs. INPUT POWER

Collector Current I_c (mA), Collector Efficiency η_c (%)

OUTPUT POWER, POWER GAIN, COLLECTOR CURRENT, COLLECTOR EFFICIENCY vs. INPUT POWER

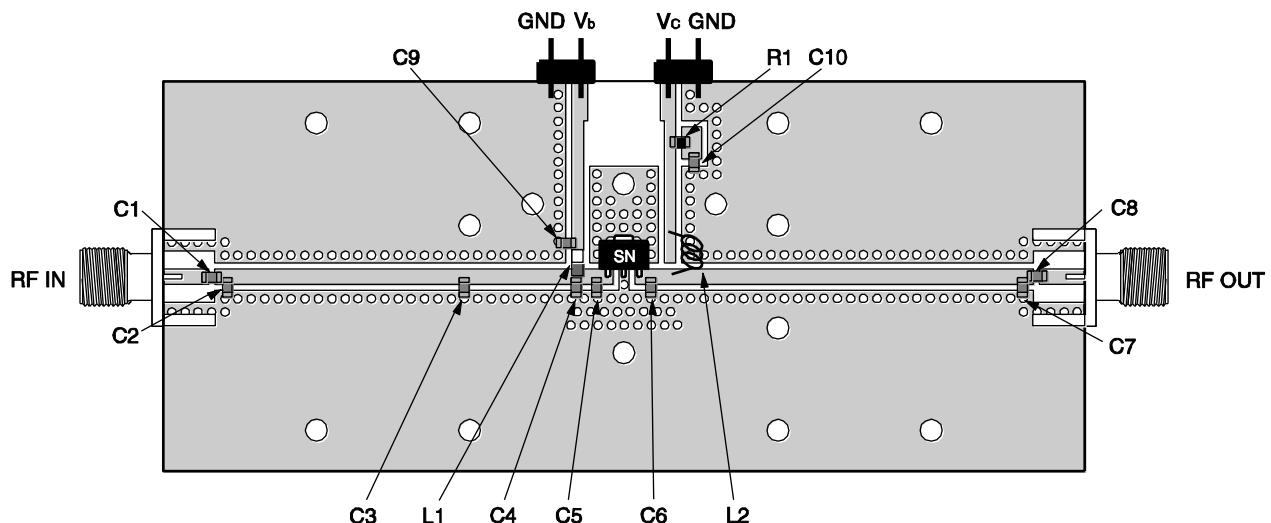
Collector Current I_c (mA), Collector Efficiency η_c (%)

NOISE FIGURE, ASSOCIATED GAIN vs. COLLECTOR CURRENT

Associated Gain G_a (dB)

Remark The graphs indicate nominal characteristics.

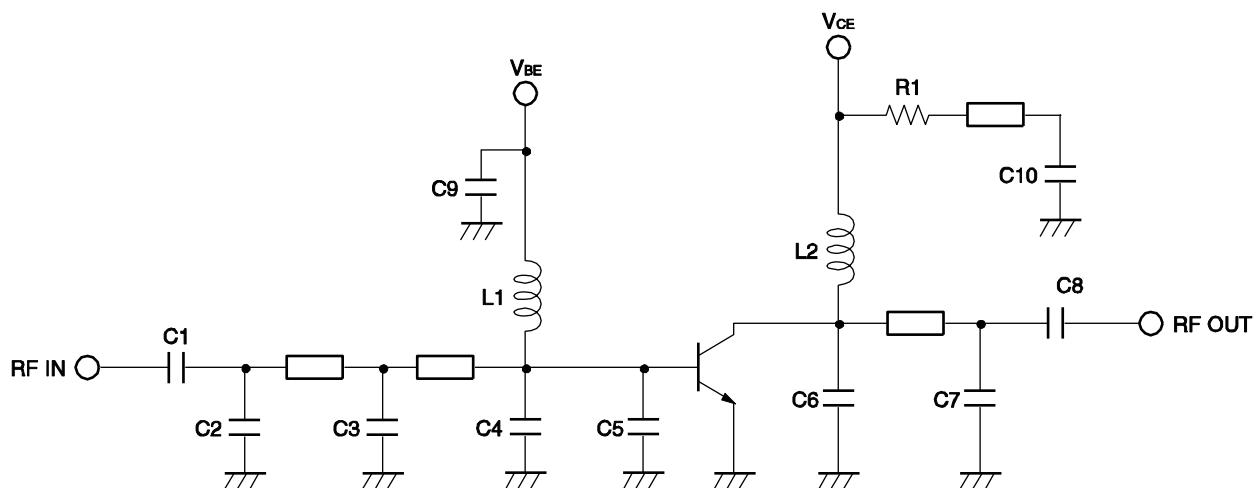
■ PA EVALUATION BOARD (f = 460 MHz)



Notes

1. $38 \times 90 \text{ mm}$, $t = 0.8 \text{ mm}$ double sided copper clad glass epoxy PWB.
2. Back side: GND pattern
3. Solder gold plated on pattern
4. $\circ \bigcirc$: Through holes

■ PA EVALUATION CIRCUIT (f = 460 MHz)

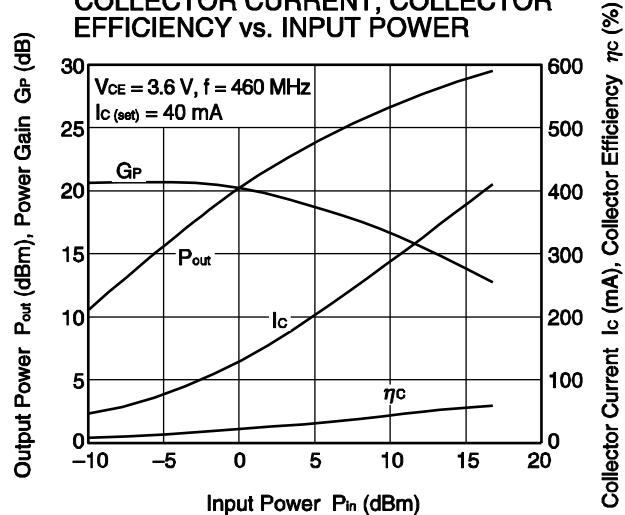


The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

COMPONENT LIST

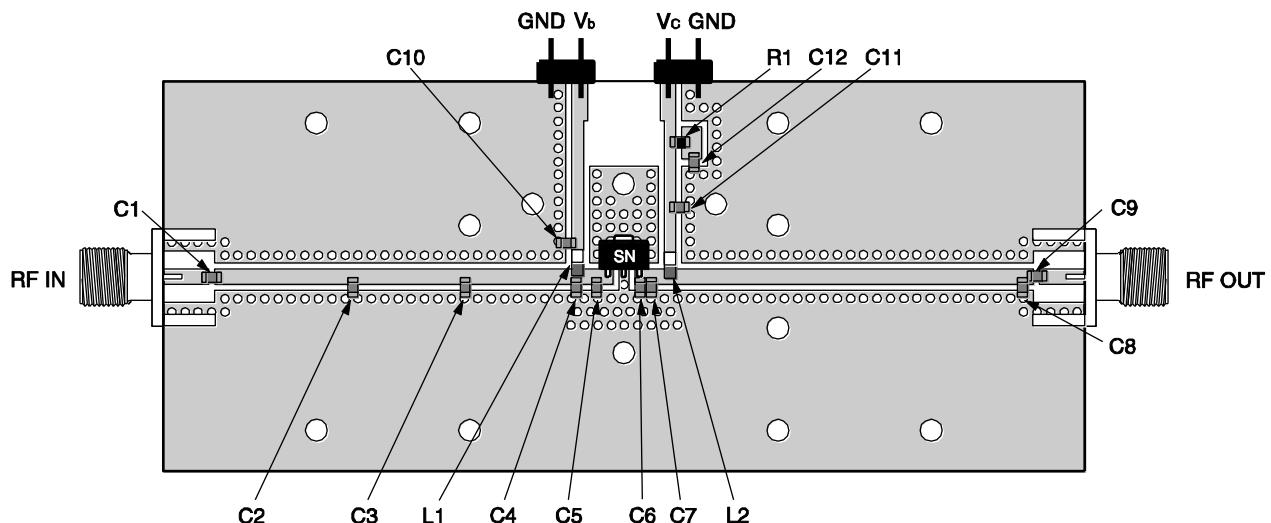
	Value	Maker
C1	30 pF	Murata
C2	6 pF	Murata
C3, C4	7 pF	Murata
C5	3 pF	Murata
C6	0.5 pF	Murata
C7	5 pF	Murata
C8	10 pF	Murata
C9, C10	100 nF	Murata
L1	100 nH	Toko
L2	3 nH	Toko
R1	30 Ω	SSM

PA EVALUATION CIRCUIT TYPICAL CHARACTERISTICS

OUTPUT POWER, POWER GAIN,
COLLECTOR CURRENT, COLLECTOR
EFFICIENCY vs. INPUT POWER

Remark The graph indicates nominal characteristics.

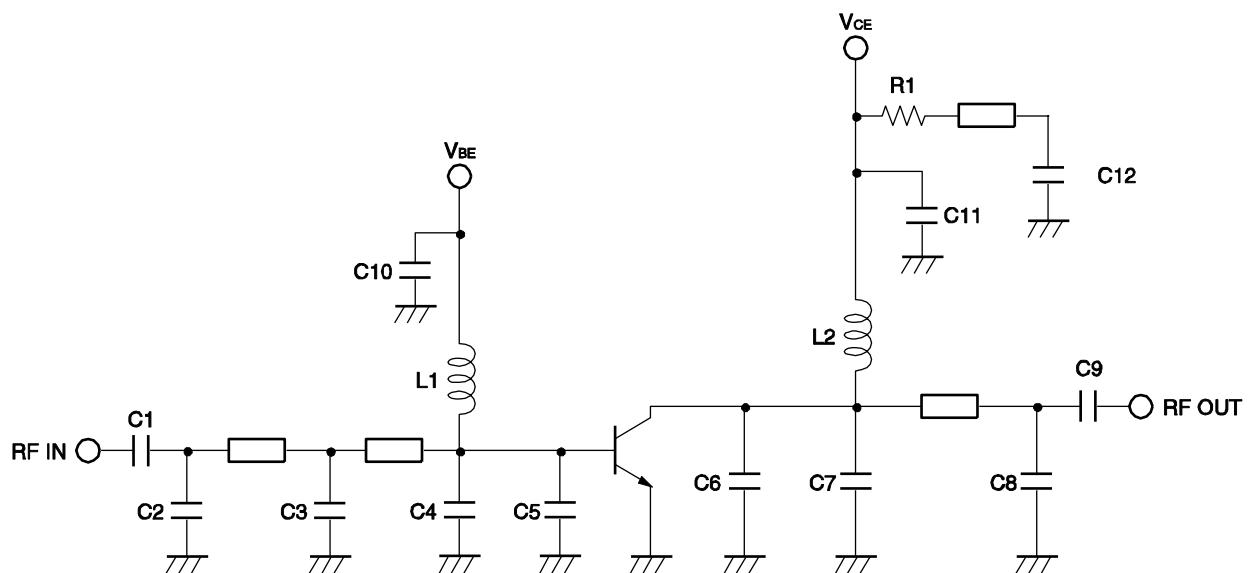
■ DISTORTION EVALUATION BOARD (f = 460 MHz)



Notes

1. $38 \times 90 \text{ mm}$, $t = 0.8 \text{ mm}$, double sided copper clad glass epoxy PWB.
2. Back side: GND pattern
3. Solder gold plated on pattern
4. $\circ \bigcirc$: Through holes

■ DISTORTION EVALUATION CIRCUIT (f = 460 MHz)

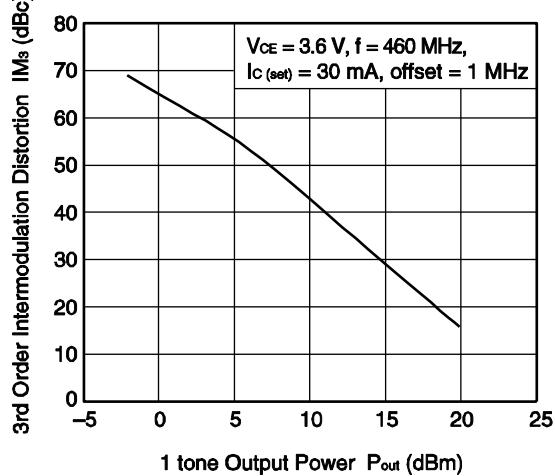


The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

COMPONENT LIST

	Value	Maker
C1	47 pF	Murata
C2	12 pF	Murata
C3, C4	7 pF	Murata
C5	3 pF	Murata
C6	6 pF	Murata
C7	0.5 pF	Murata
C8	5 pF	Murata
C9	51 pF	Murata
C10, C12	100 nF	Murata
C11	1 μ F	Murata
L1	100 nH	Toko
L2	15 nH	Toko
R1	30 Ω	SSM

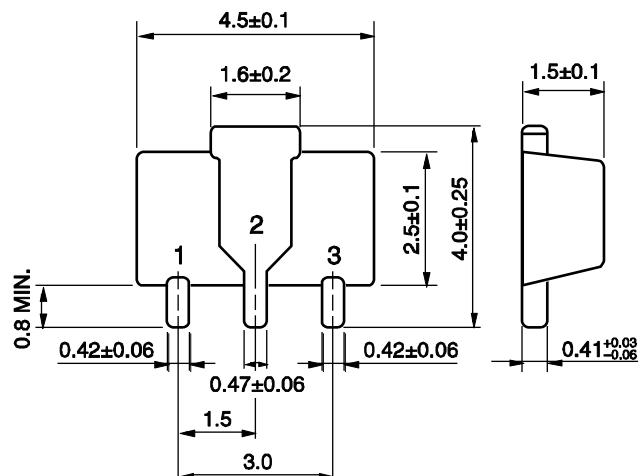
DISTORTION EVALUATION CIRCUIT TYPICAL CHARACTERISTICS

3RD ORDER
INTERMODULATION DISTORTION
vs. 1 TONE OUTPUT POWER

Remark The graph indicates nominal characteristics.

PACKAGE DIMENSIONS

3-PIN POWER MINIMOLD (34 PACKAGE) (UNIT: mm)



PIN CONNECTIONS

1. Collector
2. Emitter
3. Base

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