

January 1997

**NOT RECOMMENDED
FOR NEW DESIGNS**
Use CMOS Technology

CD74FCT841A, CD74FCT842A

**BiCMOS FCT Interface Logic,
10-Bit Transparent Latches, Three-State**

Features

- Buffered Inputs
- Typical Propagation Delay: 6.8ns at $V_{CC} = 5V$, $T_A = 25^\circ C$, $C_L = 50pF$ (CD74FCT841A)
- CD74FCT841A
 - Noninverting
- CD74FCT842A
 - Inverting
- SCR Latchup Resistant BiCMOS Process and

Circuit Design

- Speed of Bipolar FAST™/AS/S
- 48mA Output Sink Current
- Output Voltage Swing Limited to 3.7V at $V_{CC} = 5V$
- Controlled Output Edge Rates
- Input/Output Isolation to V_{CC}
- BiCMOS Technology with Low Quiescent Power

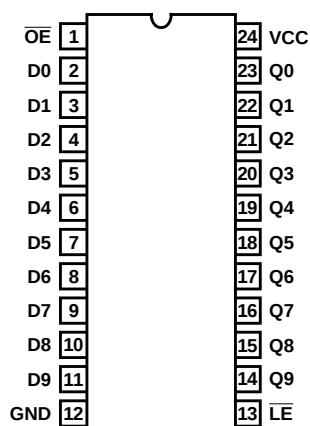
Ordering Information

PART NUMBER	TEMP. RANGE ($^\circ C$)	PACKAGE	PKG. NO.
CD74FCT841AEN	0 to 70	24 Ld PDIP	E24.3
CD74FCT841AM	0 to 70	24 Ld SOIC	M24.3
CD74FCT842AM	0 to 70	24 Ld SOIC	M24.3

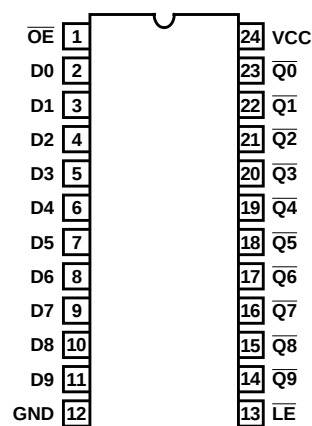
NOTE: When ordering the suffix M packages, use the entire part number. Add the suffix 96 to obtain the variant in the tape and reel.

Pinouts

CD74FCT841A
(PDIP, SOIC)
TOP VIEW



CD74FCT842A
(PDIP, SOIC)
TOP VIEW



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