

October 2012

BC556/557/558/559/560 PNP Epitaxial Silicon Transistor

Features

· Switching and Amplifier

High Voltage: BC556, V_{CEO} = -65V
Low Noise: BC559, BC560

• Complement to BC546 ... BC 550



Absolute Maximum Ratings $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage		
//	: BC556	-80	V
	: BC557/560	-50	V
	: BC558/559	-30	V
V_{CEO}	Collector-Emitter Voltage		
	: BC556	-65	V
	: BC557/560	-45	V
	: BC558/559	-30	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current (DC)	-100	mA
P _C	Collector Power Dissipation	500	mW
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-65 ~ 150	°C

Electrical Characteristics $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CBO}	Collector Cut-off Current	V _{CB} = -30V, I _E =0	7		-15	nA
h _{FE}	DC Current Gain	V _{CE} = -5V, I _C =2mA	110		800	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I_{C} = -10mA, I_{B} = -0.5mA I_{C} = -100mA, I_{B} = -5mA		-90 -250	-300 -650	mV mV
V _{BE} (sat)	Collector-Base Saturation Voltage	I _C = -10mA, I _B = -0.5mA I _C = -100mA, I _B = -5mA		-700 -900		mV mV
V _{BE} (on)	Base-Emitter On Voltage	V_{CE} = -5V, I_{C} = -2mA V_{CE} = -5V, I_{C} = -10mA	-600	-660	-750 -800	mV mV
f _T	Current Gain Bandwidth Product	V_{CE} = -5V, I_{C} = -10mA, f =10MHz		150		MHz
C _{ob}	Output Capacitance	V _{CB} = -10V, I _E =0, f=1MHz			6	pF
NF	Noise Figure : BC556/557/558 : BC559/560	V_{CE} = -5V, I_{C} = -200μA f=1KHz, R_{G} =2K Ω		2 1	10 4	dB dB
	: BC559 : BC560	V_{CE} = -5V, I_{C} = -200μA R_{G} =2KΩ, f =30~15000MHz		1.2 1.2	4 2	dB dB

h_{FE} Classification

Classification	А	В	С		
h _{FE}	110 ~ 220	200 ~ 450	420 ~ 800		

Typical Performance Characteristics

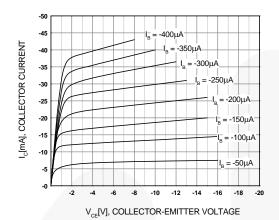


Figure 1. Static Characteristic

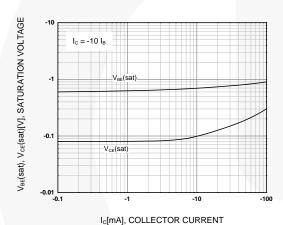


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

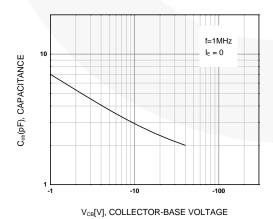


Figure 5. Collector Output Capacitance

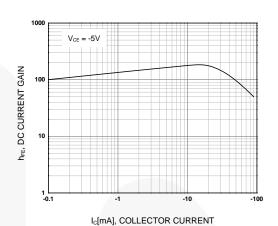


Figure 2. DC current Gain

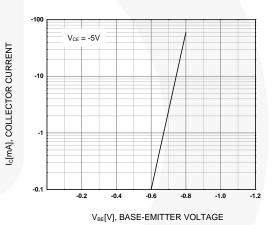


Figure 4. Base-Emitter On Voltage

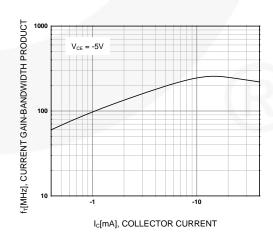
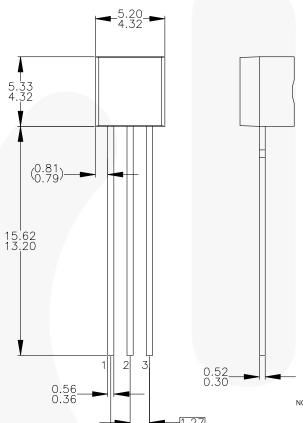
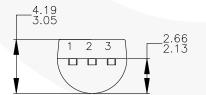


Figure 6. Current Gain Bandwidth Product

Physical Dimensions

TO-92





NOTES: UNLESS OTHERWISE SPECIFIED

- DRAWING WITH REFERENCE TO JEDEC TO-92 RECOMMENDATIONS.
 ALL DIMENSIONS ARE IN MILLIMETERS.
 DRAWING CONFORMS TO ASME Y14.5M-1994.
 TO-92 (92,94,96,97,98) PIN CONFIGURATION:

z		92			94			96			97		98		
α.	Ρ	F	М	Ρ	F	М	В	F	М	Р	F	М	Р	F	М
1	Ε	S	S	Ε	S	S	В	D	G	С	G	D	С	G	D
2	В	D	G	C	G	D	Ε	S	S	В	D	G	Ε	S	S
3	С	G	D	В	D	G	С	G	D	Ε	S	S	В	D	G

LEGEND: P - BIPOLAR F - JFET M - DMOS E - EMITTER
B - BASE
C - COLLECTOR D - DRAIN S - SOURCE G - GATE

- E) FOR PACKAGE 92, 94, 96, 97 AND 98:
 PIN CONFIGURATION DRAIN "D" AND SOURCE "S"
 ARE INTERCHANGEAGLE AT JFET "F" OPTION.
 F) DRAWING FILENAME: MKT-ZAO3DREV3.

Dimensions in Millimeters





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Definition of Terms							
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