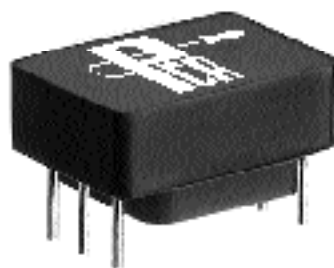


EPA2465

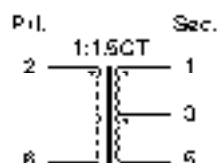


- Used In Crystal Semiconductor's CS61681 Chips •
- Through Hole Package •
- 1900 Vrms Isolation •

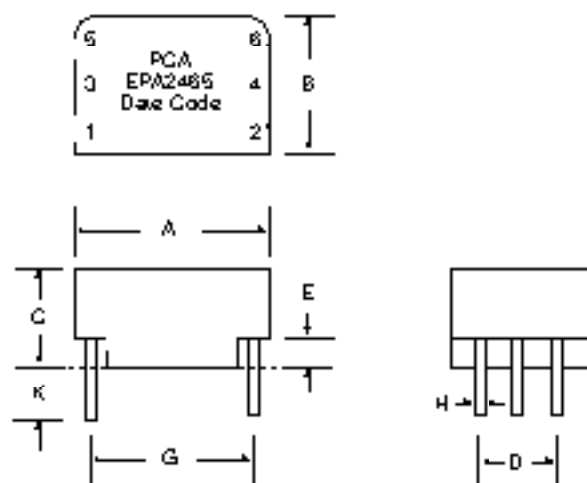
Electrical Parameters @ 25°C

Primary Inductance (mH Min.)	Interwinding Capacitance (pF Max.)	Leakage Inductance (μH Max.)	Turns Ratio (±5%)	DCR (Ω Max.)	
@100 KHz. 20 mVrms	@100 KHz. 20 mVrms	@100 KHz. 20 mVrms		Pri.	Sec.
1.5	35	80	1 : 1.15CT	.70	.90

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	.490	.500	.490	12.19	12.7	12.45
B	.300	.390	.340	9.39	9.99	8.64
C	.200	.290	.240	5.94	8.35	6.10
D			.200			5.09
E	.075	.095	.090	1.91	2.16	2.03
G			.400			10.16
H	.017	.023	.020	.432	.594	.509
K			.100			3.30