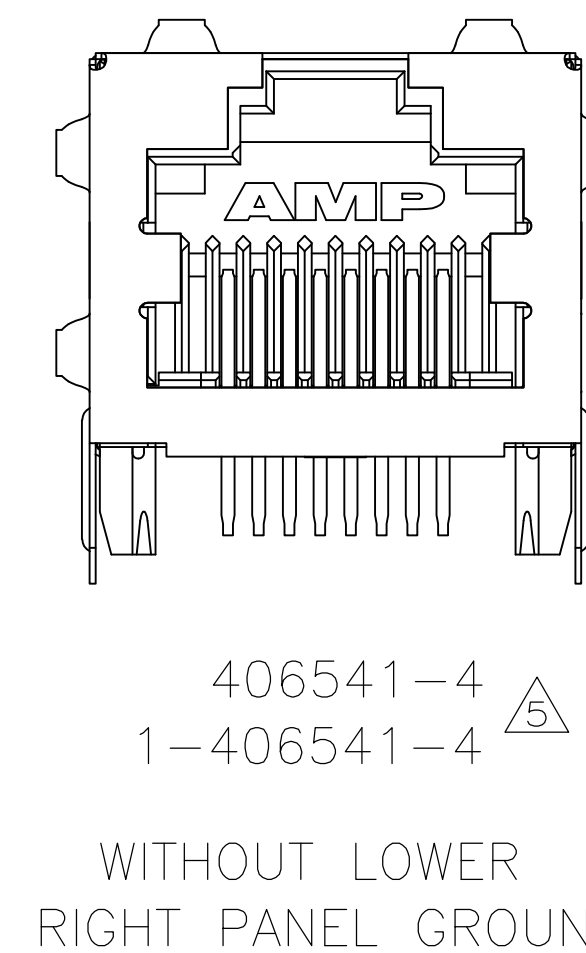
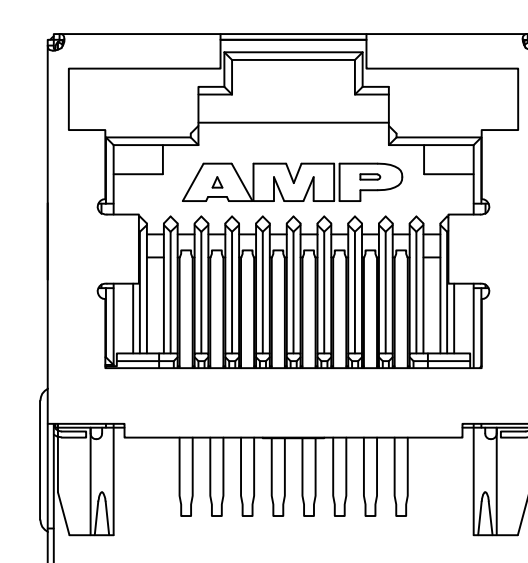
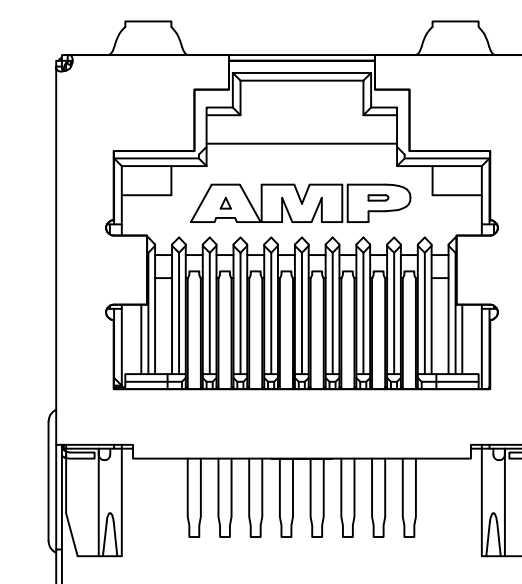




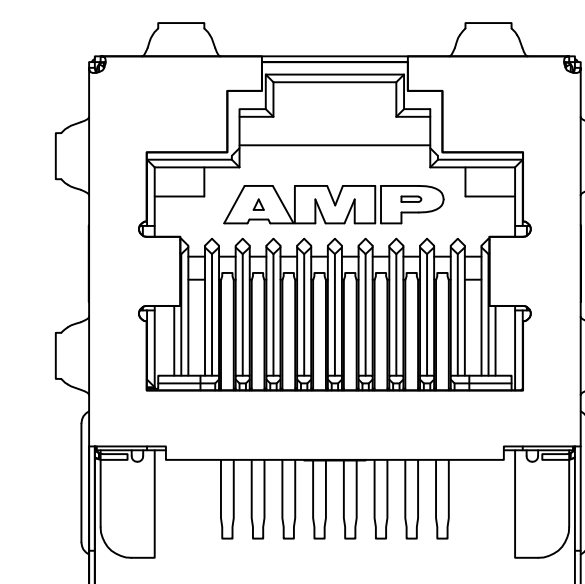
$$-Z-$$



1-406541-7

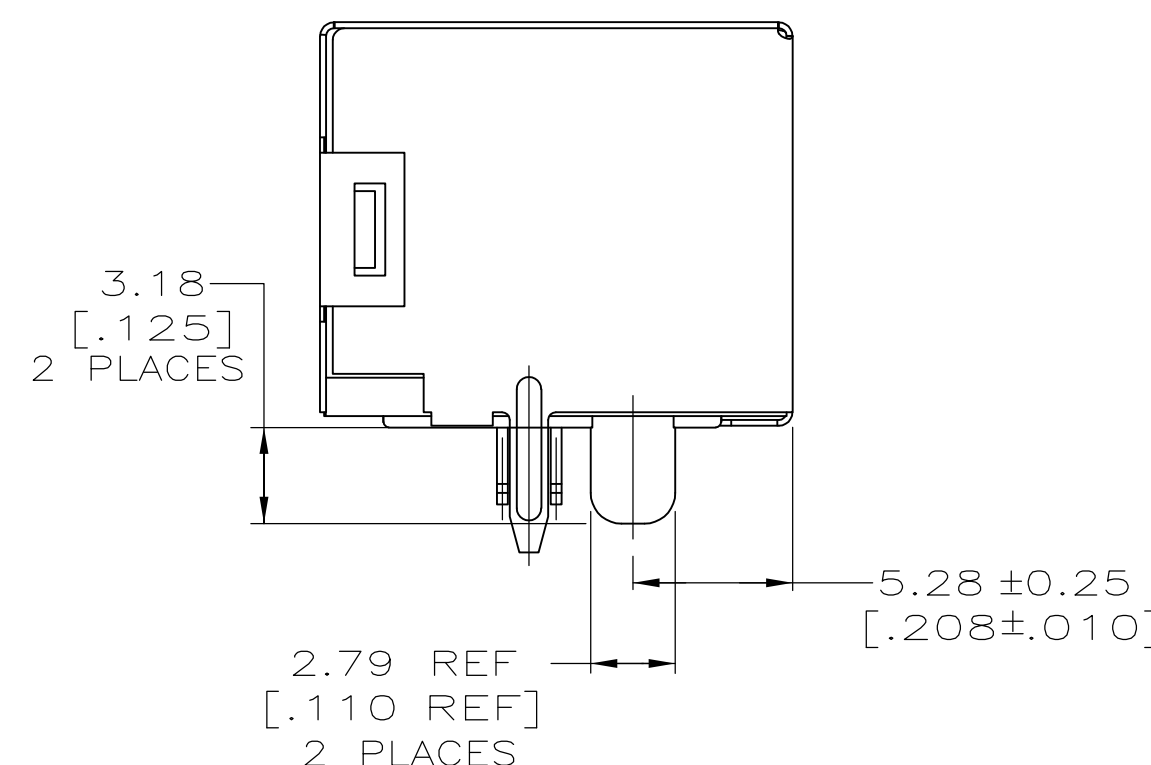


2-406541-1
2-406541-4
WITHOUT SIDE PANEL GROUND

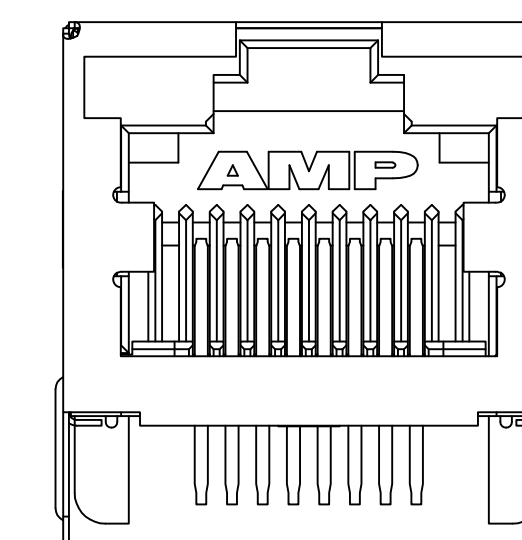


2-406541-3

NARROW POST TYPE



△ 2-406541-0 , 2-2
NARROW POST TYPE

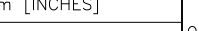


7 OBSOLETE

OBSOLETE —

MATTE TIN	TAPE/REEL	2	2.54[.100]	2-406541-4	
	TRAY	6	3.05[.120]	2-406541-3	
	TRAY	0	2.54[.100]	2-406541-2	
	TRAY	2	2.54[.100]	2-406541-1	
	TAPE/REEL	0	2.54[.100]	2-406541-0	
	TRAY	6	3.05[.120]	1-406541-9	
	TAPE/REEL	0	2.54[.100]	1-406541-8	
	TRAY	6	2.54[.100]	1-406541-7	
	TRAY	6	3.56[.140]	1-406541-6	
	TRAY	0	2.54[.100]	1-406541-5	
BRIGHT TIN-LEAD	TRAY	5	2.54[.100]	1-406541-4	
	TRAY	6	2.00[.079]	1-406541-3	
	TRAY	6	2.54[.100]	1-406541-1	
	TRAY	6	3.56[.140]	406541-6	
	TRAY	0	2.54[.100]	406541-5	
	TRAY	5	2.54[.100]	406541-4	
	TRAY	6	2.00[.079]	406541-3	
	TRAY	6	2.54[.100]	406541-1	
	TERMINAL PLATING	PACKAGING	PANEL GROUNDS	DIM A	PART NUMBER

SUGGESTED PRINTED CIRCUIT BOARD LAYOUT
(COMPONENT SIDE)

THIS DRAWING IS A CONTROLLED DOCUMENT.		DIN TS / L.A. MAYER CHK J. WESTMAN APVD S. FLICKINGER		09/JAN/2008 09/JAN/2008 09/JAN/2008		 TE Connectivity	
DIMENSIONS: mm [INCHES]		TOLERANCES, UNLESS OTHERWISE SPECIFIED:		NAME		INVERT MODULAR JACK ASSEMBLY 1 X 1, SHIELDED, PANEL GROUND	
		0 PLC ± - 1 PLC ± - 2 PLC ± ± 0.2% [0.10] 3 PLC ± ± - 4 PLC ± ± - ANGLES ± ± -		PRODUCT SPEC - APPLICATION SPEC -		SIZE -	
MATERIAL		FINISH		WEIGHT		RESTRICTED TO	
SEE TABLE		SEE TABLE		-		-	
CUSTOMER DRAWING				A1 00779 C=406541		SCALE 4:1 SHEET 1 of 1 REV N	