

Products > ADC1443D200W0 demo board; compliant with Altera, Xilinx FPGA boards through specific connectors

ADC1443D200W0 Obsolete

ADC1443D200W0 demo board; compliant with Altera, Xilinx FPGA boards through specific connectors

The IDT ADC demonstration board is suitable for dynamic performances evaluation from low to high IF configuration. A FPGA mother board (Xilinx, Altera) could be connected to ease the evaluation and analysis of the ADC dynamic and enable usage of the JESD204B full features sets.

Features

- SMA connector for clock and analog input signals
- JESD204B outputs up to 5 Gbps (HSMC connector)
- Optional HSMC to FMC adaptor board
- Single power supply (on-board regulators)

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Buy Sample
ADC1443D200W0-DB	Obsolete	BD0	BOARD	0	I	No	Availability

Technical Documentation

[SHOW ALL COLUMNS](#)

Title	Format	File Size	Date
User Guides & Manuals			
ADC1443D and 53D Evaluation board user manual guide	PDF	4.47 MB	Jun 2, 2014
Demo Board Software ADC4	ZIP	4.59 MB	Jun 2, 2014
Other			
ADC1443D-53D Evaluation Board Schematics&Layout	PDF	4.20 MB	Jun 2, 2014

Related Products

Product ID	Title ▼
ADC1443D200HD	Dual channel 14-bit ADC; 200 Msps; JESD204B serial outputs

DOWNLOAD MANUAL - EVAL BOARD



Quick Links

[Product Options](#)
[Technical Documentation](#)
[Related Products](#)

Tools & Support

TECHNICAL SUPPORT	
REQUEST INFORMATION	
CONTACT A SALES REPRESENTATIVE	

Images

ADC1443D-53DWOP Evaluation Demo Board

