

August 2012

QSE156, QSE157, QSE158, QSE159 QSE156C, QSE157C, QSE158C, QSE159C Plastic Silicon OPTOLOGIC[®] Photosensor

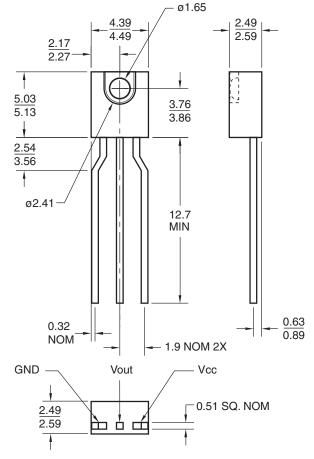
Features

- Bipolar silicon IC
- Package type: Sidelooker
- Medium wide reception angle, 50°
- Package material and color: black epoxy
- Also available in clear package: "C" suffix
- Matched emitter: QEE113/QEE123
- Daylight filter
- High sensitivity
- Direct TTL/LSTTL interface

Description

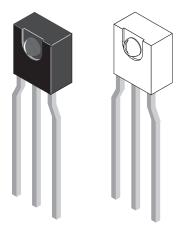
The QSE15X family are OPTOLOGIC® ICs which feature a Schmitt trigger at output which provides hysteresis for noise immunity and pulse shaping. The basic building block of this IC consists of a photodiode, a linear amplifier, voltage regulator, Schmitt trigger and four output options. The TTL/LSTTL compatible output can drive up to ten TTL loads over supply currents from 4.5 to 16.0 Volts. The devices are marked with a color stripe for easy identification.

Package Dimensions



Note:

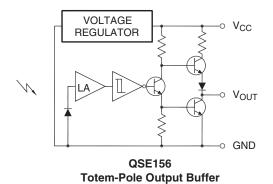
1. Dimensions for all drawings are in millimeters.

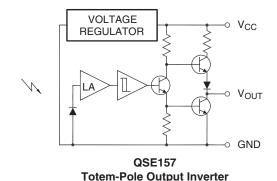


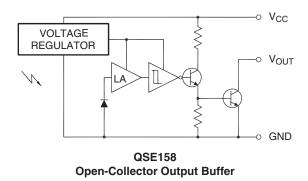
Pa	Color Code	
QSE156/C	Totem-Pole, buffer output	Red
QSE157/C	Totem-Pole, inverter output	Yellow
QSE158/C	Open-collector, buffer output	Green
QSE159/C	Open-collector, inverter output	Blue

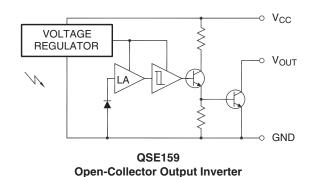
Input/Output Table				
Part Number	Light	Output		
QSE156/C	On	HIGH		
	Off	LOW		
QSE157/C	On	LOW		
	Off	HIGH		
QSE158/C	On	HIGH		
	Off	LOW		
QSE159/C	On	LOW		
	Off	HIGH		

Block Diagrams









Absolute Maximum Ratings (T_A = 25°C unless otherwise specified)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating	Unit	
T _{OPR}	Operating Temperature	-40 to +85	°C	
T _{STG}	Storage Temperature	-40 to +100	°C	
T _{SOL-I}	Soldering Temperature (Iron) ^(2,3,4) 240 for 5		°C	
T _{SOL-F}	Soldering Temperature (Flow) ^(2,3) 260 for 10 sec		°C	
Io	Output Current	50	mA	
V _{CC}	Supply Voltage	4.0 to 16	V	
Vo	Output Voltage	35	V	
P _D	Power Dissipation ⁽¹⁾	100	mW	

Notes:

- 1. Derate power dissipation linearly 2.50mW/°C above 25°C.
- 2. RMA flux is recommended.
- 3. Methanol or isopropyl alcohols are recommended as cleaning agents.
- 4. Soldering iron tip 1/16" (1.6mm) minimum from housing.

Electrical Characteristics ($T_A = -40$ °C to +85°C, $V_{CC} = 4.5$ V to 16V)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
Ee(+)	Positive Going Threshold Irradiance ⁽⁵⁾	T _A = 25°C	0.025		0.250	mW/cm ²
Ee(+)/Ee(-)	Hysteresis Ratio		1.10		2.00	
I _{CC}	Supply Current ⁽⁵⁾	Ee = 0 or 0.3mW/cm ²			5.0	mA
	Peak to Peak Ripple which will Cause False Triggering	f = DC to 50MHz			2.00	V
QSE156/C (Buffer Totem Pole)					
V _{OH}	High Level Output Voltage ⁽⁵⁾	Ee = 0.3 mW/cm ² , I_{OH} = -10 mA	V _{CC} – 2.1			V
V _{OL}	Low Level Output Voltage	Ee = 0, I _{OL} = 16mA			0.40	V
QSE157/C (Inverter Totem Pole)					
V _{OH}	High Level Output Voltage	Ee = 0, I _{OH} = -10mA	V _{CC} – 2.1			V
V _{OL}	Low Level Output Voltage ⁽⁵⁾	$Ee = 0.3 \text{mW/cm}^2$, $I_{OL} = 16 \text{mA}$			0.40	V
QSE158/C (Buffer Open Collector)					
I _{OH}	High Level Output Current ⁽⁵⁾	Ee = 0.3 mW/cm ² , $V_{OH} = 30$ V			100	μA
V _{OL}	Low Level Output Voltage	Ee = 0, I _{OL} = 16mA			0.40	V
QSE159/C (nverter Open Collector)					
I _{OH}	High Level Output Current	Ee = 0, V _{OH} = 30V			100	μΑ
V _{OL}	Low Level Output Voltage ⁽⁵⁾	Ee = 0.3 mW/cm ² , I_{OL} = 16 mA			0.40	V
QSE156/C, 0	QSE157/C					
t _R , t _F	Output Rise, Fall Times	Ee = 0 or 0.3 mW/cm ² ,			70	nS
t _{PHL} , t _{PLH}	Propagation Delay	f = 10kHz, DC = 50%, $R_L = 360\Omega^{(5)}$		6.0		μS
QSE158/C, 0	QSE159/C					
t _R , t _F	Output Rise, Fall Times	Ee = 0 or 0.3 mW/cm ² ,			100	nS
t _{PHL} , t _{PLH}	Propagation Delay	f = 10kHz, DC = 50%, $R_L = 360\Omega^{(5)}$		6.0		μS

Note:

5. λ = 880nm (AlGaAs).

Typical Performance Curves (Sensor Coupled to QEE113 Emitter)

Fig. 1 Output Voltage vs. Input Current (Inverters)

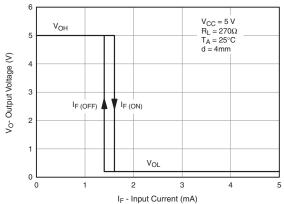


Fig. 2 Output Voltage vs. Input Current (Buffers)

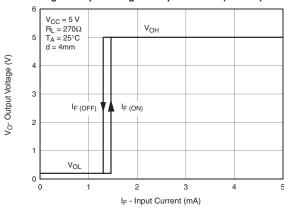


Fig. 3 Threshold Current vs. Distance

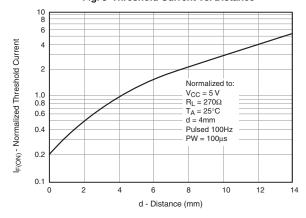
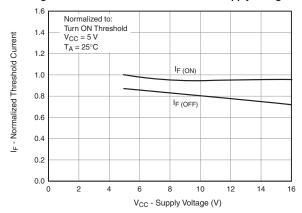
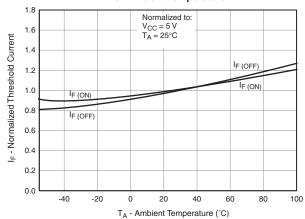


Fig. 4 Normalized Threshold Current vs. Supply Voltage



Typical Performance Curves (Sensor Coupled to QEE113 Emitter) (Continued)

Fig. 5 Normalized Threshold Current vs. Ambient Temperature



0

IO - Output Current (mA)

Fig. 6 Low Output Voltage vs. Output Current

Fig. 7 Response Time vs. Forward Current

0.01

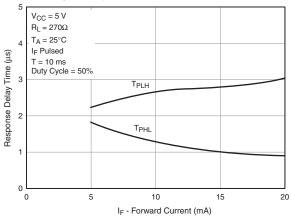
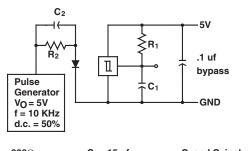


Fig. 8 Switching Speed Test Circuit



 $R_1 = 360\Omega$ $R_2 = 180\Omega$ C₁= 15 pf C₂= 20 pf C₁ and C₂ include probe and stray wire capacitance

Fig. 9 Switching Times Definition for Buffers

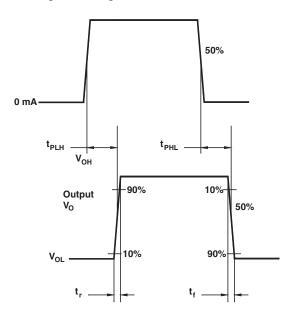
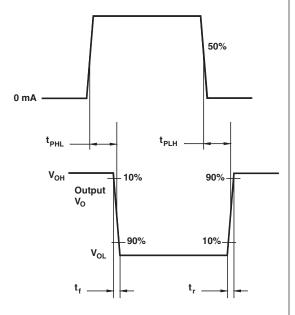


Fig. 10 Switching Times Definition for Inverters







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