

PMEG3002EJ

30 V, 200 mA low VF Schottky barrier rectifier

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD323F (SC-90) small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: I_{F(AV)} ≤ 0.2 A
- Reverse voltage: V_R ≤ 30 V
- · Low forward voltage
- Small and flat lead SMD plastic package

3. Applications

- · Low voltage rectification
- · High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- · Reverse polarity protection
- · Ultra high-speed switching
- · Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{amb} ≤ 135 °C	[1]	-	-	0.2	А
		δ = 0.5; f = 20 kHz; square wave; T _{sp} ≤ 145 °C		-	-	0.2	A
V_R	reverse voltage	T _j = 25 °C		-	-	30	V
V _F	forward voltage	I _F = 200 mA; T _j = 25 °C		-	420	480	mV
I _R	reverse current	V _R = 30 V; T _j = 25 °C		-	10	40	μA

[1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.



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5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	1 2	К .[К.] А
2	А	anode	SC-90 (SOD323F)	sym001

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG3002EJ		plastic, surface-mounted package; 2 leads; 1.7 mm x 1.25 mm x 0.7 mm body	SOD323F

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG3002EJ	1M

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	30	V
I _{F(AV)}	average forward current	δ = 0.5; f = 20 kHz; square wave; T _{amb} ≤ 135 °C	[1]	-	0.2	А
		δ = 0.5; f = 20 kHz; square wave; $T_{sp} \le$ 145 °C		-	0.2	А
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ ms}; \delta \le 0.25$		-	2.6	А
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; square wave; T_j = 25 °C prior to surge; $T_{j(init)}$ = 25 °C		-	2.75	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[2] [3]	-	385	mW
			[2] [4]	-	695	mW
			[5] [2]	-	1.045	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- [1] Device mounted on a ceramic Printed-Circuit Board (PCB), Al₂O₃, standard footprint.
- [2] Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- 4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [5] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.

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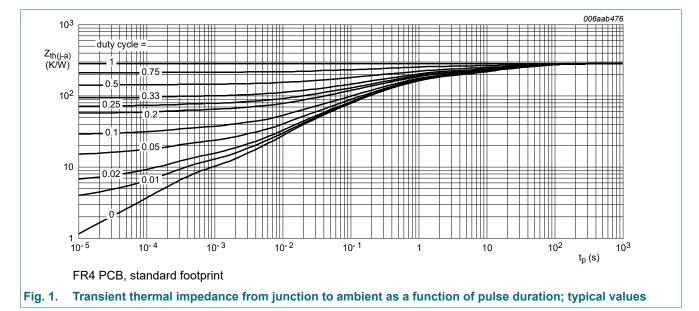
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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2] [3]	-	-	325	K/W
			[1] [2] [4]	-	-	180	K/W
			[1] [2] [5]	-	-	120	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[6]	-	-	25	K/W

- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Reflow soldering is the only recommended soldering method.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².
- [5] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [6] Soldering point of cathode tab.



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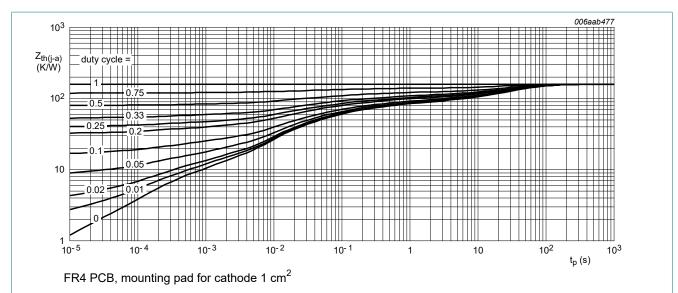


Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

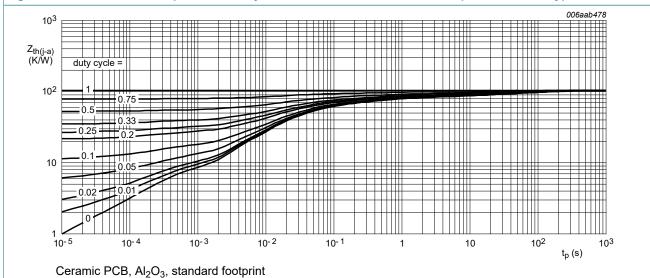


Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _F	forward voltage	I _F = 0.1 mA; T _j = 25 °C	-	130	190	mV
		I _F = 1 mA; T _j = 25 °C	-	190	250	mV
ı		I _F = 10 mA; T _j = 25 °C	-	250	300	mV
		I _F = 100 mA; T _j = 25 °C	-	355	400	mV
		I _F = 200 mA; T _j = 25 °C	-	420	480	mV
I _R	reverse current	V _R = 10 V; T _j = 25 °C	-	2.5	10	μΑ
		V _R = 30 V; T _j = 25 °C	-	10	40	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	18	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	7	-	pF
t _{rr}	reverse recovery time	When switched from I_F = 10 mA to I_R = 10 mA; R_L = 100 Ω ; measured at I_R = 1 mA.; T_j = 25 °C	-	5	-	ns

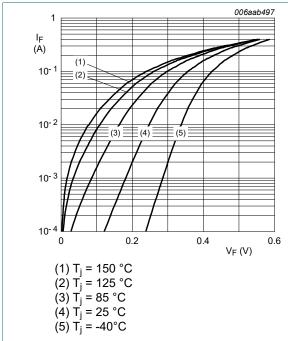


Fig. 4. Forward current as a function of forward voltage; typical values

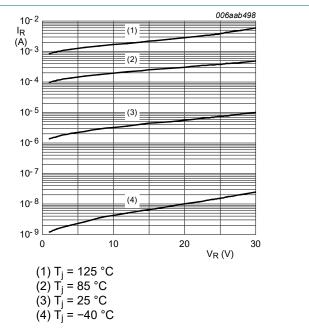


Fig. 5. Reverse current as a function of reverse voltage; typical values

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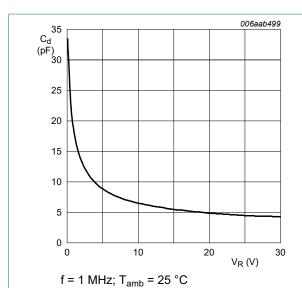
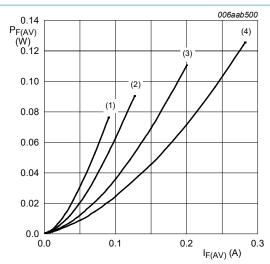
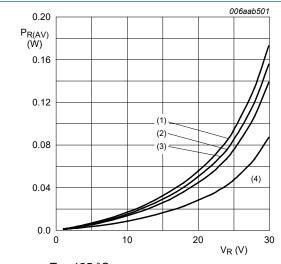


Fig. 6. Diode capacitance as a function of reverse voltage; typical values



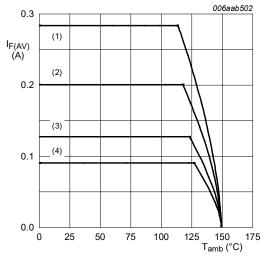
 $T_j = 150 \,^{\circ}\text{C}$ (1) $\delta = 0.1$ (2) $\delta = 0.2$ (3) $\delta = 0.5$ (4) $\delta = 1$

Fig. 7. Average forward power dissipation as a function of average forward current; typical values



 $T_j = 125 \,^{\circ}\text{C}$ (1) $\delta = 1$ (2) $\delta = 0.9$ (3) $\delta = 0.8$ (4) $\delta = 0.5$

Fig. 8. Average reverse power dissipation as a function of reverse voltage; typical values



FR4 PCB, standard footprint $T_j = 150 \,^{\circ}\text{C}$ (1) $\delta = 1$; DC

(2) $\delta = 0.5$; f = 20 kHz

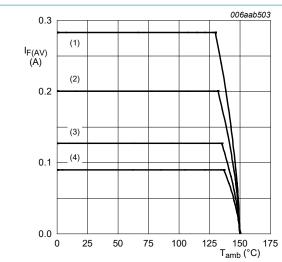
(3) $\delta = 0.2$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz(4) $\delta = 0.1$; f = 20 kHz

Fig. 9. Average forward current as a function of ambient temperature; typical values

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FR4 PCB, mounting pad for cathode 1 cm²

 $T_i = 150 \, ^{\circ}C$

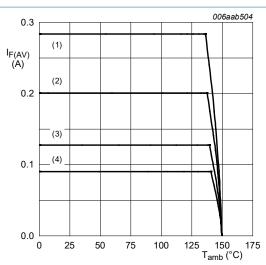
 $(1) \delta = 1; DC$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 10. Average forward current as a function of ambient temperature; typical values



Ceramic PCB, Al₂O₃, standard footprint

T_i = 150 °C

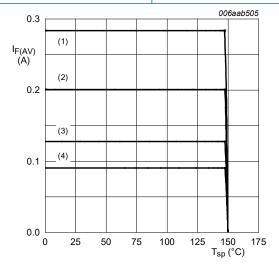
 $(1) \delta = 1; DC$

(2) $\delta = 0.5$; f = 20 kHz

(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 11. Average forward current as a function of ambient temperature; typical values



T_i = 150 °C

 $(1) \delta = 1$; DC

(2) $\delta = 0.5$; f = 20 kHz

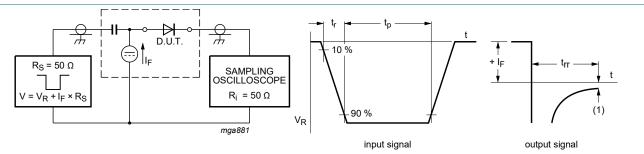
(3) $\delta = 0.2$; f = 20 kHz

(4) $\delta = 0.1$; f = 20 kHz

Fig. 12. Average forward current as a function of solder point temperature; typical values

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11. Test information



(1) $I_R = 1 \text{ mA}$

Input signal: reverse pulse rise time t_r = 0.6 ns; reverse voltage pulse duration t_p = 100 ns; duty cycle δ = 0.05 Oscilloscope rise time t_r = 0.35 ns

Fig. 13. Reverse recovery time: test circuit and waveforms

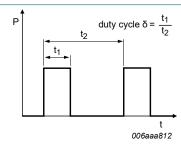


Fig. 14. Duty cycle definition

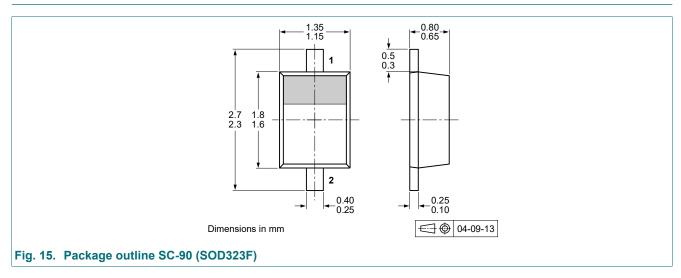
The current ratings for the typical waveforms are calculated according to the equations:

 $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current

 $I_{RMS} = I_{F(AV)}$ at DC

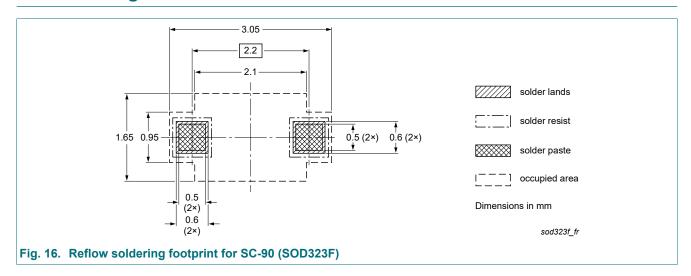
 I_{RMS} = I_{M} × $\sqrt{\delta}$ with I_{RMS} defined as RMS current

12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes					
PMEG3002EJ v.3	20250930	Product data sheet	-	PMEG3002EJ v.2					
Modifications:		 Product(s) changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s). 							
PMEG3002EJ v.2	20231101	Product data sheet	-	PMEG3002EJ v.1					
PMEG3002EJ v.1	20090515	Product data sheet	-	-					

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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