TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74LCX245F,TC74LCX245FW,TC74LCX245FT

#### Low-Voltage Octal Bus Transceiver with 5-V Tolerant Inputs and Outputs

The TC74LCX245F/FW/FT is a high-performance CMOS octal bus transceiver. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for both inputs and outputs.

The direction of data transmission is determined by the level of the DIR input. The enable input (OE) can be used to disable the device so that the busses are effectively isolated.

All inputs are equipped with protection circuits against static discharge.

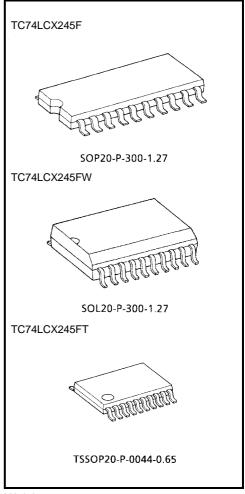
#### **Features**

- Low-voltage operation:  $V_{CC} = 2.0$  to 3.6 V
- High-speed operation:  $t_{pd} = 7.0 \text{ ns (max)} \text{ (VCC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Ouput current: |IOH|/IOL = 24 mA (min) (VCC = 3.0 V)
- Latch-up performance: ±500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Bidirectional interface between 5.0 V and 3.3 V signals
- · Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 245 type

Note: Do not apply a signal to any bus pins when it is in the output mode. Damage may result.

All floating (high impedance) bus pins must have their input levels fixed by means of pull-up or pull-down resistors.

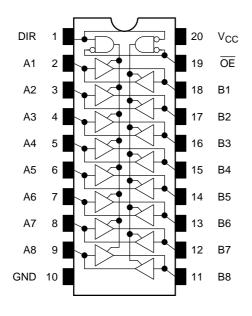
Note: xxxFW (JEDEC SOP) is not available in Japan.



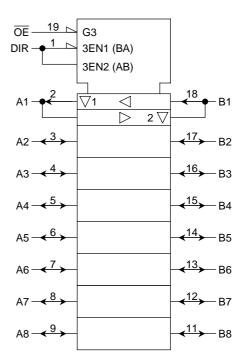
Weight

SOP20-P-300-1.27: 0.22 g (typ.) SOL20-P-300-1.27: 0.46 g (typ.) TSSOP20-P-0044-0.65: 0.08 g (typ.)

### Pin Assignment (top view)



### **IEC Logic Symbol**



### **Truth Table**

Inputs		Outputs	Function		
ŌE	DIR	Odipuis	A-Bus	B-Bus	
L	L	A = B	Output	Input	
L	Н	B=A	Input	Output	
Н	Х	Z	Z		

X: Don't care

Z: High impedance



#### **Maximum Ratings**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage (DIR, $\overline{\text{OE}}$ )	V <sub>IN</sub>	-0.5 to 7.0	V
		-0.5 to 7.0 (Note 1)	
DC bus I/O voltage	V <sub>I/O</sub>	$-0.5$ to $V_{CC} + 0.5$	V
		(Note 2)	
Input diode current	I <sub>IK</sub>	-50	mA
Output diode current	lok	±50 (Note 3)	mA
DC output current	lout	±50	mA
Power dissipation	P <sub>D</sub>	180	mW
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA
Storage temperature	T <sub>stg</sub>	-65 to 150	

Note 1: Output in OFF state

Note 2: High or low state. I<sub>OUT</sub> absolute maximum rating must be observed.

Note 3:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 

### **Recommended Operating Conditions**

Characteristics	Symbol Rating		Unit	
Power supply voltage	V	2.0 to 3.6	V	
Fower supply voltage	V <sub>CC</sub>	1.5 to 3.6 (Note 4)		
Input voltage (DIR, $\overline{\text{OE}}$ )	V <sub>IN</sub>	0 to 5.5	V	
Bus I/O voltage	V <sub>I/O</sub>	0 to 5.5 (Note 5)	V	
Bus I/O Voltage	V 1/O	0 to V <sub>CC</sub> (Note 6)	V	
Output current	la/la.	±24 (Note 7)	mA	
Output current	I <sub>OH</sub> /I <sub>OL</sub>	±12 (Note 8)	mA	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 9)	ns/V	

Note 4: Data retention only

Note 5: Output in OFF state

Note 6: High or low state

Note 7:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 8:  $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$ 

Note 9:  $V_{IN} = 0.8 \text{ to } 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$ 



#### **Electrical Characteristics**

# DC Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

Characteristics		Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
land to the sec	H-level	V <sub>IH</sub>	-	_	2.7 to 3.6	2.0	_	
Input voltage	L-level	V <sub>IL</sub>	-	_	2.7 to 3.6	_	0.8	V
				$I_{OH} = -100 \mu A$	2.7 to 3.6	V <sub>CC</sub> - 0.2	_	
	H-level	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> = -12 mA	2.7	2.2	_	
				$I_{OH} = -18 \text{ mA}$	3.0	2.4		
Output voltage				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	V
			$I_{OL} = 100 \mu A$	2.7 to 3.6	_	0.2		
	L-level	V/-	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 12 mA	2.7	_	0.4	
	L-ievei	L-level V <sub>OL</sub>		I <sub>OL</sub> = 16 mA	3.0	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V	•	2.7 to 3.6	_	±5.0	μΑ
3-state output OFF state current		loz	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = 0 \text{ to } 5.5 \text{ V}$		2.7 to 3.6	_	±5.0	μА
Power-off leakage current		l <sub>OFF</sub>	V <sub>IN</sub> /V <sub>OUT</sub> = 5.5 V		0	_	10.0	μΑ
Quiescent supply current		loo	V <sub>IN</sub> = V <sub>CC</sub> or GND		2.7 to 3.6	_	10.0	
Quiescent supply current		Icc	V <sub>IN</sub> /V <sub>OUT</sub> = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μΑ
Increase in I <sub>CC</sub> per input		Δl <sub>CC</sub>	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6	_	500	

# AC Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	t <sub>pLH</sub>	Figure 1 Figure 2	2.7	_	8.0	ns
Tropagation delay time	t <sub>pHL</sub>	Figure 1, Figure 2		1.5	7.0	115
Output enable time	$t_{pZL}$	Figure 1, Figure 3	2.7		9.5	- ns
Output enable time	t <sub>pZH</sub>		$3.3 \pm 0.3$	1.5	8.5	
Output disable time	t <sub>pLZ</sub>	Figure 1, Figure 3	2.7	_	8.5	ns
Output disable time	t <sub>pHZ</sub>	rigure 1, rigure 3	$3.3 \pm 0.3$	1.5	7.5	115
Output to output skew	t <sub>osLH</sub>	(Note 10)	2.7	_	_	nc
Output to output skew	t <sub>osHL</sub>	(Note 10)	$3.3 \pm 0.3$	_	1.0	ns

Note 10: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$ 

#### **Dynamic Switching Characteristics**

(Ta = 25°C, input:  $t_r = t_f = 2.5 \text{ ns}$ ,  $C_L = 50 \text{ pF}$ ,  $R_L = 500 \Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic V <sub>OL</sub>	V <sub>OLP</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	V <sub>IH</sub> = 3.3 V, V <sub>IL</sub> = 0 V	3.3	0.8	V

#### **Capacitive Characteristics (Ta = 25°C)**

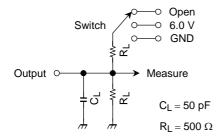
Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	DIR, OE		3.3	7	pF
Bus input capacitance	C <sub>I/O</sub>	An, Bn		3.3	8	pF
Power dissipation capacitance	C <sub>PD</sub>	$f_{IN} = 10 \text{ MHz}$	Note 11)	3.3	25	pF

Note 11: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per bit)}$ 

#### **AC Test Circuit**



Parameter	Switch
t <sub>pLH</sub> , t <sub>pHL</sub>	Open
t <sub>pLZ</sub> , t <sub>pZL</sub>	6.0 V
t <sub>pHZ</sub> , t <sub>pZH</sub>	GND

Figure 1

#### **AC Waveform**

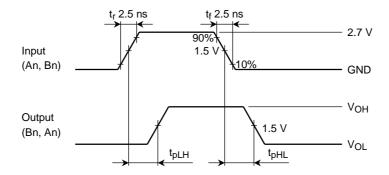
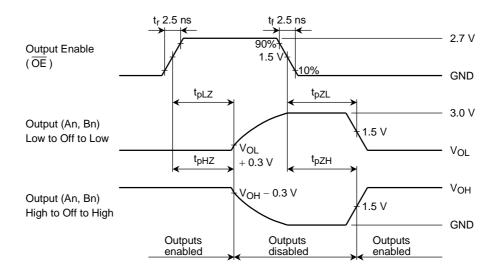


Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>

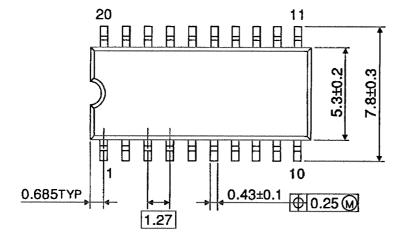


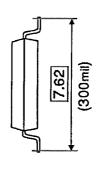
 $\textbf{Figure 3} \quad t_{\text{pLZ}}, \, t_{\text{pHZ}}, \, t_{\text{pZL}}, \, t_{\text{pZH}}$ 

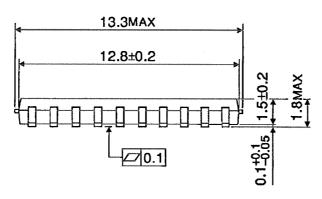
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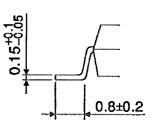
### **Package Dimensions**

SOP20-P-300-1.27 Unit: mm





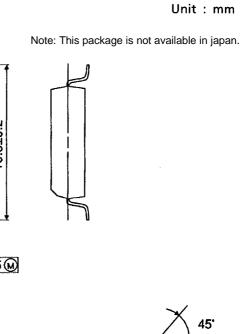


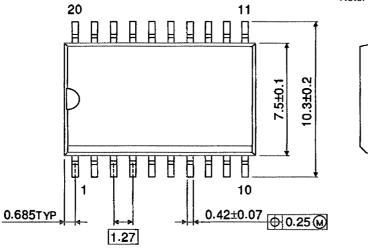


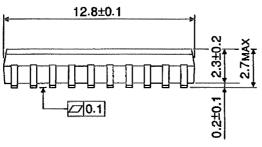
Weight: 0.22 g (typ.)

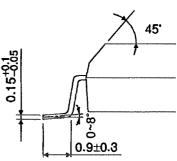
### **Package Dimensions**

SOL20-P-300-1.27







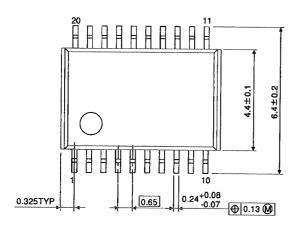


Weight: 0.46 g (typ.)

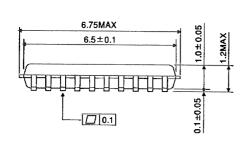
Unit: mm

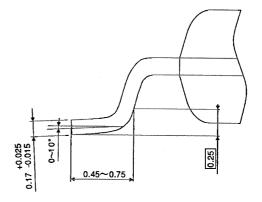
### **Package Dimensions**

TSSOP20-P-0044-0.65









Weight: 0.08 g (typ.)

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10

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