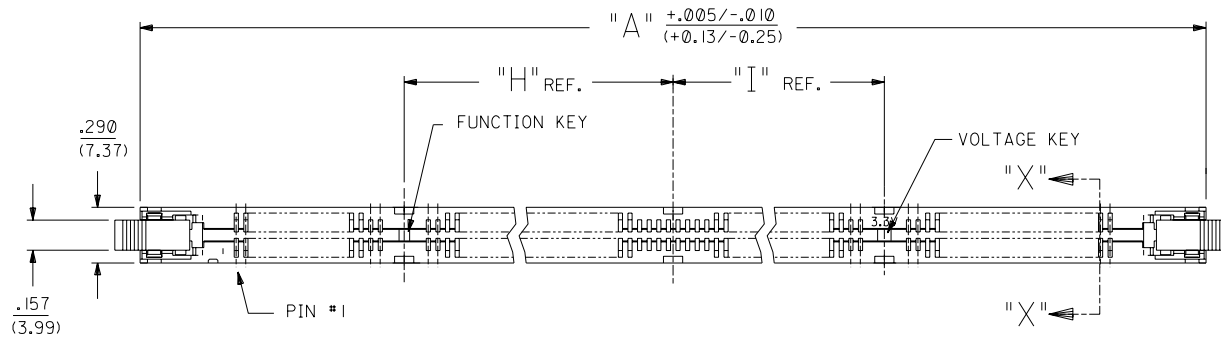




- NOTES:
- CARD SLOT ACCEPTS $.050 \pm .004$ (1.27 $\pm$ 0.10) MODULE THICKNESS. (MEASURED OVER P.C. PADS).
 - ALL PEGS ARE INTERFERENCE FITS TO PCB UNLESS NOTED ON THE DWG.
 - REFER TO PRODUCT SPEC PS-71243-9999 FOR PERFORMANCE SPECS.
 - PRODUCT IS PACKAGED IN TRAYS.
 - RECOMMENDED MODULE LAYOUT SHALL BE PER SHEET 2.
 - RECOMMENDED PLATING ON MODULE PADS: 30 MICROINCH/(0.76 MICROMETER) MINIMUM HARD GOLD (Au) OVER 79 MICROINCH/(2.0 MICROMETER) MINIMUM NICKEL (Ni).
 - PRODUCT WILL HAVE DATE CODE STAMPED ON SIDE OF HOUSING.

MATERIALS:

HOUSING - GLASS FILLED LIQUID CRYSTAL POLYMER (LCP), UL 94V-0, COLOR: BLACK.
 TERMINAL - PHOSPHOR BRONZE
 LATCHES - GLASS FILLED HIGH TEMPERATURE NYLON, UL 94V-0, COLOR: IVORY.

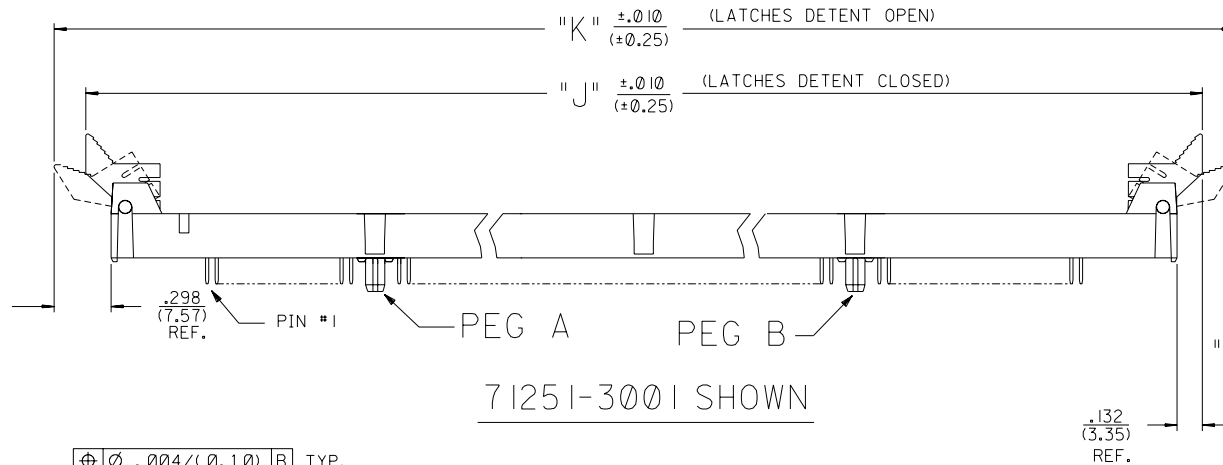
PLATING:

CONTACT AREA: GOLD (Au) FLASH: THICKNESS=2-10 MICROINCH/(0.05-0.25 MICROMETER), OVER PALLADIUM-NICKEL (Pd-Ni): THICKNESS=30 MICROINCH/(0.76 MICROMETER) MINIMUM.
 SOLDER TAILS: TIN (Sn): THICKNESS=150 MICROINCH/(3.81 MICROMETER) MINIMUM.

UNDERPLATE: NICKEL (Ni) OVER ENTIRE CONTACT.

NOTE FOR LEAD FREE CONVERSION:

THE PRIMARY SHIPPING CARTON WILL BE LABELED "COMPLIANT TO ROHS DIRECTIVE 2002/95/EC AND ELV ANNEX II OF DIRECTIVE 2000/53/EC". CARTONS WITHOUT THIS LABEL MAY CONTAIN PRODUCT WITH LEAD.

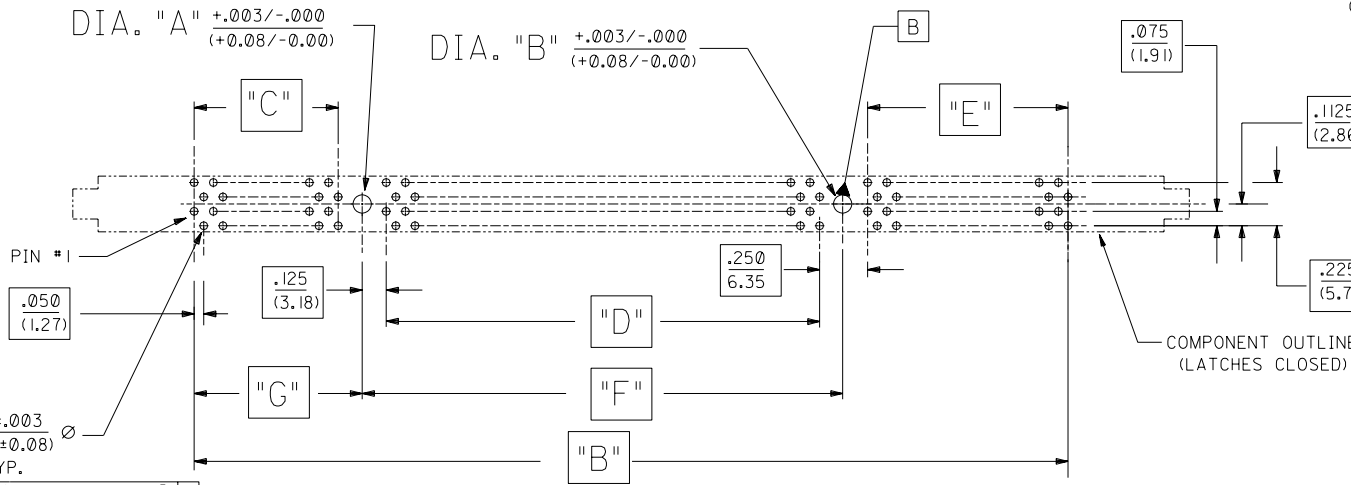


71251-3001 SHOWN

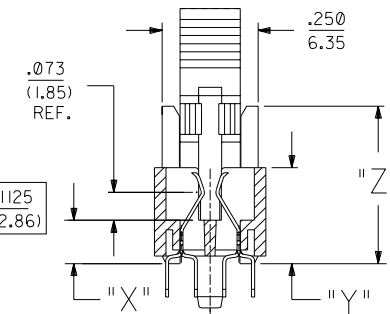
$\phi .004 / (0.10)$ B TYP.

DIA. "A" $\pm 0.03 / -0.00$ (+0.08/-0.00)

DIA. "B" $\pm 0.03 / -0.00$ (+0.08/-0.00)



RECOMMENDED
P.C. BOARD HOLE PATTERN
(CONNECTOR SIDE)



SECTION "X-X"

SCALE 4:1

F1	ADD LEAD FREE NOTE ECN# UCP2004-1732 DMORGAN 04/03/09
F	CHG Sn-Pb THICKNESS ECN# UDT1999-0454 DMORGAN 99/08/09
E	ADD P/N -3201 ECN #UDT1999-0582 DMORGAN 99/01/25
D	ADD P/N -3101 ECN #UDT1999-0385 DMORGAN 98/11/25
C	REVISED PER ECR# U61133 96-9-25 LAURX
B	REVISED PER ECR# U51288 8-4-95 JCL
A	RMVD X, REVISED PER ECR# U50617 12-27-94 JCL
2	REVISED PER ECR# U50419 11-9-94 JCL
1	**X* RELEASED PER ECR# U40711 10-20-94 JCL

DIMENSIONS SHOWN (METRIC) INCH	
UNLESS OTHERWISE SPECIFIED TOLERANCES: ANGULAR $\pm 1/2^\circ$	
INCH	METRIC
3 PLACE $\pm .005$	---
2 PLACE $\pm .01$	± 0.13
1 PLACE ---	± 0.25
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	
DRWG. BY JCL	CHK'D BY DCB
APPRO. BY	SCALE 2 : 1

REVISIONS	
REV.	DESCRIPTION
3	D
2	F
1	F1
MFG. SH. REV. LTR.	
REVISE ONLY ON CAD SYSTEM	
TITLE .050/(1.27) PITCH MULTI-KEY DIMM ASSEMBLY FOR SDRAM 10.0mm LATCH SALES DWG	
MOLEX INCORPORATED SHEET NO. 1 OF 3 DATE 10/19/94	
PART NO. SEE CHART SDA-71251-3***	
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION	

I3		I2		I1		I0		9		8		7		6		71251		4		3		2		1	
ITEM NUMBER 71251-3...	CKT SIZE	DESCRIPTION	FUNCTION KEY	VOLTAGE KEY	DIA. 'A'	DIA. 'B'	DIM. 'A'	DIM. 'B'	DIM. 'C'	DIM. 'D'	DIM. 'E'	DIM. 'F'	DIM. 'G'	DIM. 'H'	DIM. 'I'	DIM. 'J'	DIM. 'K'	DIM. 'L'	DIM. 'P'	DIM. 'S'	DIM. 'T'	DIM. 'W'	DIM. 'X'	DIM. 'Y'	DIM. 'Z'
-3001	200	3.3 VOLT LV TTL	CENTERED	CENTERED	.093 (2.36)	.093 (2.36)	6.350 (161.29)	5.350 (135.89)	.750 (19.05)	3.050 (77.47)	1.050 (26.67)	3.300 (83.82)	.875 (22.22)	1.800 (45.72)	1.500 (38.10)	6.614 (168.00)	6.946 (176.43)	6.050 (153.67)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.664 (16.87)	.113 (2.87)	.250 (6.35)	.410 (10.41)
-3002	200	3.3 VOLT SDRAM	OFFSET LEFT	CENTERED	.093 (2.36)	.093 (2.36)	6.350 (161.29)	5.350 (135.89)	.750 (19.05)	3.050 (77.47)	1.050 (26.67)	3.300 (83.82)	.875 (22.22)	1.839 (46.72)	1.500 (38.10)	6.614 (168.00)	6.946 (176.43)	6.050 (153.67)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.664 (16.87)	.113 (2.87)	.250 (6.35)	.410 (10.41)

J

J

I

I

H

H

G

G

F

F

E

E

D

D

C

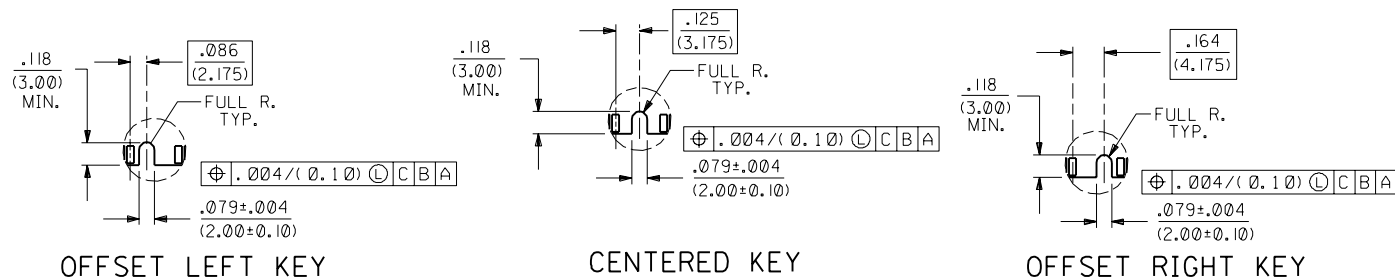
C

B

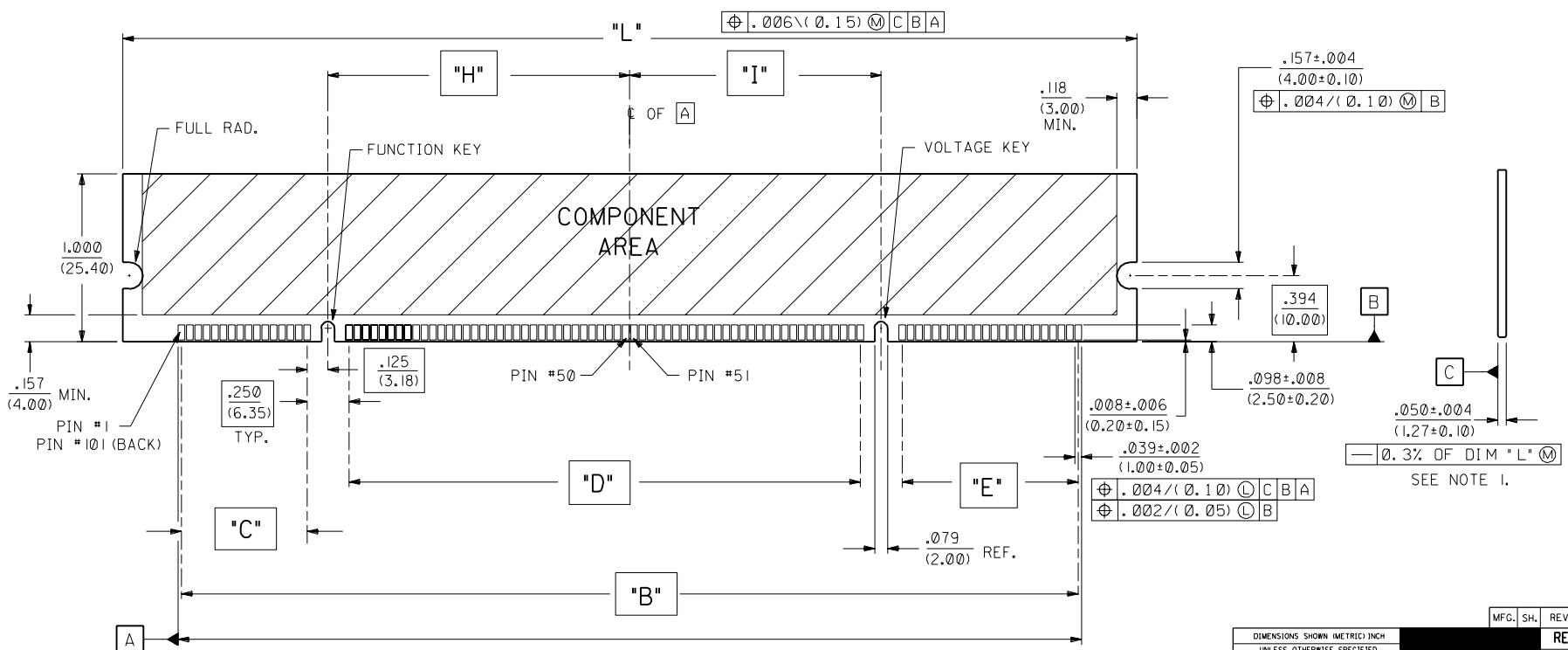
B

A

A



- NOTES:
1. STRAIGHTNESS OF MODULE APPLIES TO THE AREA FROM THE BOTTOM OF THE CARD UP .157/(4.00).
 2. IF TIE BARS ARE ATTACHED TO PADS, THE TIE BAR SHOULD BE ON AN INTERNAL LAYER, SO THAT THE REMNANT CANNOT CAUSE DAMAGE TO THE CONTACTS.



F	SEE SHEET I
E	SEE SHEET I
C	SEE SHEET I
B	SEE SHEET I
A	SEE SHEET I
2	SEE SHEET I
1	SEE SHEET I

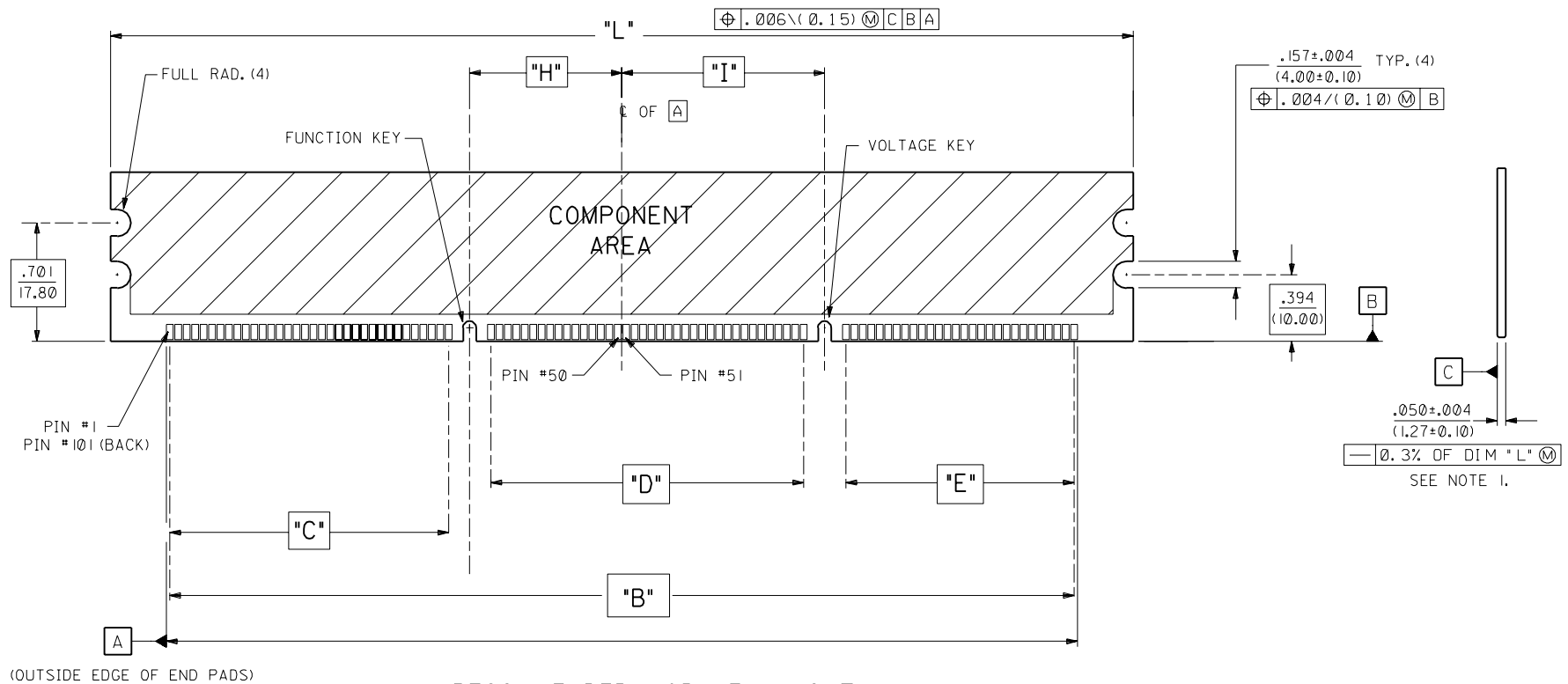
DIMENSIONS SHOWN (METRIC) INCH UNLESS OTHERWISE SPECIFIED TOLERANCES ANGULAR ± 1/2°		TITLE 0.050/(1.27) PITCH MULTI-KEY DIMM ASSEMBLY FOR SDRAM 10.0mm LATCH SALES DWG	
5 PLACE ± .005	INCH	2 PLACE ± .01	METRIC
2 PLACE ± .01	INCH	1 PLACE ± .05	METRIC
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		PART NO. MOLX INCORPORATED 1151EJLL 60532	
DRW'G. BY JCL	CHK'D. BY DCB	FILE NAME S725132	DATE 10/19/94
APP'D. BY	SCALE 2: 1	DIV. CP	
REVISE ONLY ON CAD SYSTEM		SHEET NO. 2	
SEE CHART		SDA-71251-3...	

I3	I2	I1	I0	9	8	7	6	5	4	3	2	I
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I3		I2		I1		I0		9		8		7		6		71251			4		3		2		1	
ITEM NUMBER 71251-3***	CKT SIZE	DESCRIPTION	FUNCTION KEY	VOLTAGE KEY	DIA. 'A'	DIA. 'B'	DIM. 'A'	DIM. 'B'	DIM. 'C'	DIM. 'D'	DIM. 'E'	DIM. 'F'	DIM. 'G'	DIM. 'H'	DIM. 'I'	DIM. 'J'	DIM. 'K'	DIM. 'L'	DIM. 'P'	DIM. 'S'	DIM. 'T'	DIM. 'W'	DIM. 'X'	DIM. 'Y'	DIM. 'Z'	
-3101	200	2.5 VOLT DDR	CENTERED	CENTERED	.093 (2.36)	.093 (2.36)	6.350 (161.29)	5.350 (135.89)	1.650 (41.91)	1.850 (46.99)	1.350 (34.29)	2.100 (53.34)	1.775 (45.09)	.900 (22.86)	1.200 (30.48)	6.614 (168.00)	6.946 (176.43)	6.050 (153.67)	.155 (3.94)	.020 (0.51)	.127 (3.23)	.664 (16.87)	.113 (2.87)	.250 (6.35)	.410 (10.41)	

NOTES:

1. STRAIGHTNESS OF MODULE APPLIES TO THE AREA FROM THE BOTTOM OF THE CARD UP .157/(4.00).
2. IF TIE BARS ARE ATTACHED TO PADS, THE TIE BAR SHOULD BE ON AN INTERNAL LAYER, SO THAT THE REMNANT CANNOT CAUSE DAMAGE TO THE CONTACTS.
3. SEE SHEETS 1 & 2 FOR DIMENSIONS NOT SHOWN.



RECOMMENDED MODULE LAYOUT
(MODULE FOR 71251-3101 SHOWN)

D	SEE SHEET 1
C	SEE SHEET 1
B	SEE SHEET 1
A	SEE SHEET 1
2	SEE SHEET 1
1	SEE SHEET 1

DIMENSIONS SHOWN (METRIC) INCH		UNLESS OTHERWISE SPECIFIED TOLERANCES ANGULAR ± 1/2°		TITLE	
INCH		METRIC		.050/(1.27) PITCH MULTI-KEY DIMM ASSEMBLY FOR SDRAM 10.0mm LATCH SALES DWG	
3 PLACE	± .005	---	---	MFG. SH. REV. LTR. REVISIONS	
2 PLACE	± .01	± 0.13	---	PART NO. 71251-3101	
1 PLACE	---	± 0.25	---	DATE 10/19/94	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				MOLEX INCORPORATED 135LEJLL 60532 U.S.A.	
DRWG. BY JCL	CHK'D BY DCB	SCALE 2: 1		SEE CHART SDA-71251-3***	
APP'D. BY	FILE NAME S725132.DWG	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION		DIV. CP	