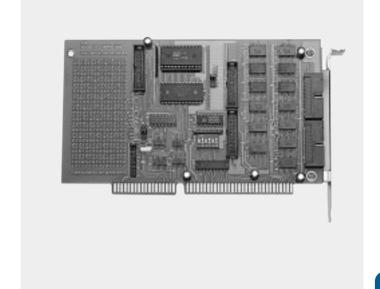
ACL-7120

32-CH DI & 32-CH DO Timer / Counter Card

Features

- 32 TTL digital input channels
- 32 TTL digital output channels
- High output drive and low input loading
- 4 independent programmable 16-bit down counters
- One cascaded 32-bit timer
- Crystal based frequency source
- Clock frequency: 2MHz, 1MHz, 500kHz, or 250kHz, jumper selectable
- Frequency divider: divided by 1, 10, 100, or user adjustable
- Breadboard area for customized circuit
- Internal/external interrupt generation
- Registers compatible to PCL-720



Introduction

ACL-7120 is a digital I/O and counter card for PC/AT and compatibles.

It has almost everything on-board, including TTL compatible 32 digital inputs, 32 digital outputs, and 3 or 6 channels with a 16 bit counter/timer. The breadboard area is provided for user circuit designs. All these features make the ACL-7120 a powerful "Swiss Army Knife" of Digital I/O cards.

Specifications

Digital Output

- Number of channels: 32 channels
- Signal level: TTL compatible
- Logic output level:
 - Low: -0.5V min to +0.8 max @ 24mA (sink)
 - High: 2.4V min to 5.0V max @ 0.8mA (source)
- Output status can be readback

Digital Input

- Number of channels: 32 channels
- Signal level: TTL compatible
- Logic low: Min. -0.5V Max. 0.8V
- Logic high: Min. 2.0V Max 5.0V

Counter/Timer

- Chip: 8253 or equivalent
- Frequency of base clock: 4MHz
- Number of counters: 3 (3 more counters on ACL-7120/6)
- Counter usage
- Counter 0 ~ Counter 2 are flexible for users
- Counter 3 is used for 16-bit event counting interrupt source
- Counter 4~5 are cascaded together for 32-bit timer pacer generation.

Bread Board Area

- Pitch: standard 100 mils
- Two strips of holes for power signals (+5V,GND)

General Specifications

- I/O connectors:
 - Five 20-pin header connectors
- Operating temperature: 0° ~ 60°C
- Storage temperature:-20° ~ 80°C
- Humidity: 5~95%,non-condensing
- Power rquirement:
 - +5V @ 420mA typical
 500mA maximum
- Dimension: 194 mm x 114 mm

Termination Boards

- ACLD-9185
- ACLD-9182
- ACLD-9178
- ACLD-9188
- DIN-20P

Ordering Information

ACL-7120/3

32-CH Digital I/O with 3-CH Timer / Counter Card

ACL-7120/6

32-CH Digital I/O with 6-CH Timer / Counter Card